

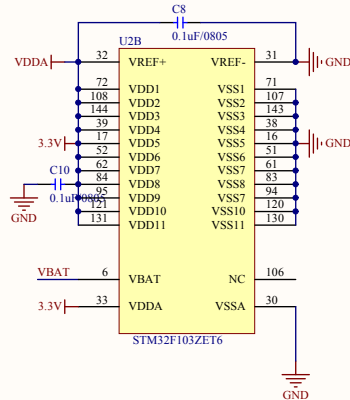
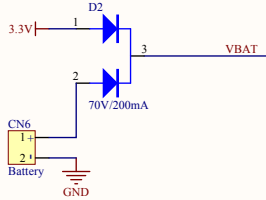
U2A

PA0	34	PA0/WKUP/USART2_CTS/ADC123_IN0/TIM2_CH1_ETR/TIM5_CH1/TIM8_ETR
PA1	35	PA1/USART2_RTS/ADC123_IN1/TIM5_CH2/TIM2_CH2
PA2	36	PA2/USART2_TX/TIM5_CH3/ADC123_IN2/TIM2_CH3
PA3	37	PA3/USART2_RX/TIM5_CH4/ADC123_IN3/TIM2_CH4
PA4	40	PA4/SPI1_NSS/USART2_CK/DAC_OUT1/ADC12_IN4
PA5	41	PA5/SPI1_SCK/DAC_OUT2/ADC12_IN5
PA6	42	PA6/SPI1_MISO/TIM8_BKIN/ADC12_IN6/TIM3_CH1
PA7	43	PA7/SPI1_MOSI/TIM8_CH1IN/ADC12_IN7/TIM3_CH2
PA8	100	PA8/USART1_CK/TIM1_CH1/MCO
PA9	USART1_TXD	101 PA9/USART1_TX/TIM1_CH2
PA10	USART1_RXD	102 PA10/USART1_RX/TIM1_CH3
PA11	103	PA11/USART1_CTS/USBDP/CAN_RX/TIM1_CH4
PA12	104	PA12/USART1_RTS/USBDP/CAN_TX/TIM1_ETR
PA13	JTMS	105 PA13/JTMS-SWDIO
PA14	JTCK	109 PA14/JTCK-SWCLK
PA15	JTDI	110 PA15/JTDI/TIM2_CH1/TIM2_ETR/SPI1_NSS/SPI3_NSS/I2S3_WS

PB0	46	PB0/ADC12_IN8/TIM3_CH3/TIM8_CH2N
PB1	47	PB1/ADC12_IN9/TIM3_CH4/TIM8_CH3N
PB2	BOOT1	48
PB3	JTDO	133 PB3/JTDO/TRACESWO/TIM2_CH2/SPI1_SCK/SPI3_SCK/I2S3_CK
PB4	JTRST	134 PB4/NJTRST/TIM3_CH1/SPI1_MISO/SPI3_MISO
PB5	135	PB5/TIM3_CH2/SPI1_MOSI/I2C1_SMBA/SPI3_MOSI/I2S3_SD
PB6	136	PB6/USART1_TX/I2C1_SCL/TIM4_CH1
PB7	137	PB7/USART1_RX/I2C1_SDA/FSMC_NADV/TIM4_CH2
PB8	139	PB8/I2C1_SCL/CAN_RX/TIM4_CH3/SDIO_D4
PB9	I2C1	140 PB9/I2C1_SDA/CAN_TX/TIM4_CH4/SDIO_D5
PB10	USART3_TXD	69 PB10/I2C2_SCL/USART3_TX
PB11	USART3_RXD	70 PB11/I2C2_SDA/USART3_RX
PB12	71	PB12/SPI2_NSS/I2S2_WS/I2C2_SMBA/USART3_CK/TIM1_BKIN
PB13	74	PB13/SPI2_SCK/I2S2_CK/USART3_CTS/TIM1_CH1N
PB14	75	PB14/SPI2_MISO/TIM1_CH2N/USART3_RTS
PB15	76	PB15/SPI2_MOSI/I2S2_SD/TIM1_CH3N

PC0	26	PC0/ADC123_IN10
PC1	27	PC1/ADC123_IN11
PC2	28	PC2/ADC123_IN12
PC3	29	PC3/ADC123_IN13
PC4	44	PC4/ADC12_IN14
PC5	45	PC5/ADC12_IN15
PC6	96	PC6/TIM3_CH1/TIM8_CH1/I2S2_MCK/SDIO_D6
PC7	97	PC7/TIM3_CH2/TIM8_CH2/I2S3_MCK/SDIO_D7
PC8	98	PC8/TIM3_CH3/TIM8_CH3/SDIO_D8
PC9	99	PC9/TIM3_CH4/TIM8_CH4/SDIO_D1
PC10	111	PC10/USART4_TX/SDIO_D2/USART3_TX
PC11	112	PC11/USART4_RX/SDIO_D3/USART3_RX
PC12	113	PC12/USART5_TX/SDIO_CK/USART3_CK
PC13	7	PC13/TAMPER-RTC

STM32F103ZET6

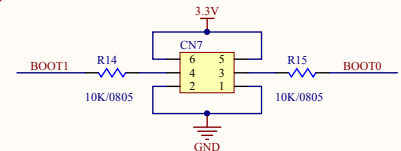
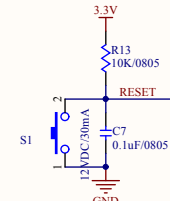
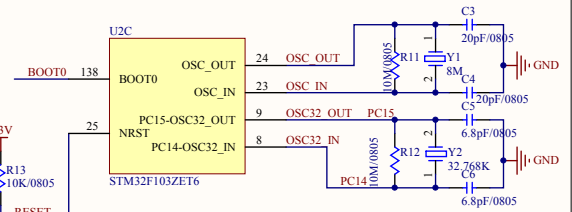
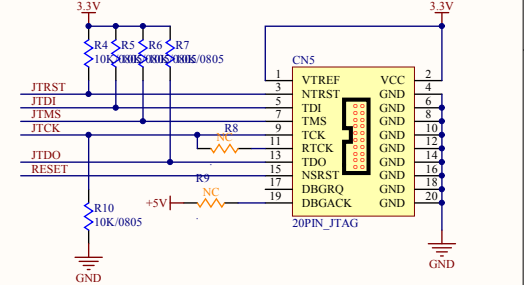


PD0/CAN_RX/FMC_D2	114	PD0
PD1/CAN_TX/FMC_D3	115	PD1
PD2/TIM3_ETR/USART5_RX/SDIO_CMD	116	PD2
PD3/FSMC_CLK/USART2_CTS	117	PD3
PD4/USART2_RTS/FMC_NOE	118	PD4
PD5/USART2_TX/FMC_NWE	119	PD5
PD6/USART2_RX/FSMC_NWAIT	122	PD6
PD7/USART2_CK/FMC_NE1/FMC_NCE2	123	PD7
PD8/USART3_TX/FMC_D13	77	PD8
PD9/USART3_RX/FMC_D14	78	PD9
PD10/USART3_CK/FMC_D15/LCD_B3	79	PD10
PD11/USART3_CTS/FMC_A16	80	PD11
PD12/TIM4_CH1/USART3_RTS/FMC_A17	81	PD12
PD13/TIM4_CH2/FMC_A18	82	PD13
PD14/TIM4_CH3/FMC_D0	85	PD14
PD15/TIM4_CH4/FMC_D1	86	PD15

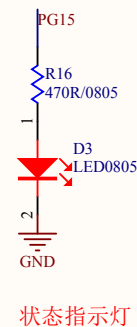
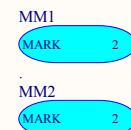
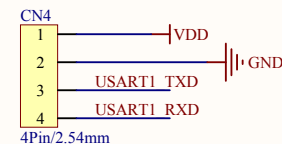
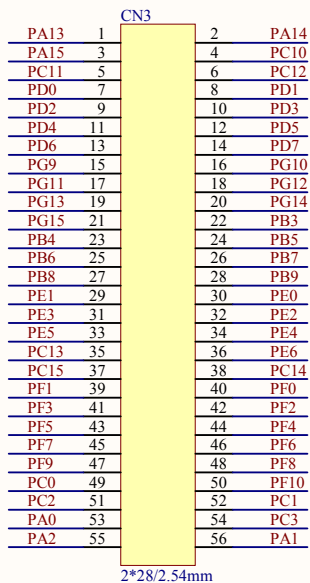
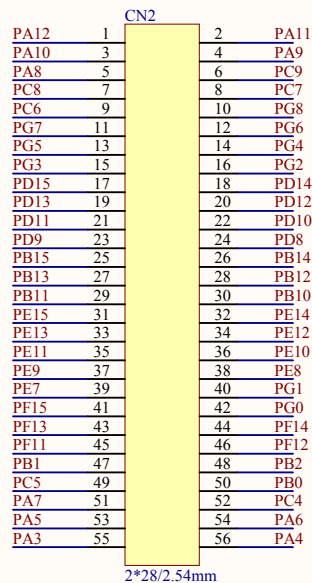
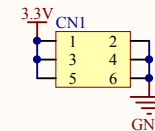
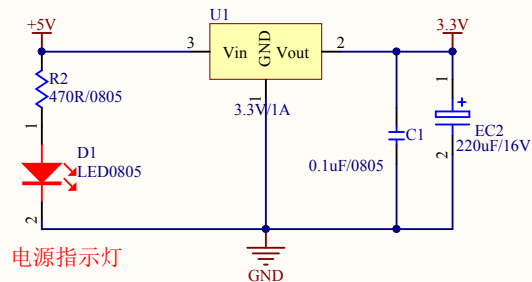
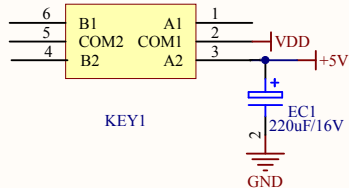
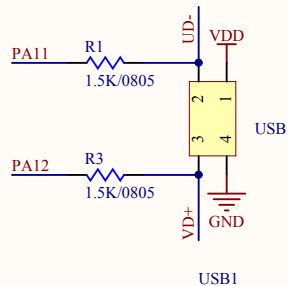
PE0/TIM4_ETR/FSMC_NBL0	141	PE0
PE1/FSMC_NBL1	142	PE1
PE2/TRACCLK/FMC_A23	1	PE2
PE3/TRACED0/FMC_A19	2	PE3
PE4/TRACED1/FMC_A20	3	PE4
PE5/TRACED2/FMC_A21	4	PE5
PE6/TRACED3/FMC_A22	5	PE6
PE7/FSMC_D4	58	PE7
PE8/FSMC_D5	59	PE8
PE9/FSMC_D6	60	PE9
PE10/FSMC_D7	63	PE10
PE11/FSMC_D8	64	PE11
PE12/FSMC_D9	65	PE12
PE13/FSMC_D10	66	PE13
PE14/FSMC_D11	67	PE14
PE15/FSMC_D12	68	PE15

PF0/FMC_A0	10	PF0
PF1/FMC_A1	11	PF1
PF2/FMC_A2	12	PF2
PF3/FMC_A3	13	PF3
PF4/FMC_A4	14	PF4
PF5/FMC_A5	15	PF5
PF6/FMC_A6	18	PF6
PF7/FMC_A7	19	PF7
PF8/FMC_A8	20	PF8
PF9/FMC_A9	21	PF9
PF10/FMC_A10	22	PF10
PF11/FMC_A11	49	PF11
PF12/FMC_A12	50	PF12
PF13/FMC_A13	51	PF13
PF14/FMC_A14	54	PF14
PF15/FMC_A15	55	PF15

PG0/FMC_A16	56	PG0
PG1/FMC_A17	57	PG1
PG2/FMC_A18	87	PG2
PG3/FMC_A19	88	PG3
PG4/FMC_A20	89	PG4
PG5/FMC_A21	90	PG5
PG6/FMC_A22	91	PG6
PG7/FMC_A23	92	PG7
PG8/FMC_A24	93	PG8
PG9/FMC_A25	124	PG9
PG10/FMC_A26	125	PG10
PG11/FMC_A27	126	PG11
PG12/FMC_A28	127	PG12
PG13/FMC_A29	128	PG13
PG14/FMC_A30	129	PG14
PG15/FMC_A31	132	PG15



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