**深 圳 大 学 实 验 报 告**

**课程名称： 微机原理与接口技术**

**实验项目名称： 计数/定时器8254程序设计实验**

**学院： 电子与信息工程学院**

**专业： 电子信息工程**

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**实验报告提交时间： 2023年2月17日**

**教务部制**

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| **实验目的与要求：**  掌握PC机里定时/计数器8254的基本原理和编程方法，用示波器观察不同方式下的波形。  **一、计数器8254程序设计实验**  将计数器0设置为方式0，计数器初值为N(N≤0FH),用手动逐个输入单脉冲，编程使计数值再屏幕上显示，并同时用逻辑笔观察OUT0电平变化(当输入N+1个脉冲后OUT0变低电平)。  1．图中虚线部分是实验时需要使用实验导线连接。  2．在TPC-486EM集成开发环境下输入程序，编译、连接，生成.exe执行文件。  3．在执行计数器程序后每按一次单脉冲屏幕上的值减1，等减到结果等于1时OUT0输出低电平。  **二、定时器8254程序设计实验**  将计数器0，计数器1分别设置为方式3，计数初值设为1000，用逻辑笔观察OUT1输出电平的变化(频率Hz)。  1．图中虚线部分是实验时需要使用实验导线连接。  2．在TPC-486EM集成开发环境下输入程序，编译、连接，生成.exe执行文件。  3. 在执行定时器程序后，OUT1输出1Hz频率，逻辑笔上灯出现一定时间亮灭。 |
| **内容和步骤：**  **一、计数器8254程序设计实验**   1. **Circuit physical connection between 8086 and 8254A.**   We first observe the structure of 8254. From the following figure, we find the 8254 contains three timers and each of them has three interface which includes CLK, GATE and OUT. In addition, 8254 has totally two address lines (A0 and A1) which point to controller and three timers. Piece choice control signal interface CS, read enable signal interface RD and write enable signal interface WR also belongs to 8254.  For physical connection, we focus on the most important part that is address line connection. In this experiment, we use the 210H, 211H,212H and 213H to respectively connect to controller, timer0, timer1 and timer2. Since the IO address is sequential arrangement, we connect the A0 of the 8254 to the A0 of the 8086 and connect the A1 of the 8254 to the A1 of the 8086. We only use the timer0 to time so GATE0 connects signal pulse, CLK0 connects 1MHz and OUT0 connects logic pen to observe the level of the output signal. The CS of 8254 connects to the IO address 210H-21FH. The WR of 8254 connects to the IOW of 8086. The RD of 8254 connects to the IOR of 8086. Here we finish all the physical connection.  **9a610f4c3af6f8d16a919612c967f1b**   1. **Program the corresponding code.**   The main part of the whole program includes (a) the initialization of 8254, (b) output the current value of timer0 and (c) display the current value.   1. **The initialization of 8254**   We first calculate the control word and send it to the controller. We use the mode2 in this experiment. The control word is 00010100D. Since the address of controller is 213H, we should move the address to DX. The control word move to AL. We use OUT instruction to send the control word to controller. Next, we send the initial value to the timer0. Here we set the initial value to 0FH and move it to AL. The address of timer0 is 210H. We use OUT instruction to send the initial value to timer0.   1. **Output the current value of timer0**   We use the IN instruction to output the current value and the current value is saved in AL.   1. **Display the current value**   We write a subprogram to display each character. It is worth to say that if the output character is A-F, we should add 37H and if the output character is 0-9, we should add 30H. We use the function 2 of the INT 21H to display the character in the screen. And we use the function 0DH of the INT 21H to conduct enter in convince to observe the current value.  All the program is shown in the following figure.  **捕获**  **捕获2**   1. **Observe the result of the experiment.**   When the output is F-2, the color of logic pen is red which indicates that the level is high as shown in the following two figures.  7bc18273e9c47ec2681334c219f91ad9a5b90bb61180389544fb2b82697975  When the output is 1, the color of logic pen changes to green which indicates that the level is low as shown in the following two figures.  57c16be7364e1704e0f8f193710891e91d72ca9f01de8be39830049ed94b23  When the finish one time period from F to 1, it will automatically begin another time period as shown in the following two figure. At the same time, the level change to high again until time to 1.  805b4a5e0467b435077668f6a5e4ab90cfb38f24214db6b62a6b46101f9820  The above experiment result corresponds to the theory as shown in the following figure that is after writing the control word, the output will become high. The timer starts immediately after the count value is written. During the counting process, the output will always be high level, until the count value is 1, the output will become low level. After a CLK cycle, the output returns to high and the counter begins to count again. Therefore, it can work continuously and output pulses of fixed frequency.  微信截图_20230216233904  The video of the experiment result is attached to the report.    **二、定时器8254程序设计实验**   1. **Circuit physical connection between 8086 and 8254A.**   In this experiment, we use the 210H, 211H,212H and 213H to respectively connect to controller, timer0, timer1 and timer2. Since the IO address is sequential arrangement, we connect the A0 of the 8254 to the A0 of the 8086 and connect the A1 of the 8254 to the A1 of the 8086. Timer0 and timer1 are cascaded and used to time so GATE0 connects to VCC, CLK0 connects to 1MHz square signal, OUT0 connects to CLK1, GATE1 connects to VCC and OUT1 connects logic pen to observe the level of the output signal. The CS of 8254 connects to the IO address 210H-21FH. The WR of 8254 connects to the IOW of 8086. The RD of 8254 connects to the IOR of 8086. Here we finish all the physical connection as shown in the following figure.     1. **Program the corresponding code.**   The main part of the whole program includes the initialization of 8254. We first calculate the control word and send it to the controller. We both use the mode3 for counter0 and counter1 in this experiment. The control word for counter0 is 00110110D and the control word for counter1 is 01110110D. Since the initial value is 1000 (>255), we should use 2 bytes to represent it. Since the address of controller is 213H, we should move the address to DX. The control word move to AL. We use OUT instruction to send the control word to controller. Next, we send the initial value to the counter0 and counter1. Here we set the initial value to 1000H for both counter0 and counter1 and move it to AL. The address of counter0 is 210H and the address of counter1 is 211H. We use OUT instruction to send the initial value to counter0 and counter1. We first send the low bytes and next send the high bytes to counter0 and counter1. The whole program is shown in the following figure.  微信图片_20230217000535   1. **Observe the result of the experiment.**   In theoretical, the OUT1 will output the 1MHZ/(1000×1000)=1Hz square wave. In the experiment, we observe that the logic pen changes the level around every 0.5 second (from high to low and next to high) as shown in the following figure. In other word, OUT1 is a square wave whose period is 1s.  0cfb38f24214db6b62a6b46101f982091d72ca9f01de8be39830049ed94b23  The above experiment result corresponds to the theory as shown in the following figure that is after the control word is written, the output will become high level. When the initial value is written, the count will start and the output will remain high level; When the count reaches half of the initial value, the output becomes low level, until the count is 0, the output becomes high level again, and the count starts again.  微信截图_20230217002450 |
| **实验结论：**  In this experiment, we use 8254 to achieve count and time. For 8254 initialization, we first send the control word to the controller and send the initial value to the corresponding counter. We use IN and OUT instruction to send the control word and initial value to controller or counter. In total, 8254 has 6 modes and each mode has its characteristic. Through this experiment, we verify the mode0 and mode3.  The characteristic of mode0 is after writing the control word, the output will become high. The timer starts immediately after the count value is written. During the counting process, the output will always be high level, until the count value is 1, the output will become low level. After a CLK cycle, the output returns to high and the counter begins to count again. Therefore, it can work continuously and output pulses of fixed frequency.  The characteristic of mode3 is after the control word is written, the output will become high level. When the initial value is written, the count will start and the output will remain high level; When the count reaches half of the initial value, the output becomes low level, until the count is 0, the output becomes high level again, and the count starts again.  微信截图_20230216233904微信截图_20230217002450 |
| **指导教师批阅意见：**    **成绩评定：**  **指导教师签字：**  **年 月 日** |
| **备注：** |

注：1、报告内的项目或内容设置，可根据实际情况加以调整和补充。

2、教师批改学生实验报告时间应在学生提交实验报告时间后10日内。