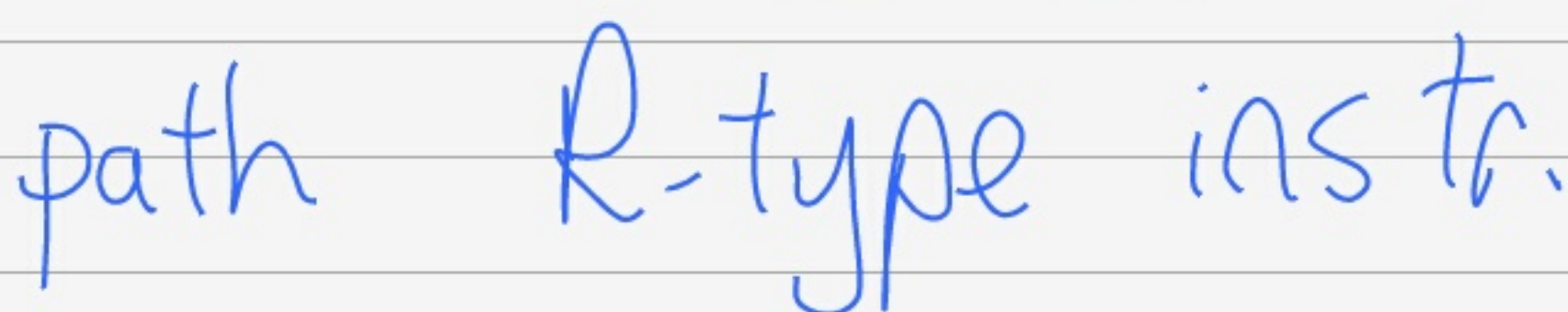
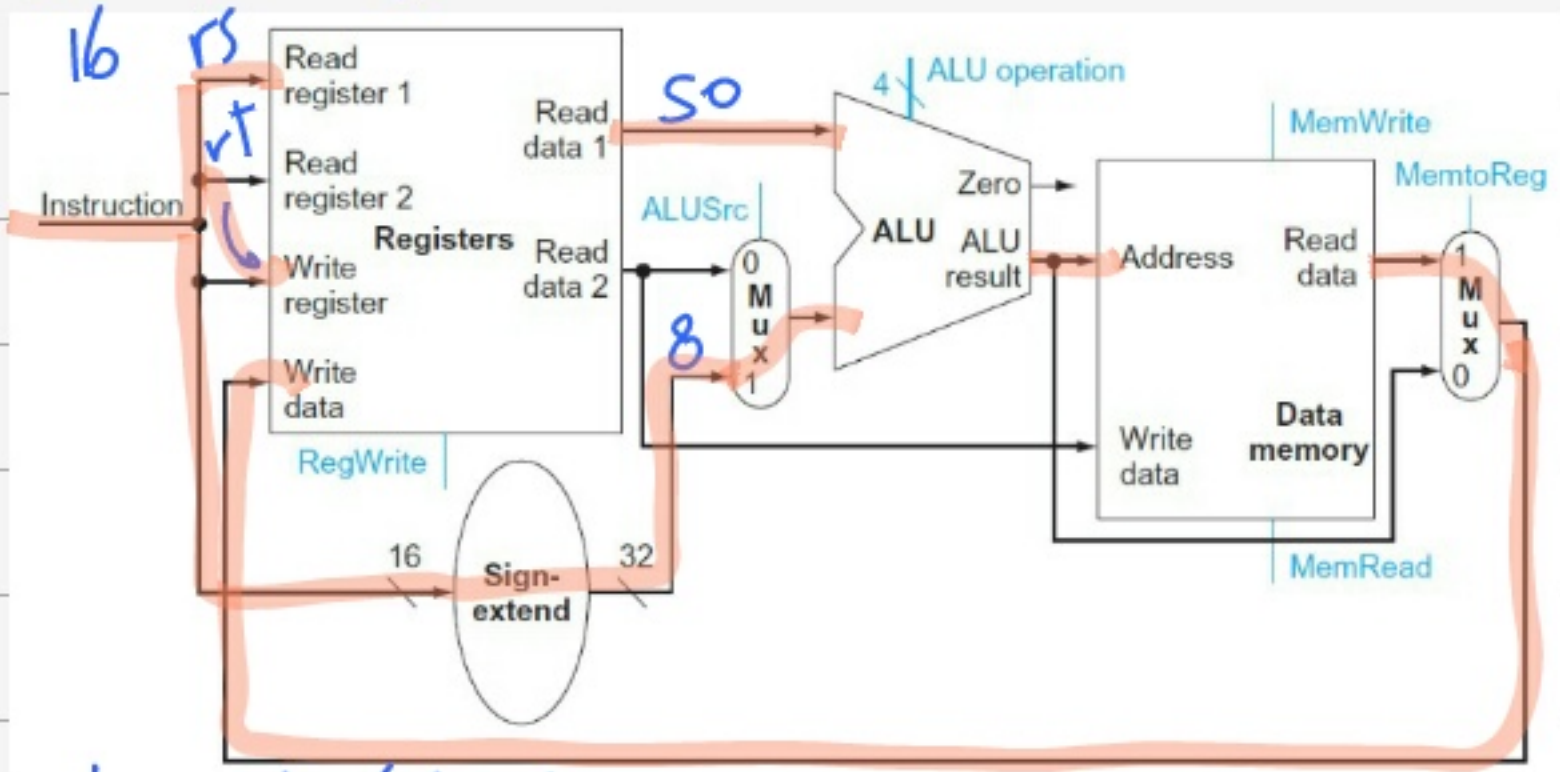
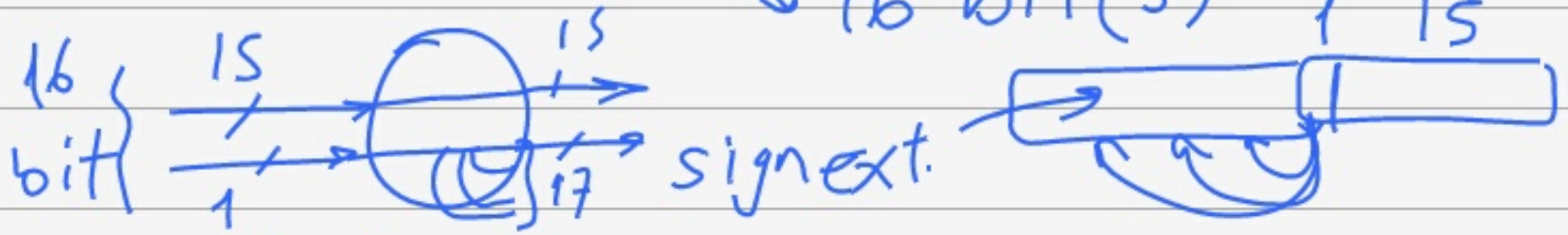


0	17	18	8	0	32
rs		rt	rd	funct	

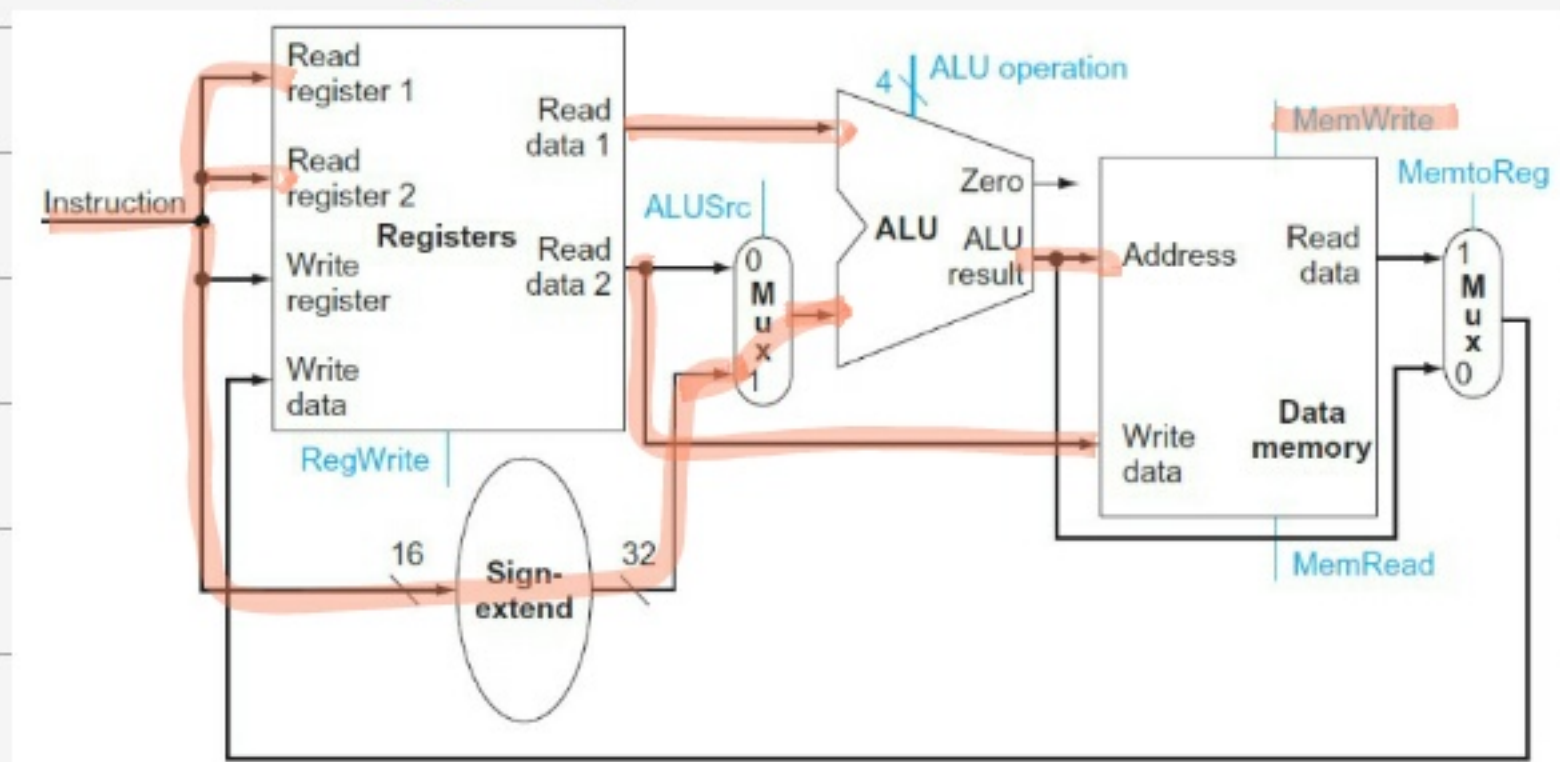


lw \$t0, 8(\$s0) op | rs | rt | addr. | offset
 eff. address \leftarrow $\text{rs} + \text{offset}$
 16 bit(s)

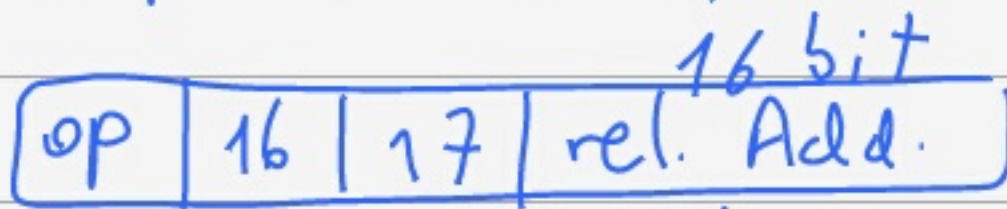


Load (lw)

store (sw)



beq \$s0, \$s1, L1



$$(PC+4) + \boxed{} 00 = \text{target}$$

