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conf\_tb\_asiso\_gen\_calc\_8.vhd

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-- File      : conf_tb_asiso_gen_calc_8.vhd  
-- Description : configuration(s) for the SISO "calc" architecture  
-- Author     : Sabih Gerez, University of Twente  
-- Creation date: September 12, 2018  
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-- $Rev: 1$  
-- $Author: gerezsh$  
-- $Date: Sat Sep 17 16:32:33 CEST 2022$  
-- $Log$  
-----  
  
configuration conf_tb_asiso_gen_calc_8_mix of tb_asiso_gen_top is  
  for top  
    for tg: tb_asiso_gen use entity work.tb_asiso_gen(structure)  
      generic map (word_length => 8);  
    for structure  
      for duv: siso_gen use entity work.siso_gen(calc);  
    end for;  
    for tv: tv_asiso_gen use entity work.tv_asiso_gen(tester)  
      generic map (word_length => 8, -- code does not compile w/o this  
                  half_clock_period => 2.5 ns,  
                  in_file_name => "mix8.in",  
                  out_file_name => "mix8.out");  
    end for;  
  end for;  
end conf_tb_asiso_gen_calc_8_mix;
```