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conf\_tb\_nios\_asiso\_sec\_filter\_post.vhd

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-- File      : conf_tb_nios_asiso_sec_soft.vhd  
-- Description : configuration for nios_asiso "sec_soft" simulation  
-- Author     : Sabih Gerez, University of Twente  
-- Creation date: August 26, 2015  
-----  
-- $Rev: 1$  
-- $Author: gerezsh$  
-- $Date: Thu Sep 29 11:57:46 CEST 2022$  
-- $Log$  
-----  
  
library on_chip_ra;  
  
configuration conf_tb_nios_asiso_sec_filter_post of tb_nios_asiso is  
  for structural  
    for nios_system: nios_asiso use entity work.nios_asiso(rtl);  
      for rtl  
        for gp_custom_0: gp_custom use entity work.gp_custom(flat_filter_none_5);  
          end for;  
        for on_chip_ra: nios_asiso_on_chip_ra  
          use entity on_chip_ra.nios_asiso_on_chip_ra(europa)  
            generic map (INIT_FILE => "my_software/sec_filter.hex");  
          end for;  
        end for;  
      end for;  
    for tvn: tvn_nios_asiso use entity work.tvn_nios_asiso(file_io)  
      generic map (in_file_name => "sec_soft.in",  
                  out_file_name => "sec_soft.out");  
    end for;  
  end for;  
end conf_tb_nios_asiso_sec_filter_post;
```