

PutsCallback::addressCallback

PrintfCallback::addressCallback

MemoryState::getTranslate

```
graph LR; A[PutsCallback::addressCallback] --> C[MemoryState::getTranslate]; B[PrintfCallback::addressCallback] --> C;
```

The diagram illustrates a mapping from two source functions to a single target function. On the left, there are two white rectangular boxes with black borders. The top box contains the text 'PutsCallback::addressCallback' and the bottom box contains 'PrintfCallback::addressCallback'. On the right, there is a single gray rectangular box with a black border containing the text 'MemoryState::getTranslate'. Two blue arrows originate from the right side of the left boxes and point towards the left side of the gray box, indicating that both source functions are mapped to the target function.