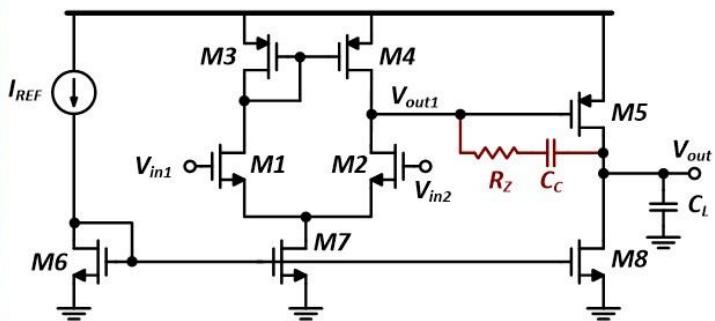


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## Thursday Analog Quiz



Assume all transistors are biased in SI and the square-law is valid. If IREF is halved ( $\times 0.5$ ), the DC gain is multiplied by how much?

$$A_{V_1} \times \frac{2}{\sqrt{2}}$$

$$A_2 = g_m S R_{out_2}$$

$$R_{out} \times 2 \text{ as } r_o = \frac{1}{g_m} \frac{1}{I_D}$$

$$I_S = I_{REF} \Rightarrow I_{REF} \times \frac{1}{2} \Rightarrow I_S \times \frac{1}{2}$$

$$g_m S \times \frac{1}{\sqrt{2}} \text{ and } R_{out} \times 2 \rightarrow 0$$

$$A_V \times \frac{2}{\sqrt{2}} \times \frac{2}{\sqrt{2}} \Rightarrow A_{V_{total}} \times 2$$

$$A_V \times \frac{2}{\sqrt{2}} \times \frac{2}{\sqrt{2}} \Rightarrow A_{V_{total}} \times 2$$

gain of stage 1:  $A_{V_1}$

gain of stage 2:  $A_{V_2}$

$$A_{V_{total}} = A_{V_1} \cdot A_{V_2}$$

$$A_{V_1} = g_m S \cdot R_{out_1}$$

$$\frac{I_{REF}}{2} \Rightarrow I_2 = \frac{I_{REF}}{2} \times \frac{1}{2} \Rightarrow I_2 \times \frac{1}{2}$$

$$g_m = \sqrt{2 I_D \frac{W}{L} V_{DS}} \Rightarrow g_m \times \frac{1}{\sqrt{2}}$$

$$A_V \times \frac{2}{\sqrt{2}}$$