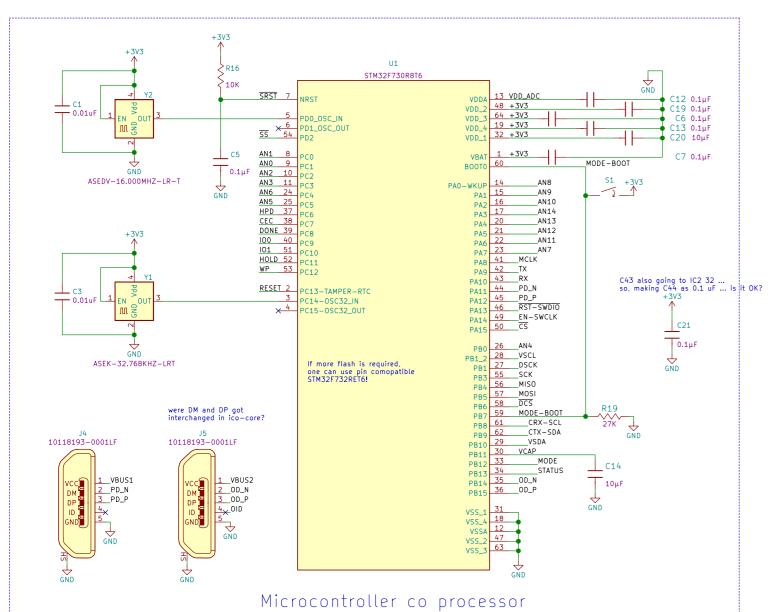
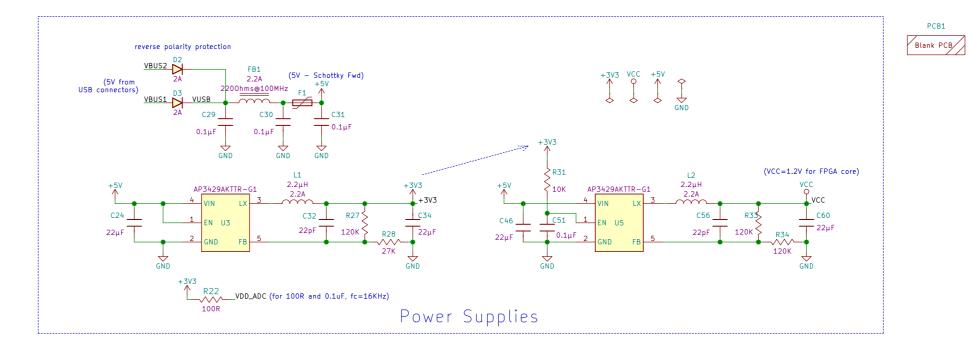


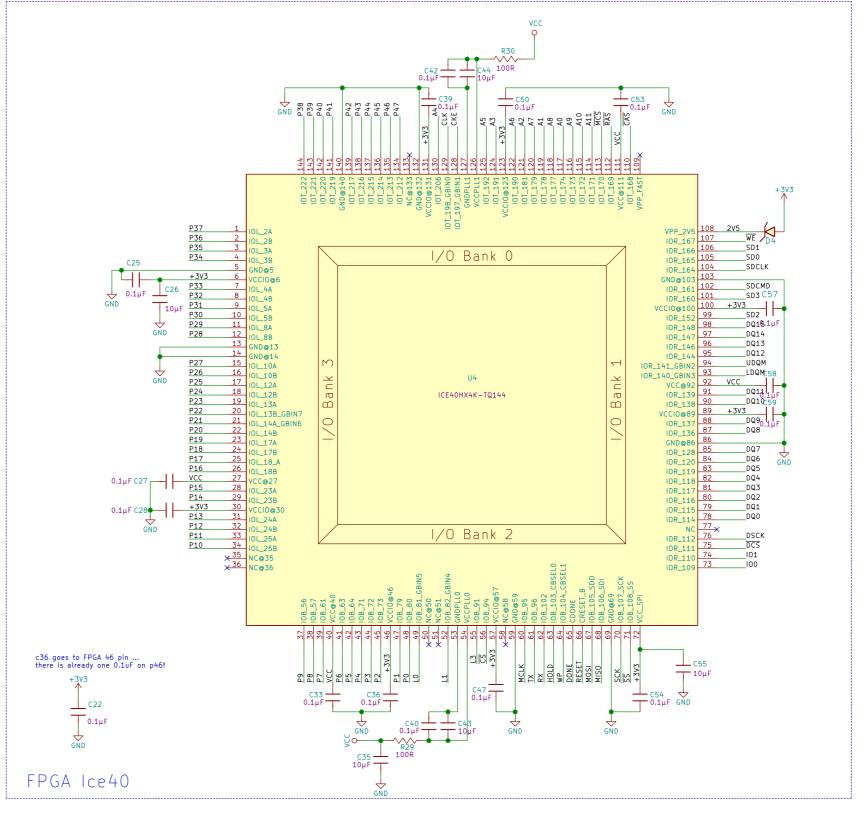
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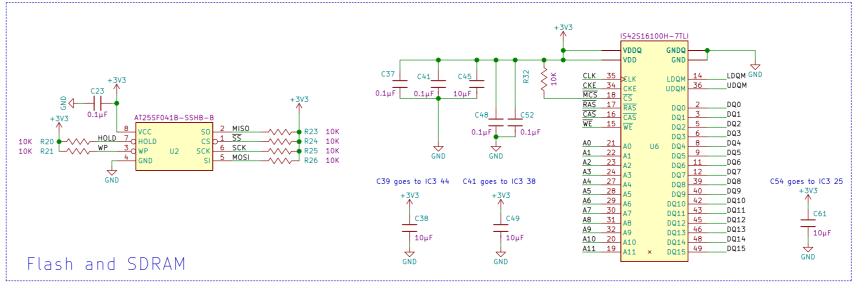
AN15 AN16 AN17

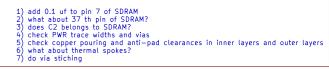
AN18 AN19 AN20 AN21 AN22











Sch No : SCH-XXXXXX Approved by : Reviewed by Created by : S. R. Zinka Sheet: / File: IceCore.sch Title: IceCore Lattice FPGA Board Size: A2 Date: 2020-12-19
KiCad E.D.A. kicad 5.1.5-52549c584ubuntu18.04.3 Rev: 0.0.1