



PCB1

Blank PCB

1) add 0.1 uf to pin 7 of SDRAM
 2) what about 37 th pin of SDRAM?
 3) does C2 belongs to SDRAM?
 4) check PWR trace widths and vias
 5) check copper pouring and anti-pad clearances in inner layers and outer layers
 6) what about thermal spokes?
 7) do via stitching

Sch No : SCH-XXXXXX
 Approved by :
 Reviewed by :
 Created by : S. R. Zinka

Sheet: /
 File: IceCore.sch

Title: IceCore Lattice FPGA Board

Size: A2 Date: 2020-12-19 Rev: 0.0.1
 KiCad E.D.A. kicad 5.1.5-52549c584ubuntu18.04.1 Id: 1/1