

|                                     |        |              |      |
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|                        |                         |          |  |
|------------------------|-------------------------|----------|--|
| Project Size.          | 2.4 inch                |          |  |
| Model No.              | P024H029-IPS            |          |  |
| Samples No.            |                         |          |  |
| Product type.          | 240xRGBx320<br>MCU mode |          |  |
| Signature by customer: |                         |          |  |
| Prepared               | Checked                 | Approved |  |
|                        |                         |          |  |

Shenzhen P&O Technology Co.,Limited

Email: [polcd@polcd.com](mailto:polcd@polcd.com)

Mobile: 86-136 0019 7172

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## 1.0 GENERAL DESCRIPTION

### 1.1 Introduction

Display model P024H029-IPS is a (TM)Transmissive type color active matrix thin Film transistor(TFT) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching device.This model is composed of a TFT LCD panel, a driving circuit, a back light system. The resolution of a 2.4” contains 240<sub>RGB</sub>X320 dots and can display up to 262K colors.

| Item                          | Specification               | Unit     |
|-------------------------------|-----------------------------|----------|
| Screen Size                   | 2.4 inch                    | Diagonal |
| Number of Pixel               | 240RGB(H)x320(V)            | Pixels   |
| Display area                  | 36.72(H)x48.96(V)           | mm       |
| Pixel pitch                   | 51(H)x153(V)                | um       |
| Outline Dimension             | 42.72x60.26x2.3             | mm       |
| Pixel arrangement             | RGB Vertical Stripe         | — —      |
| Display mode                  | Normally Black/Transmissive | — —      |
| Viewing Direction(eye)        | ALL                         | — —      |
| Gray inversion direction      | --                          |          |
| Display Color                 | 262K                        | — —      |
| Luminance(cd/m <sup>2</sup> ) | 350                         | nit      |
| Contrast Ratio                | 600:1                       | — —      |
| Surface treatment             | — —                         | — —      |
| Interface                     | MCU 8//16 BIT RGB 16/18BIT  |          |
| Back-light                    | LED Side-light type         | — —      |
| Drive IC                      | ST7789V                     |          |
| Operation Temperature         | -20~70                      | °C       |
| Storage Temperature           | -30~80                      | °C       |
| Weight                        | — —                         | g        |

### 1.2 Features

- n MCU 8//16 BIT RGB 16/18BIT parallel interface.

### 1.3 Applications

- n MPOS Device.
- n Personal Navigation Device.
- n Other devices which require high quality displays.

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## 2.0 INPUT INTERFACE PIN ASSIGNMENT

FPC connector is used for electronics interface.

**I/O: I: input, O: output, P: power**

| PinNo. | Symbol   | Function  |
|--------|----------|---|
| 1      | LEDK     | LED back light(Cathode)   |
| 2      | LEDA     | LED back light(Anode)   |
| 3      | GND      | Ground  |
| 4      | VCC      | Power Supply. 2.8V  |
| 5      | IM3      | The MCU interface mode select                                     |
| 6      | TE       | Frame synchronization signal                                      |
| 7      | CS       | Chip select input pin (active low)                                |
| 8      | RS       | Display data/command selection pin in parallel                    |
| 9      | WR       | Write enable in 8080 MCU parallel interface                       |
| 10     | RD       | Read enable in 8080 MCU parallel interface                        |
| 11     | SDA      | SPI interface input/output pin                                    |
| 12     | SDO      | SPI interface output pin  |
| 13-30  | DB0-DB17 | MCU parallel interface data bus                                   |
| 31     | DEN      | Data enable signal for RGB interface operation                    |
| 32     | PCLK     | Dot clock signal for RGB interface operation                      |
| 33     | HSYNC    | Horizontal synchronizing input signal for RGB interface operation |
| 34     | VSYNC    | Vertical synchronizing input signal for RGB interface operation   |
| 35     | RESET    | External reset input  |
| 36-37  | GND      | Ground  |
| 38     | IM0      | The MCU interface mode select                                     |
| 39     | IM1      | The MCU interface mode select                                     |
| 40     | IM2      | The MCU interface mode select                                     |

|                                     |        |              |      |
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## 3.0 ABSOLUTE MAXIMUM RATINGS

### 3.1 Electrical Absolute Rating

#### 3.1.1 TFT LCD Module

| Item                     | Symbol | Min  | Max      | Unit | Note  |
|--------------------------|--------|------|----------|------|-------|
| Digital supply voltage   | VDDI   | -0.3 | +4.6     | V    | GND=0 |
| Analog supply voltage    | VCI    | -0.3 | +4.6     | V    | GND=0 |
| Logic Signal Input Level | VIN    | -0.3 | VDDI+0.5 | V    | GND=0 |

#### 3.1.2 Back-Light Unit

| Item        | Symbol          | Min | Max | Unit | Note |
|-------------|-----------------|-----|-----|------|------|
| LED current | I <sub>BL</sub> | -   | 80  | mA   | -    |
| LED voltage | V <sub>BL</sub> | 2.8 | 3.2 | V    | -    |

### 3.2 Environment Absolute Rating

| Item                  | Symbol | Min | Max | Unit | Note |
|-----------------------|--------|-----|-----|------|------|
| Operating temperature | TOPR   | -20 | 70  | °C   | -    |
| Storage temperature   | TSTG   | -30 | 80  | °C   | -    |

Note:

Permanent damage may occur to the LCD module if beyond this specification.

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## 4.0 OPTICAL CHARACTERISTICS

### 4.1 Optical specification

| Item                         |       | Symbol | Condition  | Min   | Type  | Max   | Unit              | Note      |
|------------------------------|-------|--------|--|-------|-------|-------|-------------------|-----------|
| White luminance (Center)     |       | Lv     | Θ=0<br>Normal<br>Viewing<br>Angle<br>I <sub>BL</sub> =80mA | --    | 250   | --    | cd/m <sup>2</sup> | (4)(5)(7) |
| Response time                |       | Tr+Tf  |  | --    | 35    | 45    | ms                | (3)       |
| Contrast ratio               |       | CR     |  | --    | 600   | --    | --                | (2)(4)    |
| Color Chromaticity (CIE1931) | white | Wx     |  | 0.290 | 0.310 | 0.330 |                   | (6)       |
|                              |       | Wy     |  | 0.316 | 0.336 | 0.356 |                   |           |
| Viewing Angle                | Hor   | ΘL     | CR≥10  | --    | 80    | --    |                   | (1)       |
|                              |       | ΘR     |  | --    | 80    | --    |                   |           |
|                              | Ver   | ΘU     |  | --    | 80    | --    |                   |           |
|                              |       | ΘD     |  | --    | 80    | --    |                   |           |
| Brightness uniformity        |       | Avg    | Θ=0  | 80    | 90    | --    | %                 | (5)       |
| Color Gamut                  |       | NTSC   | Θ=0  | --    | 70    | --    | %                 | (6)       |
| Optima View Direction        |       | Free   |  |       |       |       |                   | (1)       |

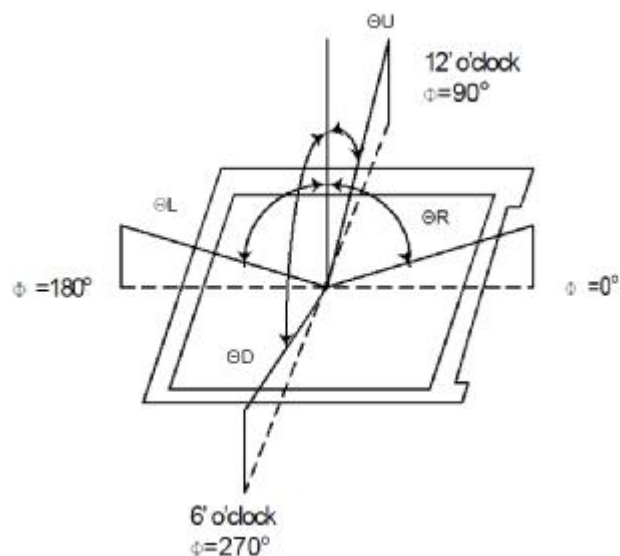
### 4.2 Measuring Condition

- n Measuring surrounding: dark room
- n LED current IL: 80mA
- n Ambient temperature:  $25 \pm 2^{\circ}C$
- n 15min. warm-up time

### 4.3 Measuring Equipment

- n FPM520 of Westar Display technologies, INC., which utilized SR-3 for Chromaticity and BM-7 for other optical characteristics.
- n Measuring spot size: 20 ~ 21 mm

## Note (1) Definition of Viewing Angle

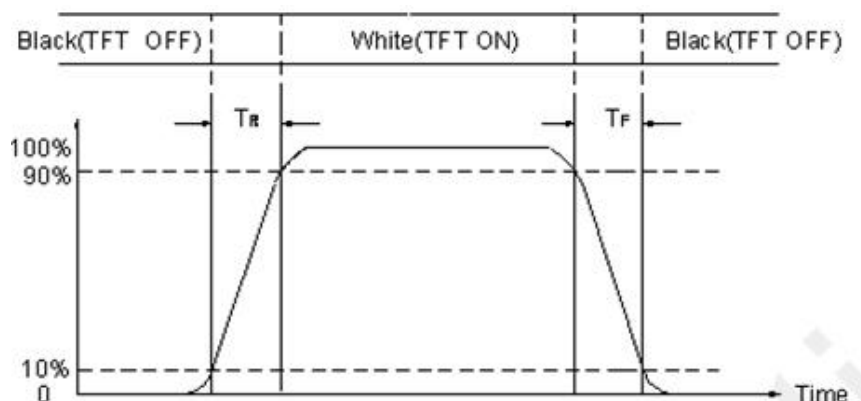


## Note (2) Definition of Contrast Ratio(CR):

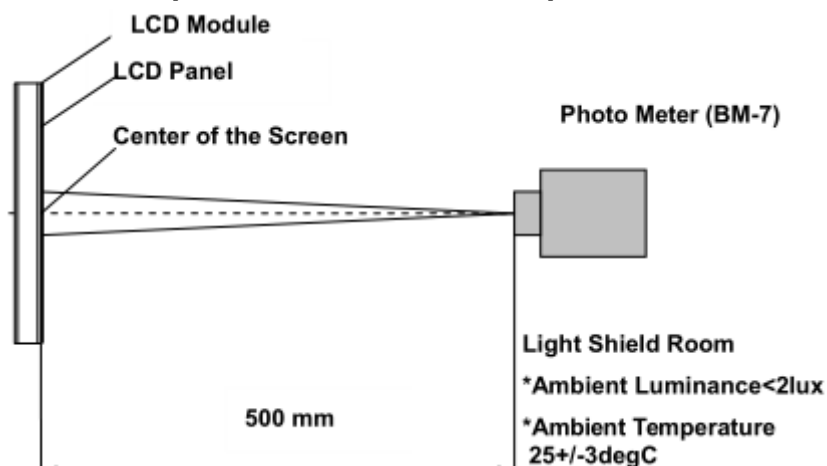
Measured at the center point of panel

$$CR = \frac{\text{Luminance with all pixels white}}{\text{Luminance with all pixels black}}$$

## Note (3) Definition of Response Time: Sum of TR and TF



## Note (4) Definition of optical measurement setup



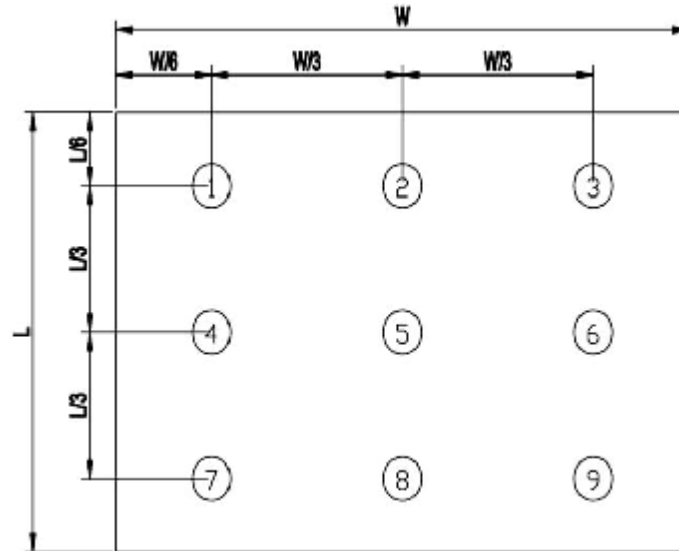
## Note (5) Definition of brightness uniformity

The luminance uniformity is calculated by using following formula.

$$\Delta B_p = B_p (\text{Min.}) / B_p (\text{Max.}) \times 100 (\%)$$

$B_p (\text{Max.})$  = Maximum brightness in 9 measured spots

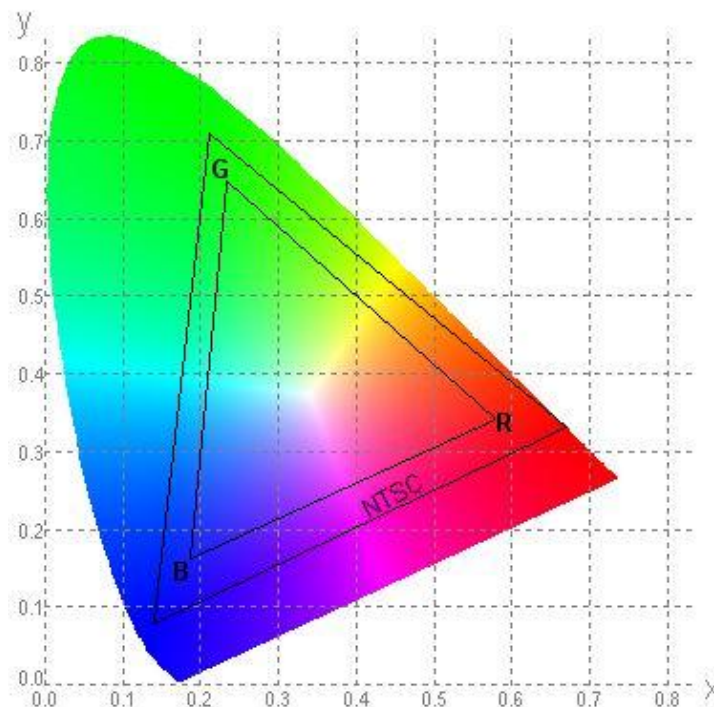
$B_p (\text{Min.})$  = Minimum brightness in 9 measured spots .



## Note (6) Definition of Color of CIE1931 Coordinate and NTSC Ratio.

Color gamut:

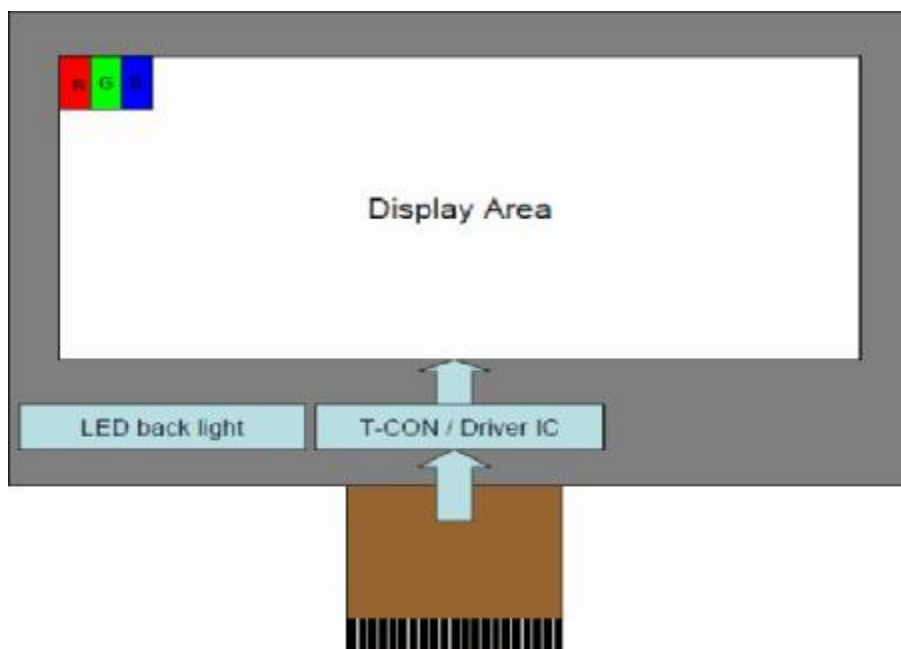
$$S = \frac{\text{Area of RGB triangle}}{\text{Area of NTSC triangle}} \times 100\%$$



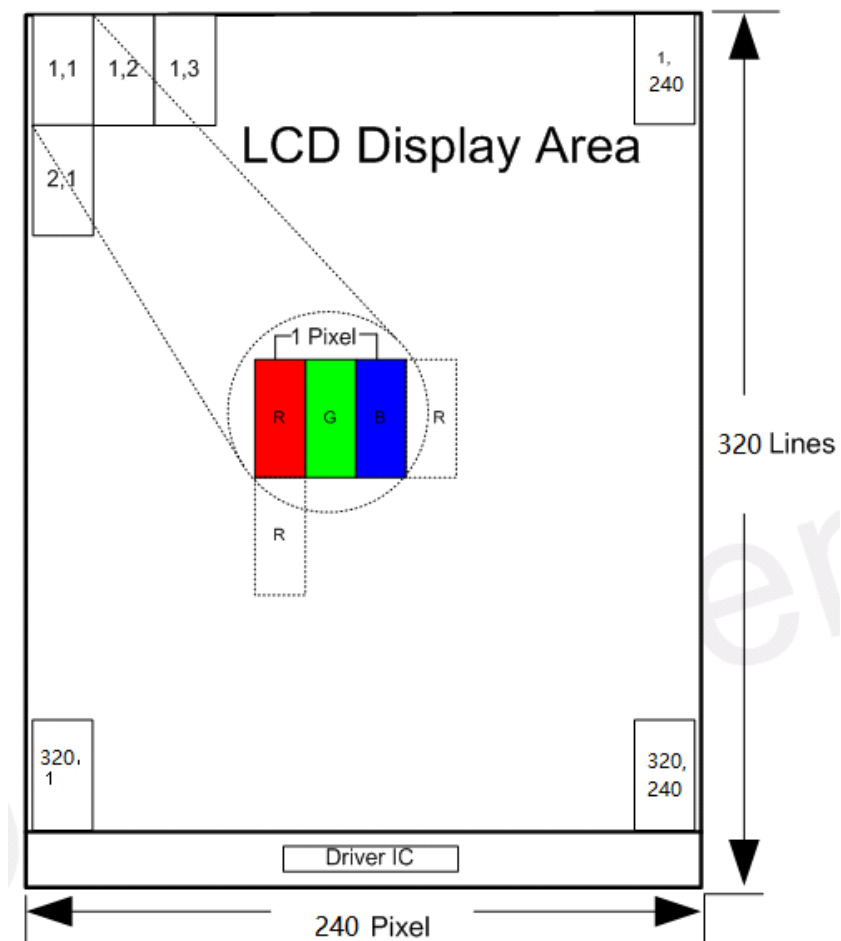
## Note (7) Measured the luminance of white state at center point.

## 5.0 BLOCK DIAGRAM

### 5.1 TFT LCD Module



### 5.2 Pixel Format





## 6.0 ELECTRICAL CHARACTERISTICS

### 6.1 TFT LCD Module

| Item                   | Symbol | Min.    | Typ. | Max.    | Unit | Remark |
|------------------------|--------|---------|------|---------|------|--------|
| Analog supply voltage  | VDD    | 2.4     | 2.8  | 3.3     | V    |        |
| Digital supply voltage | VDDI   | 1.65    | 1.8  | 3.3     |      |        |
| Input signal Voltage   | VIH    | 0.7VDDI | -    | VDDI    | V    |        |
|                        | VIL    | GND     | -    | 0.3VDDI | V    |        |

### 6.2 Back-Light Unit

The backlight system is an edge-lighting type with 4 LED Dies.

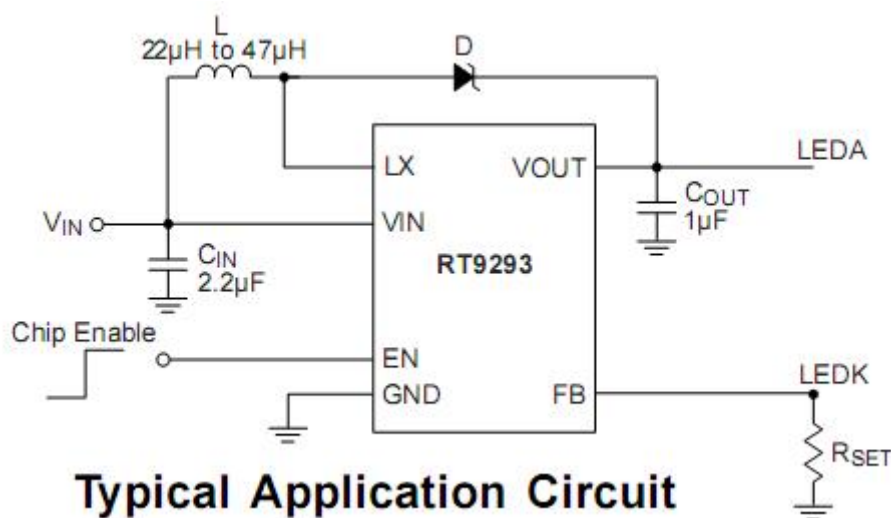
The characteristics of the LED are shown in the following tables.

| Item                    | Symbol | Min | Typ  | Max | Unit | Note   |
|-------------------------|--------|-----|------|-----|------|--------|
| LED current             | IL     | -   | 60   | 80  | mA   | (2)    |
| LED voltage             | VL     | -   | 3.2  | -   | V    |        |
| Operating LED life time | Hr     | -   | 5000 | -   | Hour | (1)(2) |

Note (1) LED life time (Hr) can be defined as the time in which it continues to operate under the condition:  $T_a=25\pm 3\text{ }^{\circ}\text{C}$ , typical IL value indicated in the above table until the brightness becomes less than 50%.

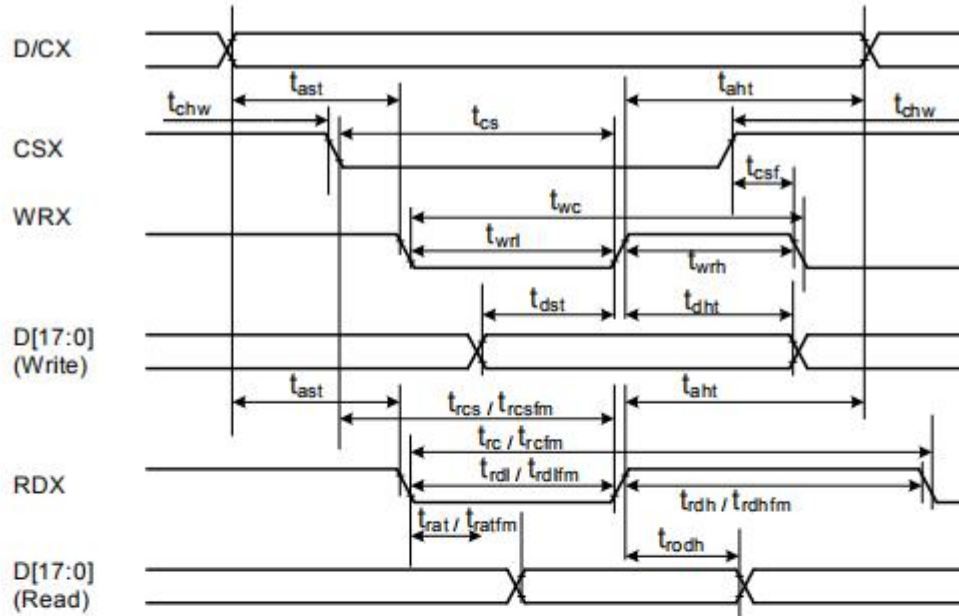
Note (2) The "LED life time" is defined as the module brightness decrease to 50% original brightness at  $T_a=25^{\circ}\text{C}$  and  $I_L=80\text{mA}$ . The LED lifetime could be decreased if operating  $I_L$  is larger than 100mA. The constant current driving method is suggested.

Note (3) Suggested schematic of LED backlight driver



### 6.3 Interface Characteristics

#### 8080 Series MCU Parallel Interface Characteristics: 16-bit Bus

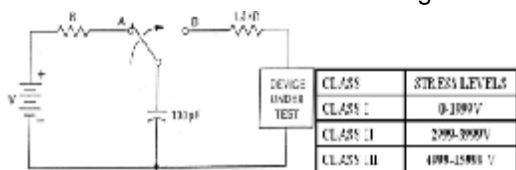


| Signal                                    | Symbol                        | Parameter                          | min | max | Unit | Description                               |
|---|-------------------------------|------------------------------------|-----|-----|------|---|
| DCX                                       | t <sub>ast</sub>              | Address setup time                 | 0   | -   | ns   |   |
|   | t <sub>ahd</sub>              | Address hold time (Write/Read)     | 0   | -   | ns   |   |
| CSX                                       | t <sub>chw</sub>              | CSX "H" pulse width                | 0   | -   | ns   |   |
|   | t <sub>cs</sub>               | Chip Select setup time (Write)     | 15  | -   | ns   |   |
|   | t <sub>r<sub>cs</sub></sub>   | Chip Select setup time (Read ID)   | 45  | -   | ns   |   |
|   | t <sub>r<sub>csfm</sub></sub> | Chip Select setup time (Read FM)   | 355 | -   | ns   |   |
|   | t <sub>csf</sub>              | Chip Select Wait time (Write/Read) | 10  | -   | ns   |   |
| WRX                                       | t <sub>wc</sub>               | Write cycle                        | 66  | -   | ns   |   |
|   | t <sub>wrh</sub>              | Write Control pulse H duration     | 15  | -   | ns   |   |
|   | t <sub>wrl</sub>              | Write Control pulse L duration     | 15  | -   | ns   |   |
| RDX (FM)                                  | t <sub>r<sub>cfm</sub></sub>  | Read Cycle (FM)                    | 450 | -   | ns   |   |
|   | t <sub>r<sub>dhfm</sub></sub> | Read Control H duration (FM)       | 90  | -   | ns   |   |
|   | t <sub>r<sub>dlfm</sub></sub> | Read Control L duration (FM)       | 355 | -   | ns   |   |
| RDX (ID)                                  | t <sub>rc</sub>               | Read cycle (ID)                    | 160 | -   | ns   |   |
|   | t <sub>r<sub>dh</sub></sub>   | Read Control pulse H duration      | 90  | -   | ns   |   |
|   | t <sub>r<sub>dl</sub></sub>   | Read Control pulse L duration      | 45  | -   | ns   |   |
| D[17:0],<br>D[15:0],<br>D[8:0],<br>D[7:0] | t <sub>dst</sub>              | Write data setup time              | 10  | -   | ns   | For maximum CL=30pF<br>For minimum CL=8pF |
|   | t <sub>dht</sub>              | Write data hold time               | 10  | -   | ns   |   |
|   | t <sub>rat</sub>              | Read access time                   | -   | 40  | ns   |   |
|   | t <sub>ratfm</sub>            | Read access time                   | -   | 340 | ns   |   |
|   | t <sub>rod</sub>              | Read output disable time           | 20  | 80  | ns   |   |

Note: T<sub>a</sub> = -30 to 70 °C, V<sub>DDI</sub>=1.65V to 3.3V, V<sub>CI</sub>=2.5V to 3.3V, V<sub>SS</sub>=0V

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## 7.0 Reliability conditions

| NO | Item   | Conditions  | Notes |
|----|--|---|-------|
| 1  | High Temperature Storage                     | Ta=80℃±2℃, 72hrs  |       |
| 2  | Low Temperature Storage                      | Ta=-30℃±2℃, 72hrs   |       |
| 3  | High Temperature Operation                   | Ta=70℃±2℃, 72hrs(Operation state)   |       |
| 4  | Low Temperature Operation                    | Ta=-20℃±2℃, 72hrs(Operation state)  |       |
| 5  | High Temperature and High Humidity (Storage) | Ta=+60℃, 90%RH, 72hrs   |       |
| 6  | Thermal Cycling Test (non operation)         | -20℃(30min) → +70℃(30min), 10cycles   |       |
| 7  | Electro static Discharge                     | Human Body Mode<br>100pF±10%/1500Ω±1%<br>Air±8kV / contact±6kV<br>Consecutive 10times/ Each discharge<br> |       |
| 8  | Vibration test(with carton)                  | Total fixed<br>amplitude:15mm<br>Vibration Frequency :10~55Hz<br>One cycle 60 seconds to 3 directions of<br>X,Y,Z for Each 15 minutes   |       |
| 9  | Drop (with carton)                           | Height: 60cm<br>1 corner, 3 edges, 6 surfaces   |       |

**Note:** There is no display function NG issue occurred, all the cosmetic specification is judged before the reliability stress.

## 8.0 Precautions

### 8.1 Operation

Burn-in sometimes happens when the same character was displayed at along time. Therefore, to prevent Burn-in, it is recommended to set up a Screen-saver function.

### 8.2 Safety

The liquid crystal in the LCD is poisonous, DO NOT put it in your mouth. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and water.

### 8.3 Handling



- a. The LCD module shall be installed flat, without twisting or bending.
- b. COF or FPC has narrow pattern width, so easily become open circuit by external force. DO NOT apply pressure to COF or FPC especially in bending area.



- c. To avoid damage in appearance or malfunction, DO NOT subject the module to mechanical shock or to excessive force on its surface.



- d. The polarizer attached to the display is very easy to damage, handle it with care to avoid scratching.



- e. To avoid contamination on the display surface, DO NOT touch the display surface with bare hands.
- f. Provide a space so that the LCD module does not come into contact with other components.

## 8.4 Static Electricity

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge.



- The LCD module shall be installed flat, without twisting or bending. Ground soldering iron tips, tools and testers when they operate.
- Ground your body when handling the products.
- DO NOT apply voltage to the input terminal without applying power supply.
- DO NOT apply voltage that exceeds the absolute maximum rating.
- Store the products in an anti-electrostatic container.
- Peel off protect tape, attached to polarizer, slowly to minimize ESD damage.

## 8.5 Storage



Store the products in a dark place at +5 ~ +25 degree C, low humidity (50%RH or less).  
DO NOT store the products in an atmosphere containing organic solvents or corrosive gases.

## 8.6 Cleaning



- DO NOT wipe the polarizer with dry cloth, as it might cause scratch.
- Wipe the polarizer with a soft cloth soaked with petroleum IPA, other chemical might damage.

## 8.7 Waste



When dispose of LCD module, manage it at the production waste according to the relevant laws and regulations.



|    |      |
|----|------|
| 1  | LEOK |
| 2  | LEMA |
| 3  | GND  |
| 4  | VCC  |
| 5  | IM3  |
| 6  | TE   |
| 7  | /CS  |
| 8  | RS   |
| 9  | /MR  |
| 10 | /RD  |
| 11 |      |
| 12 | D0   |
| 13 | D1   |
| 14 | D2   |
| 15 | D3   |
| 16 | D4   |
| 17 | D5   |
| 18 | D6   |
| 19 | D7   |
| 20 | D8   |
| 21 | D9   |
| 22 | D10  |
| 23 | D11  |
| 24 | D12  |
| 25 | D13  |
| 26 | D14  |
| 27 | D15  |
| 28 | D16  |
| 29 | D17  |
| 30 | D18  |
| 31 | D19  |
| 32 | D20  |
| 33 | D21  |
| 34 | D22  |
| 35 | D23  |
| 36 | D24  |
| 37 | D25  |
| 38 | D26  |
| 39 | D27  |
| 40 | D28  |

## 1 0.0 LOT MARK

### 10.1 Location of Lot Mark

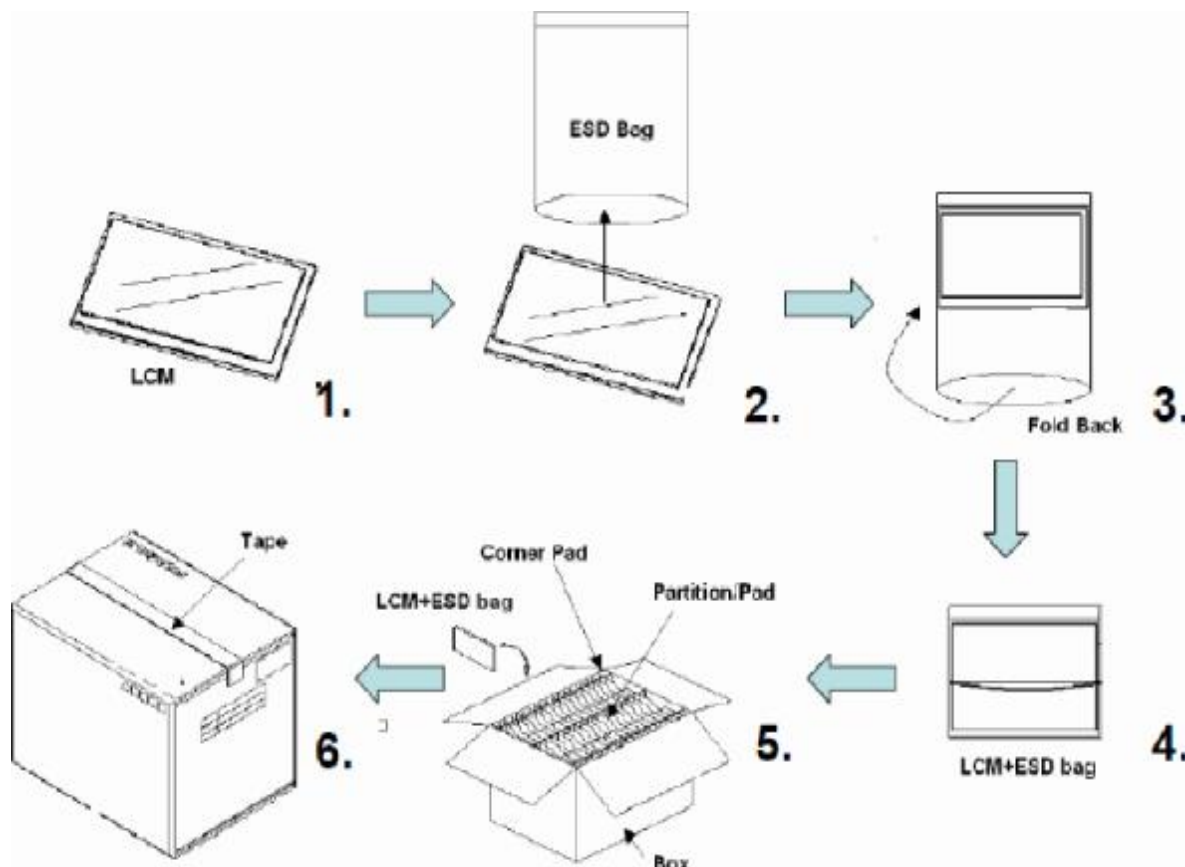
- (1) Location: The label is attached to the backside of the LCD module.
- (2) Detail of the Mark: as attached below.
- (3) This is subject to change without prior notice.

## 11.0 PACKAGE SPECIFICATION

### 11.1 Packing form

| LCM Model | LCM Qty. in the box | Inner Box Size ( mm ) | Notice |
|-----------|---------------------|-----------------------|--------|
|           | TDB                 | TDB                   |        |

### 11.2 Packing assembly drawings



| Items         | Material               | Notice    |
|---------------|------------------------|-----------|
| Box           | Corrugated Paper Board | AB Flute  |
| Partition/Pad | Corrugated Paper Board | A/B Flute |
| Corner Pad    | Corrugated Paper Board | AB Flute  |
| ESD bag       | PE                     |           |

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## 12.0 Items and Criteria:

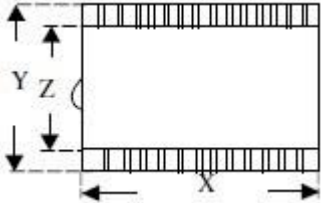
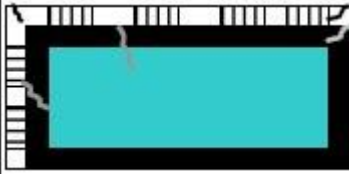
### 12.1 Guarantee

APEX warrants the quality of our products for **1 year** (from the date of delivery). If there are functional defects found during the period of warranty, the defective products would be replaced on a one-to-one basis. Apex would not be responsible for any direct /indirect liabilities consequential to any parties.

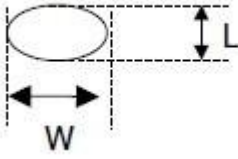
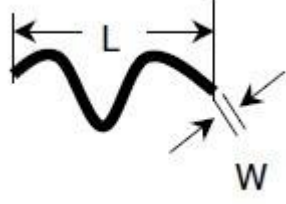
All the products should be stored or used as specified conditions described in these sheets. If module productions are not stored or used as specified conditions, herein, it will be void the **1 year** warranty(guarantee).

### 12.2 Visual inspection criterion in cosmetic

#### (1) Glass defect

| Glass defect |                  |                          |  |
|--------------|------------------|--------------------------|--|
| NO           | Defect           | Criteria                 | Remark   |
| 1            | Dimension(Minor) | By engineering diagram   |   |
| 2            | Cracks(Major)    | Extensive crack 【Reject】 |  |

#### (2) LCM appearance defect

| NO | Defect                | Criteria   |                 | Remark  |
|----|-----------------------|--|-----------------|---|
| 1  | Round type(Minor)     | Spec   | Permissible Qty | 1. $\psi = (L+W)/2$ , L: Length, W: Width<br>2. Disregard if out of A.A.<br> |
|    |                       | $\psi \leq 0.10\text{mm}$  | Disregard       |   |
|    |                       | $0.10\text{mm} < \psi \leq 0.20\text{mm}$                        | 3               |   |
|    |                       | $0.20\text{mm} < \psi$   | 0               |   |
| 2  | Line type(Minor)      | Spec   | Permissible Qty | 1. L: Length, W: Width<br>2. Disregard if out of A.A.<br>                    |
|    |                       | $W \leq 0.03\text{mm}$   | Disregard       |   |
|    |                       | $L \leq 3.0\text{mm}$ and $0.03\text{mm} < W \leq 0.05\text{mm}$ | 2               |   |
|    |                       | $L \leq 3.0\text{mm}$ and $0.05\text{mm} < W \leq 0.10\text{mm}$ | 1               |   |
|    |                       | $W > 0.10\text{mm}$ or $L > 3.0\text{mm}$                        | 0               |   |
| 3  | Polarizer dent(Minor) | Spec.  | Permissible Qty | 1. $\psi = (L+W)/2$ , L: Length, W: Width<br>2. Disregard if out of A.A.  |
|    |                       | $\psi \leq 0.20\text{mm}$  | Disregard       |   |
|    |                       | $0.20\text{mm} < \psi \leq 0.30\text{mm}$                        | 2               |   |
|    |                       | $0.30\text{mm} < \psi \leq 0.50\text{mm}$                        | 1               |   |



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(3) FPC

| NO | Defect                 | Criteria   | Remark |
|----|------------------------|--|--------|
| 1  | Copper peeling(Minor)  | Copper peeling <b>【Reject】</b>   |        |
| 2  | Golden finger          | FPC golden finger broken, dead fold, indentation makes FPC surface broken <b>【Reject】</b><br>Tin plating layer(or gold plating) scratch, but not hurt circuit <b>【Accept】</b><br>Except circuit, other position scratch but not expose metal wire <b>【Accept】</b>  |        |
| 3  | Pin                    | FPC PI layer delamination <b>【Reject】</b><br>Material and color are inconsistent with sample, FPC burrs <b>【Reject】</b><br>FPC Pin deformation but not affect function. <b>【Accept】</b><br>FPC Pin area is dirty <b>【Reject】</b><br>Other than FPC Pin area is dirty but not affect function <b>【Accept】</b>   |        |
| 4  | Golden finger          | Golden finger edge has burrs,foreign material <b>【Reject】</b><br>Golden finger oxidation (dark), uneven electroplating, pinhole, foreign material <b>【Reject】</b><br>Golden finger soldering pad crack exceeds 1/3 length of soldering pad, and soldering pad crack exceed 2 Pins <b>【Reject】</b><br>Golden finger tin plating(or gold plating)scratch, but not hurt circuit <b>【Accept】</b><br>Other than golden finger area scratch but not expose metal circuit <b>【Accept】</b> |        |
| 5  | FPC Silk printing      | Ghosting, incomplete silk printing, wrong printing <b>【Reject】</b>   |        |
| 6  | FPC Circuit line width | Line width deviation exceed 1/3 line width <b>【Reject】</b>   |        |

(4) Black tape



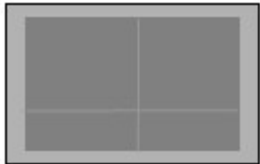
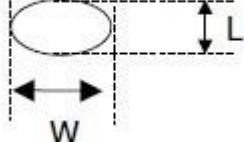
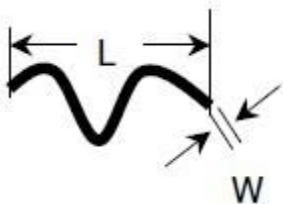
| NO | Defect               | Criteria                      | Remark |
|----|----------------------|-------------------------------|--------|
| 1  | Shift(Minor)         | IC exposed <b>【Reject】</b>    |        |
| 2  | No black tape(Minor) | No black tape <b>【Reject】</b> |        |

(5) Silicon

| NO | Defect                    | Criteria                    | Remark |
|----|---------------------------|-----------------------------|--------|
| 1  | Amount of silicon (Minor) | ITO exposed <b>【Reject】</b> |        |

|                                     |        |              |       |
|-------------------------------------|--------|--------------|-------|
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### 12.3 Visual inspection criterion in electrical display

| NO | Defect                         | Criteria   |                 | Remark  |
|----|--------------------------------|--|-----------------|---|
| 1  | No display (Major)             | Not allowed  |                 |    |
| 2  | Missing line (Major)           | Not allowed  |                 |    |
| 3  | Darker or lighter Line (Major) | Not allowed  |                 |    |
| 4  | Weak line(Major)               | By limited sample  |                 |   |
| 5  | Bright / Dark point (Minor)    | Spec.  | Permissible Qty | 1:1sub-pixel: 1R or 1G or1B<br>2:Point defect area $\geq 1/2$ sub pixel.  |
|    |                                | Bright point   | 1               |   |
|    |                                | Dark point   | 2               |   |
| 6  | Round type (Minor)             | Spec   | Permissible Qty | 1. $\psi=(L+W)/2$ , L: Length, W: Width<br>2. Disregard if out of A.A.<br> |
|    |                                | $\psi \leq 0.10\text{mm}$  | Disregard       |   |
|    |                                | $0.10\text{mm} < \psi \leq 0.20\text{mm}$                        | 3               |   |
|    |                                | $0.20\text{mm} < \psi$   | 0               |   |
| 7  | Line type (Minor)              | Spec.  | Permissible Qty | 1. L: Length, W: Width<br>2. Disregard if out of A.A.<br>                  |
|    |                                | $W \leq 0.03\text{mm}$   | Disregard       |   |
|    |                                | $L \leq 3.0\text{mm}$ and $0.03\text{mm} < W \leq 0.05\text{mm}$ | 2               |   |
|    |                                | $L \leq 3.0\text{mm}$ and $0.05\text{mm} < W \leq 0.10\text{mm}$ | 1               |   |
|    |                                | $W > 0.10\text{mm}$ or $L > 3.0\text{mm}$                        | 0               |   |
| 8  | Mura (Minor)                   | By 5% ND filter invisible  |                 |   |

### 9.2.4. Others

- Issues that are not defined in this document shall be discussed and agreed with both parties.  
(Customer and supplier)
- Unless otherwise agreed upon in writing, the criteria shall be applied to both parties.  
(Customer and supplier)