电话: 18601955397 QQ:2984664835 网店: https://oled-zjy.taobao.com

MODEL NO(不带触摸产品型号):ZJY320L-C18N MODEL NO(不带触摸产品型号):ZJY320L-C18T

3.2 inch LCD TFT

(3.2 英寸真彩色液晶显示屏)

240RGBx320 Resolution and 65K color

(240x320分辨率, 6万5千种色)

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	General Descripti 基本描述

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1.General Description 基本描述

MODEL NO 产品型号	ZJY320L-C18
Display Mode 显示模式	Transmissive 全透
Display Format 显示格式	Graphic 240RGB*320 Dot-matrix 240xRGBx320 图形点阵
Input Data 显示屏接口类型	4 Line-SPI interface 4 线-SPI 串口
Viewing Direction 视角方向	12 o'clock 12 点钟
Drive 显示屏驱动芯片	ILI9341(台湾奕力)

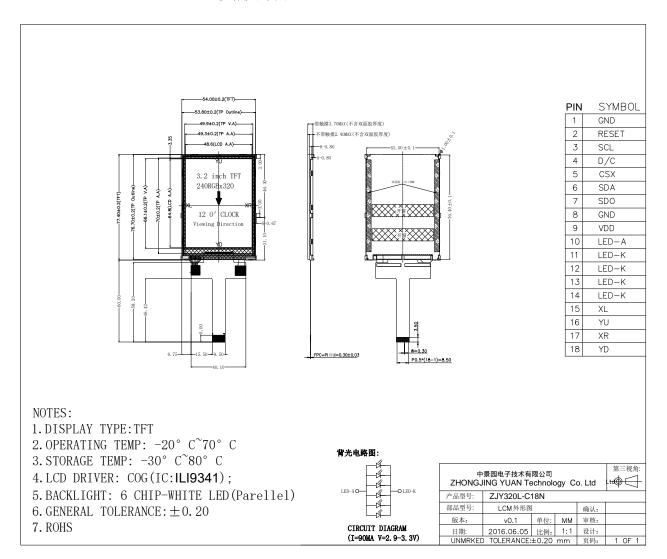
2. Mechanical Specification 机械规格

Item	Specifications	Unit
Dimensional outline 显示屏外围尺寸	54.00(W)*77.40(H)*2.40(T)(不带触摸) 54.00(W)*77.40(H)*3.70(T)(带触摸) (FPC not include)	mm
Resolution 分辨率	240RGB*320	dots
LCD Active area 显示尺寸	48.60(W)*64.80 (H)	mm
Pixel size 像素尺寸	0.2025W)*0.2025(H)	mm

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3.Mechanical Dimension 机械尺寸图



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4. Electrical Maximum Ratings 电气极限

Item	Symbol	Min	Max	Unit	Note
项目	符号	最小值	最大值	单位	备注
Supply voltage (VDDI) 工作电压(VDDI)	V	1.8	3.3	V	-
Supply voltage(VDD) 工作电压(VDD)	V	2.8	3.3	V	-
Operating temperature 工作温度范围	T _{OPR}	-20	70	°C	-
Storage temperature 存储温度范围	T _{STR}	-30	80	°C	-

※NOTE: VDDI 和 VDD 可以直接连一起,共用一组(2.8V~3.3V)电压供电。

5. Brightness characteristic&Power dissipation 亮度特性&功耗

Item	Symbol	Min	Typical	Max	Unit
项目	符号	最小值	典型值	最大值	Omt
LED module Forward voltage	V /	2. 9	3.1	2.2	V
LED 背光源正向电压	V _{LED}	2. 9	3.1	3.3	v
LED module current	T		90		mA
LED 背光源电流	I _{LED}	-	90	-	IIIA
LCD Surface Luminance	Ls	330	350		Cd/m²
显示屏表面亮度	LS	330	330	-	Cu/ III
LCM Surface brightness uniform	$L_{\mathbf{D}}$	80			%
LED 背光源均匀度	பற	80	_	-	70
LCD power dissipation	D		0.32		W
显示屏总功耗	$\mathbf{P}_{\mathbf{LCD}}$	-	0.32	-	"

 $NOTE: P_{LCD=VDD} * (I_{LED} + I_{LCD})$

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MODEL NO(不带触摸产品型号):ZJY320L-C18N MODEL NO(不带触摸产品型号):ZJY320L-C18T

6. Module Function Description 显示屏脚位定义

PIN No. 引脚序号	Symbol 引脚名称	Description 作用描述	Notes 备注
1	GND	Ground (接地脚)	-
2	RESET	-This signal will reset the device and it must be applied to properly initialize the chipSignal is active low. (显示屏复位脚,低电平有效)	-
3	SCL	-This pin is used to be serial interface clock. (4线-SPI 串口时钟)	-
4	D/C	- Display data/command selection pin in 4-line serial interface. (4线-SPI 数据/指令选择选择脚)	-
5	CSX	-Chip selection pin Low enable. High disable. (显示屏驱动芯片选脚,低电平使能)	-
6	SDA	SPI interface input pin. -The data is latched on the rising edge of the SCL signal. -If not used, please fix this pin at VDDI or DGND level. (4线-SPI 数据输入,不用时接 VDDI 或 GND)	-
7	SDO	-SPI interface output pinThe data is output on the falling edge of the SCL signalIf not used, let this pin open. (4线-SPI 数据输出,不用时悬空)	-
8	GND	Ground (接地脚)	-
9	VDD	Power Supply for Analog, Digital System and Booster Circuit. (显示屏主电源供电脚 2.8-3.3V)	-
10	LEDA	Anode of Backlight (2.9V-3.3V Typical:3.1V) (背光正极供电脚,电压范围:2.9-3.3V,典型值:3.1V)	-
11	LEDK	Cathode of Backlight (背光负极供电脚)	-
12	LEDK	Cathode of Backlight (背光负极供电脚)	-

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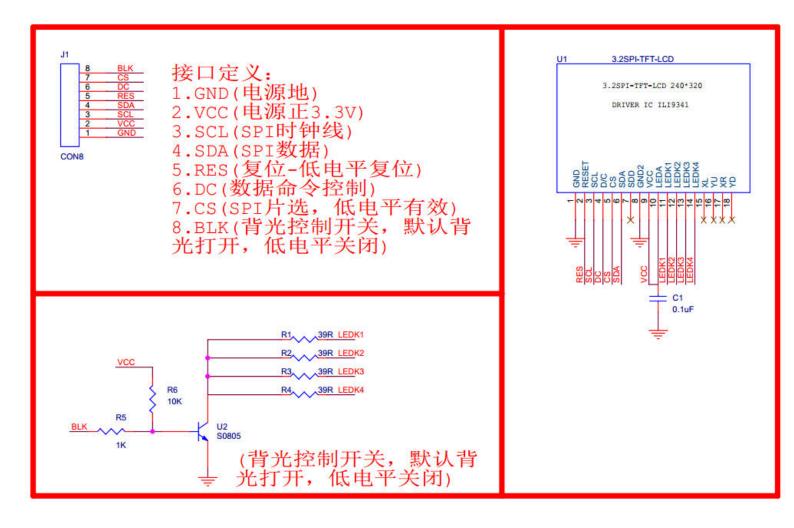
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13	LEDK	Cathode of Backlight (背光负极供电脚)	-
14	LEDK	Cathode of Backlight (背光负极供电脚)	-
15	XL	Touch panel Logical foot (四线电阻触摸屏逻辑脚)	-
16	YU	Touch panel Logical foot (四线电阻触摸屏逻辑脚)	-
17	XR	Touch panel Logical foot (四线电阻触摸屏逻辑脚)	-
18	YD	Touch panel Logical foot (四线电阻触摸屏逻辑脚)	-

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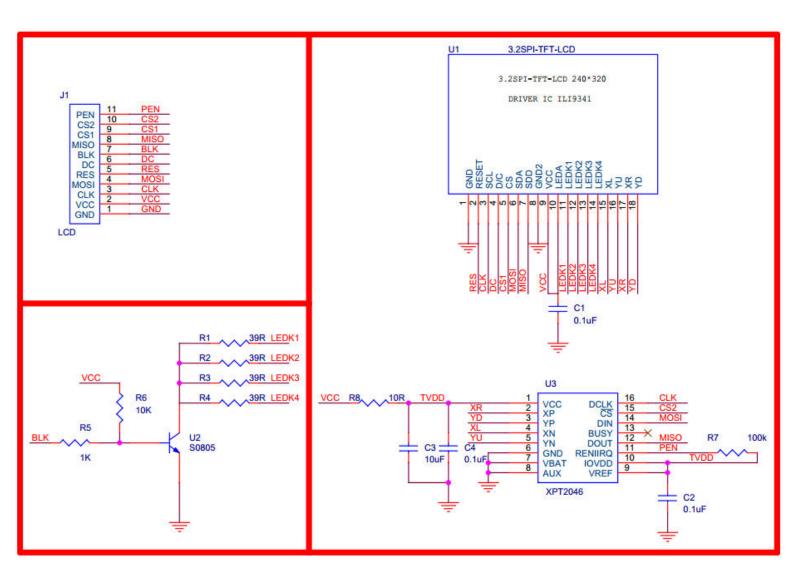


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附图 6-2: 触摸屏参考应用电路



XNOTE:

- 1.若为不带触摸版本,参考附图 6-1 连接电路,把显示屏的 XL、YU、XR、YD 四个触摸脚悬空即可。
 - 2.若为带触摸版本,参考附图 6-2 触摸应用连接电路。

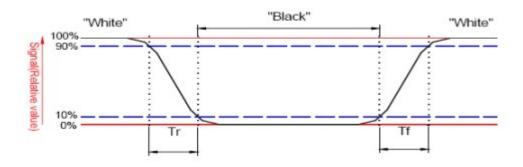
7.Response time&Contrast ratio 响应时间与对比度

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TAores	Cymah al	Condition 条件		T1		
Item 项目	Symbol 符号		Min. 最小值	Typ. 典型值	Max. 最大值	Unit 単位
Response time 响应时间	Tr+Tf	θ =0°	-	16	-	ms
Contrast ratio 对比度	CR	θ =0°	400	500	-	-



响应时间图示

Contrast ratio (CR)= Brightness on the "white" state

Brightness on the "black" state

对比度计算公式

8.Viewing Angle 视角宽度

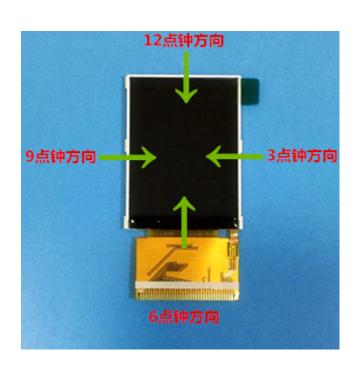
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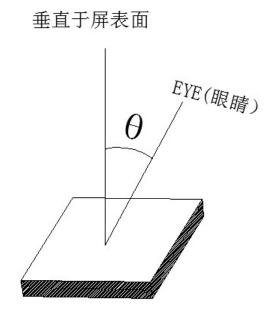
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项目	符号	条件	Min.	Тур.	Max.	单位
			最小值	典型值	最大值	
	Тор	CR≥10	40	50		
	12 点钟方向	对比度大于等于 10	40	30	-	
Viovina	Bottom	CR≥10	55	65		
Viewing angle	6 点方向	对比度大于等于 10	33	03	-	Deg.
视角宽度	Left	CR≥10	55	65	-	度
7九用 见汉	9点钟方向	对比度大于等于 10				
	Right	CR≥10	55	65	-	
	3 点钟方向	对比度大于等于 10				





NOTE:3 点,6点,9点,12点方向视角的大小指的是垂直于屏表面的线眼睛视线之间的夹角(0)。

9. Reliability Trial 可靠性实验

NO.	ITEM	CONDITION	CRITERION
I			

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序号	实验项目	实验环境	实验规范
1	High Temperature Non-Operating Test	80°C*120Hrs	No Defect Of
	高温存储实验 Low Temperature Non-Operating Test		Operational Function In Room
2	低温存储实验	-30°C*120Hrs	Temperature Are
3	High Temperature/Humidity Non Operating Test	60°C*90%RH*120Hrs	Allowable
	高温高湿实验		室温运行功能无缺陷
4	High Temperature Operating Test	70°C*72Hrs	
	高温工作实验		
5	Low Temperature Operating Test	-20°C*72Hrs	
	低温工作实验	20 0 721115	
6	Thermal Shock Test	-20 °C (30Min) ν 70 °C	
	热冲实验	(30Min) *10CYCLES	

10.Inspection standards 检验标准

10.1 Glass defect

NO	Defect item	Criteria	Remark
1	Dimension Unconformity (Major defect)	By Engineering Drawing	
2	Cracks (Major defect)	 Linear cracks panel Reject Nonlinear crack contrast by limited sample 	
3	Glass extrude the conductive area (minor defect)	a: disregards and no influence assemblage. 1) b≤1/3Pin width(non bonding area) 【Accept】 2)bonding area≤0. 5mm 【Accept】	A: Length, b: Width
4	Pin-side ,conductive area damaged (minor defect)	(a c: disregards) b≤1/3of effective length for bonding electrode 【Accept】	a: length, b: Width, c: Thickness
5	Pin-side,non-conductive area damaged (minor defect)	1)Damage area don't touch the ITO (Inclueling contraposition mark, except scribing mark) 【Accept】 2)C <t 3of="" b≤bm1="" td="" width="" 【accept】<=""><td>a: Length, b: Width c: Thickness</td></t>	a: Length, b: Width c: Thickness

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		3)c=T b not touch the seal glue [Acce 4)a disregards	ept]	٠
6	Non-pin-side damage (minor defect)	c <t 1="" 1)b="" 3bm="" [rejection="" exceeds="" glue]="" glue]<="" of="" seal="" td="" the=""><td>BM 內緣</td><td>ь</td></t>	BM 內緣	ь

10.2LCD appearance defect(View area)

NO	Defect item	Criteria		Remark
		Specification	Allowable	note1:L: Length, W: Width
	Fiber class	W ≤ 0.03mm	disregard	note2: disregard if out of AA
	Fiber glass cratch polarizer	$0.03 \text{mm} < W \le 0.05 \text{mm};$	2	← т →
1	scratch/folded	L≦3.0mm	2	
	(minor defect)	$0.05 \text{mm} < W \le 0.1 \text{mm};$	1	V W
	(mmor derect)	L≦3.0mm	1	W
		W>0.1mm;L>3.0mm	0	·
	Polarizer bubble	φ ≤ 0.2mm	disregard	note1: $\Phi = (L+W)/2$, L:Length,
2	concave and convex	0.2 mm $< \phi \le 0.3$ mm	2	W :Width
2	(minor defect)	0.3 mm $< \phi \le 0.5$ mm	1	note2:disregard if out of AA
	(mmor derect)	0.5mm< ф	0	
		φ ≤ 0.15mm	disregard	note2:disregard if out of AA
	Black dots, dirty dots,	$0.15 \text{mm} < \phi \le 0.25 \text{mm}$	2	
3	impurities, eye winker	0.25 mm $< \phi \le 0.3$ mm	1	<u>↓</u> •
	(minor defect)	0.3mm< φ	0	ϕ
		φ ≦0.1mm	disregard	note1: $\Phi = (L+W)/2$, L=Length,
4	Polarizer prick	0.1 mm $< \phi \le 0.25$ mm	3	W=Width
	(minor defect)	ф>0.25mm	0	note2:the distance between two dots>5mm

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11.Package Method 包装方法

显示屏出货包装示意图:

