# UNISONIC TECHNOLOGIES CO., LTD

# **TL431**

# LINEAR INTEGRATED CIRCUIT

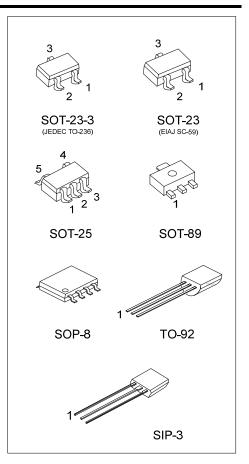
# PROGRAMMABLE PRECISION REFERENCE

#### **DESCRIPTION**

The UTC TL431 is a three-terminal adjustable regulator with a guaranteed thermal stability over applicable temperature ranges. The output voltage may be set to any value between  $V_{\text{REF}}$ (approximately 2.5V) and 36V with two external resistors. It provides very wide applications, including shunt regulator, series regulator, switching regulator, voltage reference and others.

#### **FEATURES**

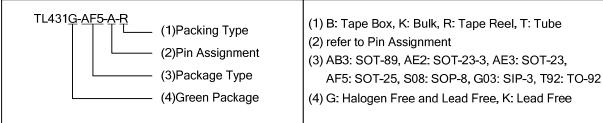
- \* Programmable output Voltage to 36V.
- \* Low dynamic output impedance  $0.2\Omega$ .
- \* Sink current capability of 1.0 to 100mA.
- \* Equivalent full-range temperature coefficient of 50ppm/°C typical for operation over full rated operating temperature range.



### ORDERING INFORMATION

Ordering Number		Daakaga		Pin Assignment 1 2 3 4 5 6 7 8				Packing			
Lead Free	Halogen Free	Package	1	2	3	4	5	6	7	8	Packing
-	TL431G-AB3-R	SOT-89	R	Α	Κ	ı	ı	ı	-	1	Tape Reel
-	TL431G-AE2-R	SOT-23-3	K	R	Α	ı	ı	ı	•	1	Tape Reel
-	TL431G-AE3-R	SOT-23	Κ	R	Α	ı	ı	ı	-	1	Tape Reel
-	TL431NSG-AE3-R	SOT-23	R	K	Α	ı	ı	ı	-	1	Tape Reel
-	TL431NSG-AE2-R	SOT-23-3	R	K	Α	ı	ı	ı	•	1	Tape Reel
-	TL431G-AF5-R	SOT-25	Χ	Х	Κ	R	Α	ı	-	1	Tape Reel
-	TL431NG-AF5-A-R	SOT-25	Χ	Α	Χ	Κ	R	ı	-	1	Tape Reel
-	TL431G-S08-R	SOP-8	Κ	Α	Α	Χ	Χ	Α	Α	R	Tape Reel
-	TL431G-G03-K	SIP-3	R	Α	K	·	ı	ı	•	-	Bulk
TL431K-T92-B	TL431G-T92-B	TO-92	R	Α	K	-	-	-	-	-	Tape Box
TL431K-T92-K	TL431G-T92-K	TO-92	R	Α	Κ	1			-	-	Bulk

Note: Pin Code: K: Cathode R: Reference X: No Connection A: Anode

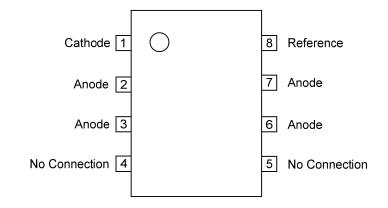


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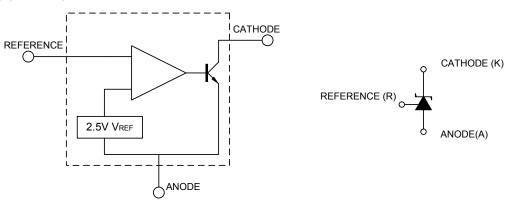
#### ■ MARKING

PACKAGE	MARKING	MARKING PACKAGE MARKING				
SOT-23-3 SOT-23 (TL431)	3   431.G   1	SOP-8	8 7 6 5  UTC 0000  TL431G  Date Code  1 2 3 4 Lot Code			
SOT-23-3 SOT-23 (TL431NS)	3 	SIP-3	431G Date Code			
SOT-25	5 4	TO-92	UTC TL431 G: Halogen Free Data Code			
SOT-89	□□□□ TL431G  Data Code					

# ■ PIN CONFIGURATION(For SOP-8)



# ■ BLOCK DIAGRAM



## ■ ABSOLUTE MAXIMUM RATINGS (Operating temperature range applies, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Cathode Voltage		$V_{KA}$	37	V
Cathode Current Range(Continuous)		I <sub>KA</sub>	-100 ~ +150	mA
Reference Input Current Range		I <sub>REF</sub>	-0.05 ~ +10	mA
	TO-92		770	mW
	SOT-89		800	mW
Power Dissipation	SOT-23/SOT-23-3	$P_D$	300	mW
Power Dissipation	SOT-25		300	MVV
	SIP-3		400	mW
	SOP-8		600	mW
Operating Junction		$T_J$	+150	°C
Operating Ambient		$T_OPR$	-40 ~ +85	°C
Storage Temperature		T <sub>STG</sub>	-65 ~ <b>+</b> 150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged Absolute maximum ratings are stress ratings only and functional device operation is not implied.

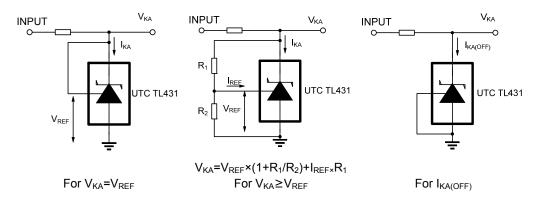
## ■ RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	
Cathode Voltage	$V_{KA}$	$V_{REF}$		36	V	
Cathode Current	I <sub>KA</sub>	1		100	mA	

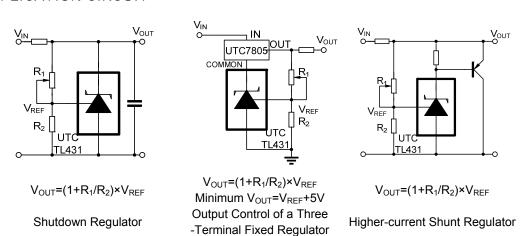
#### ■ ELECTRICAL CHARACTERISTICS (T<sub>C</sub>= 25°C, unless otherwise specified.)

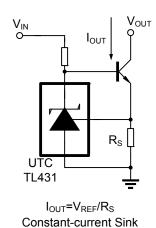
PARAMETER	SYMBOL	TEST CONDITIONS			MIN	TYP	MAX	UNIT
				TL431-A	2.483	2.495	2.507	V
Deference Input Voltage	$V_{RFF}$	\/ -\/   -1/	lm Λ	TL431-1	2.470	2.495	2.520	V
Reference Input Voltage	V REF	$V_{KA} = V_{REF}, I_{KA} = 10 \text{mA}$		TL431-2	2.520	-	2.545	V
				TL431-3	2.445	ı	2.470	V
Deviation of reference Input Voltage Over	ΔVREF	V <sub>KA</sub> =V <sub>REF</sub> ,I <sub>KA</sub> =10mA,				4.5	17	mV
temperature	ΔΤ	0°C ≤T <sub>A</sub> ≤70°C			4.5	17	IIIV	
Ratio of Change in Reference Input	ΔVREF	lκ <sub>Δ</sub> =1()mA <del></del>		10V~V <sub>REF</sub>		-1.0	-2.7	mV/V
Voltage to the Change in Cathode Voltage	$\Delta V$ ka			36V~10V		-0.5	-2.0	mV/V
Reference Input Current	$I_{REF}$	I <sub>KA</sub> =10mA, R1=10kΩ, R2=∞				1.5	4	μΑ
Deviation of Reference Input Current Over	ΔIREF	I <sub>KA</sub> =10mA, R1=10kΩ, R2=∞,				0.4	1.2	
Full Temperature Range	ΔΤ	T <sub>A</sub> =full Temperature				0.4	1.2	μA
Minimum Cathode Current for Regulation	I <sub>KA(MIN)</sub>	$V_{KA} = V_{REF}$				0.3	0.5	mA
Off-State Cathode Current	I <sub>KA(OFF)</sub>	V <sub>KA</sub> =36V, V <sub>REF</sub> =0				0.05	1.0	μΑ
Dynamic Impedance	$Z_{KA}$	V <sub>KA</sub> =V <sub>REF</sub> , I <sub>KA</sub> =1~ 100mA,f≤1.0kHz				0.15	0.5	Ω

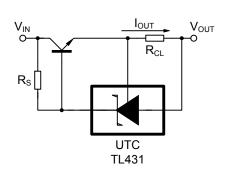
#### ■ TEST CIRCUIT



#### ■ APPLICATION CIRCUIT

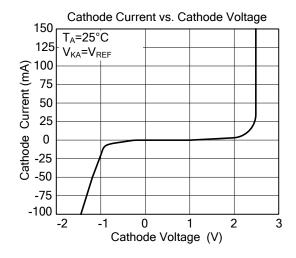


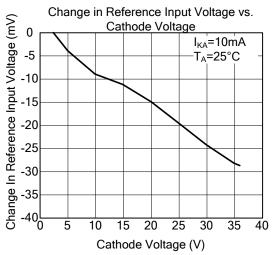


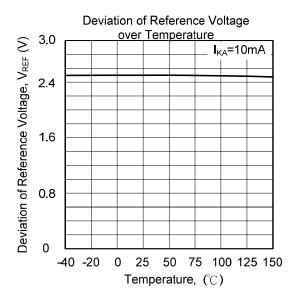


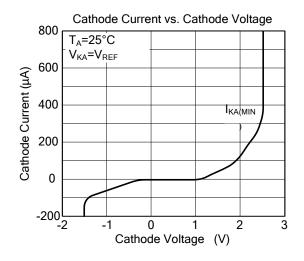
I<sub>OUT</sub> =V<sub>REF</sub>/R<sub>CL</sub>
Current Limiting or Current Source

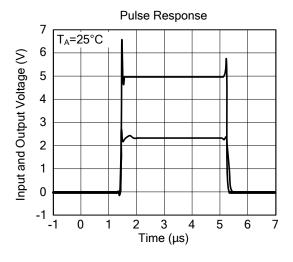
# ■ TYPICAL CHARACTERISTICS

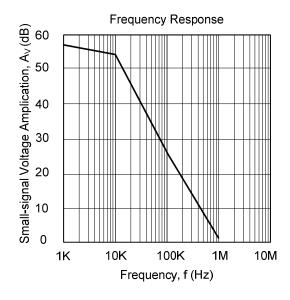












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