Universiti Tunku Abdul Rahman					
Form Title: Disclosure For Layout-Design of Integrated Circuit Protection					
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CONFIDENTIAL

The protection for the layout-design of an integrated circuit in Malaysia is governed by the Layout-Designs of Integrated Circuits Act 2000. Layout-design means the three-dimensional disposition of the elements of an integrated circuit and some or all of the interconnections of the integrated circuit or such a three dimensional disposition prepared for an integrated circuit intended for manufacture. The duration of protection is 10 years from the date the layout-design is first commercially exploited. If the layout-design is not commercially exploited, then the duration of protection is 15 years after the date the layout-design is created. Please complete this form and submit the form together with a copy of the drawing of the layout-design and a sample of the integrated circuit.

SECTION A: To be completed by the Applicant

1. General			
a. Describe the nature or function of the layout-design:			
b. Provide the date when the layout-design was fixed in material form or incorporated into an integrated circuit:			
Date (dd/mm/yyyy)			
c. Has this layout-design been commercialised?			
C. ⊓as triis layout-design been commercialised? ☐ Yes			
□ No			
(i) If Yes, please indicate the date of commercialisation:			
Date (dd/mm/yyyy)			
(ii) If Yes, please provide the details of the organisation who has commercialised it:			

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SECTION B: For Department of Consultancy and Commercialisation Use Only

Application No.	
IPMC Decision	Layout-design Eligible for Protection: ☐ Yes ☐ No
Decision Date	