



Towards Cognitive AI Systems: Workload Characterization and Hardware Architecture for Neuro-Symbolic AI

Zishen Wan

PhD Student @ School of ECE, Georgia Tech

Email: zishenwan@gatech.edu

Webpage: <https://zishenwan.github.io>

Guest Lecture @ ECE 8893, Parallel Programming for FPGAs
January 28, 2025

Neural Networks in Our Daily Life



Image Recognition



Speech Recognition



Language Translation



Autonomous Vehicle



Medical Diagnosis



Financial Services



Recommendation Systems

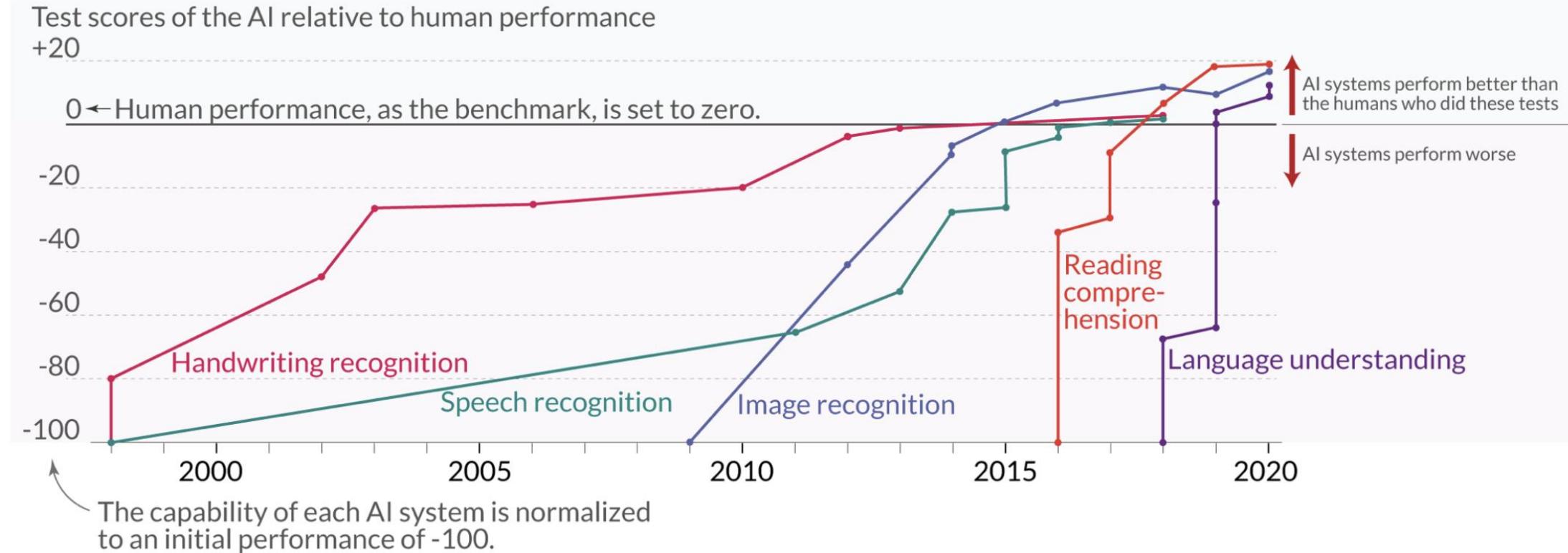


ChatGPT

State of AI / Landscape

Language and image recognition capabilities of AI systems have improved rapidly

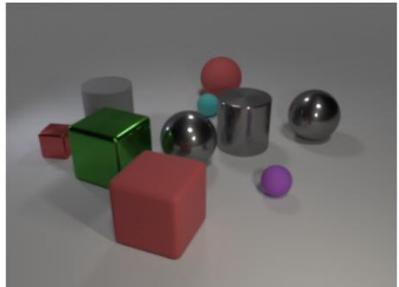
Our World
in Data



Data source: Kiela et al. (2021) – Dynabench: Rethinking Benchmarking in NLP
OurWorldinData.org – Research and data to make progress against the world's largest problems.

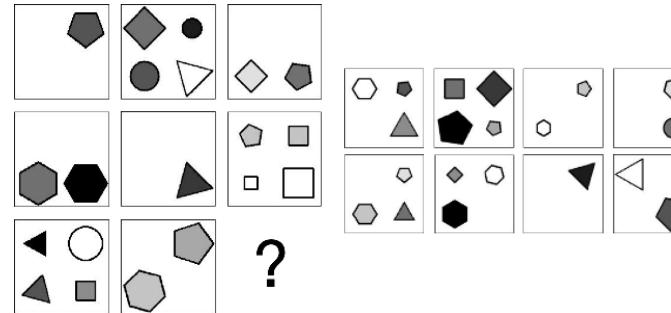
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But... Is That Enough?



(i) Remove all gray spheres. How many spheres are there? (3), (ii) Take away 3 cubes. How many objects are there? (7), (iii) How many blocks must be removed to get 1 block? (2)

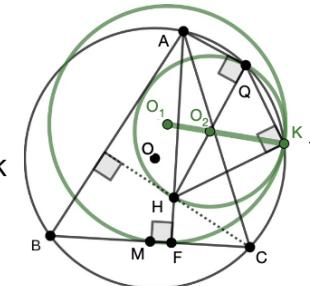
Complex Question Answering
NN accuracy: 50%



Abstract Reasoning
NN accuracy: 53%

IMO 2015 P3

"Let ABC be an acute triangle. Let (O) be its circumcircle, H its orthocenter, and F the foot of the altitude from A. Let M be the midpoint of BC. Let Q be the point on (O) such that $QH \perp QA$ and let K be the point on (O) such that $KH \perp KQ$. Prove that the circumcircles (O_1) and (O_2) of triangles FKM and KQH are tangent to each other."



Automated Theorem Proving
NN accuracy: 0%



Interactive Learning
NN accuracy: 71%

Scenario
Imagine that a stranger will give Hank one thousand dollars to break all the windows in his neighbor's house without his neighbor's permission. Hank carries out the stranger's request.

Imagine that there are five people who are waiting in line to use a single-occupancy bathroom at a concert venue. Someone at the back of the line needs to throw up immediately. That person skips to the front of the line instead of waiting in the back.

At a summer camp, there is a pool. Right next to the pool is a tent where the kids at the camp have art class. The camp made a rule that there would be no cannonballing in the pool so that the art wouldn't get ruined by the splashing water. Today, there is a bee attacking this kid, and she needs to jump into the water quickly. This kid cannonballs into the pool.



Ethical Decision Making
NN accuracy: 65%

Farmer John has N cows ($2 \leq N \leq 10^5$). Each cow has a breed that is either Guernsey or Holstein. As is often the case, the cows are standing in a line, numbered $1 \dots N$ in this order.

Over the course of the day, each cow writes down a list of cows. Specifically, cow i 's list contains the range of cows starting with herself (cow i) up to and including cow E_i ($i \leq E_i \leq N$).

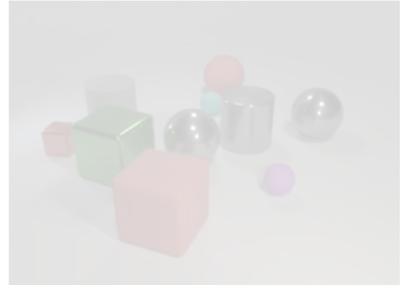
FJ has recently discovered that each breed of cow has exactly one distinct leader. FJ does not know who the leaders are, but he knows that each leader must have a list that includes all the cows of their breed, or the other breed's leader (or both).

Help FJ count the number of pairs of cows that could be leaders. It is guaranteed that there is at least one possible pair.



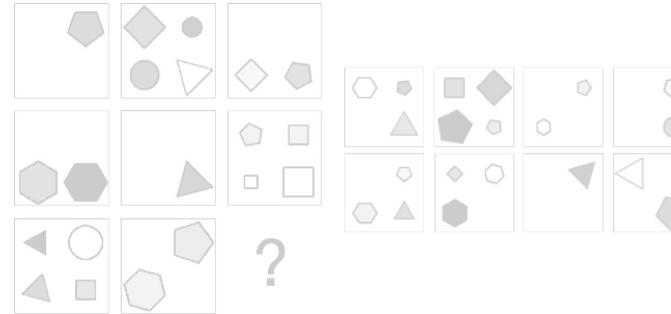
Competitive Programming
NN accuracy: 8.7%

But... Is That Enough?



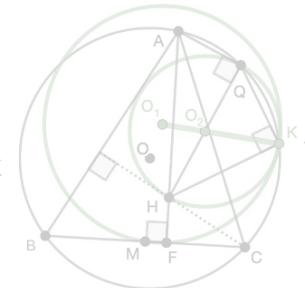
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Neuro-Symbolic AI



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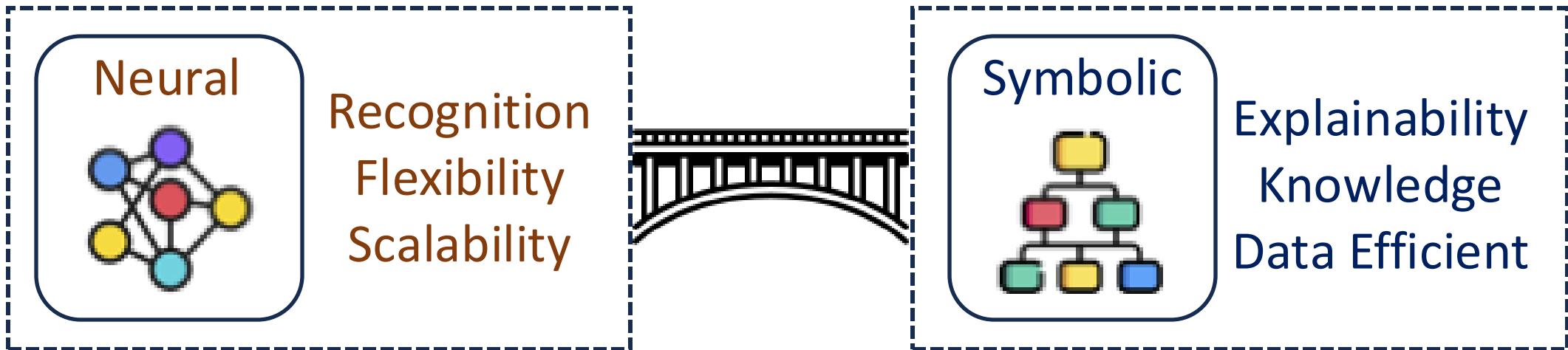
Problem

Competitive Programming
NN accuracy: 8.7%

Outline

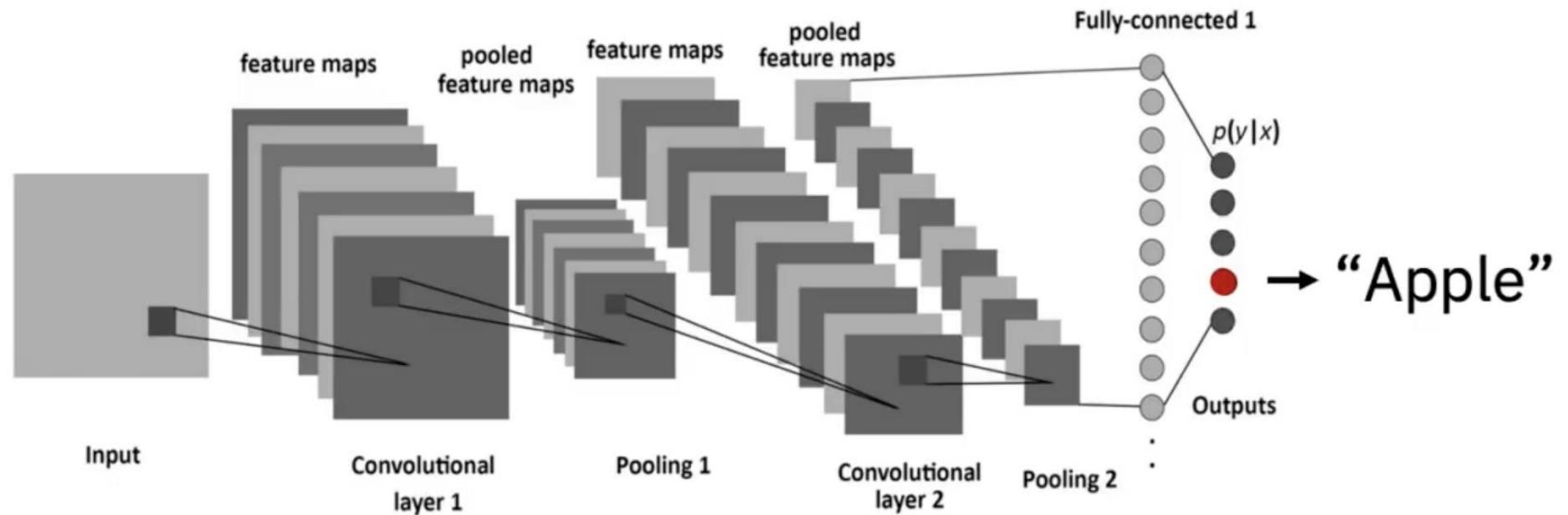
- **Neuro-symbolic AI 101**
- Neuro-symbolic AI workload characterization
- Neuro-symbolic AI hardware architecture
- Final project: neuro-symbolic kernel optimization

What is Neuro-Symbolic AI?



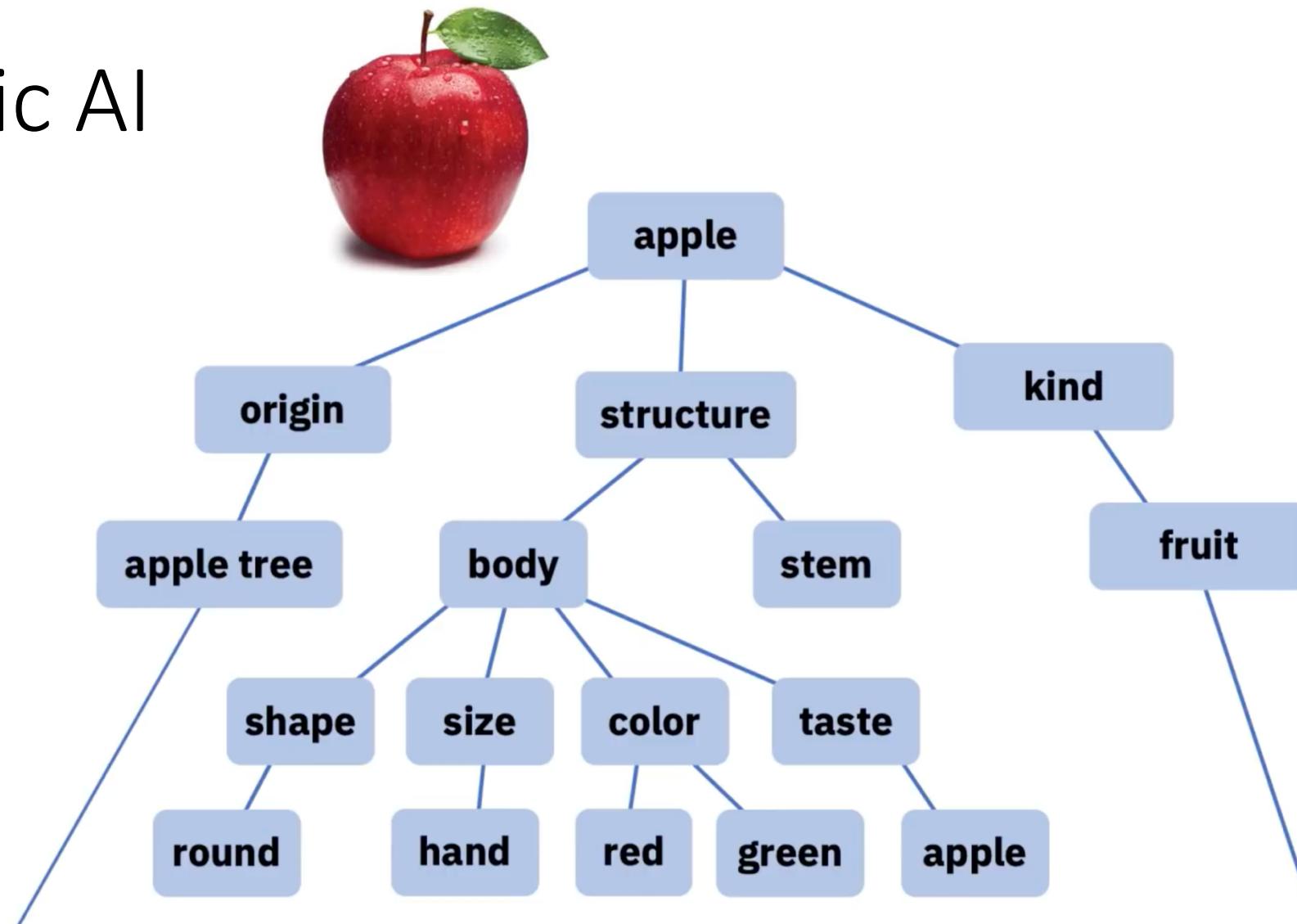
Towards Cognitive and Trustworthy AI Systems

Neural Network



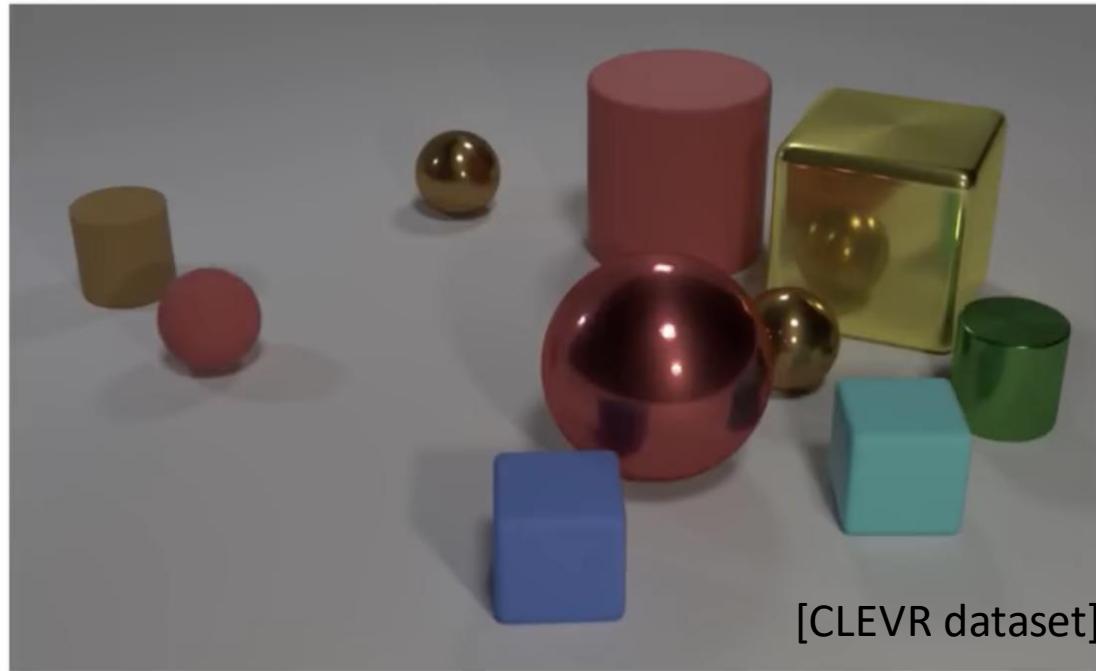
Slide Adapted from MIT 6.S191: Neurosymbolic AI

Symbolic AI



Slide Adapted from MIT 6.S191: Neurosymbolic AI

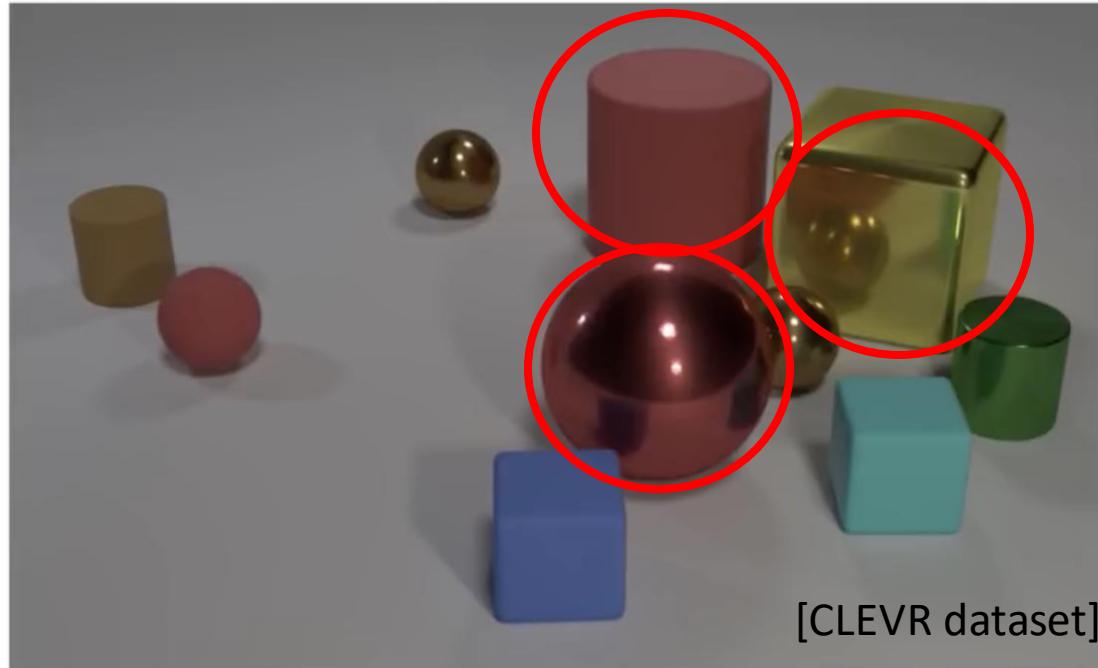
Neuro-Symbolic AI Example: Visual Reasoning



Question: *Are there an equal number of large things and metal spheres?*

Slide Adapted from MIT 6.S191: Neurosymbolic AI

Neuro-Symbolic AI Example: Visual Reasoning



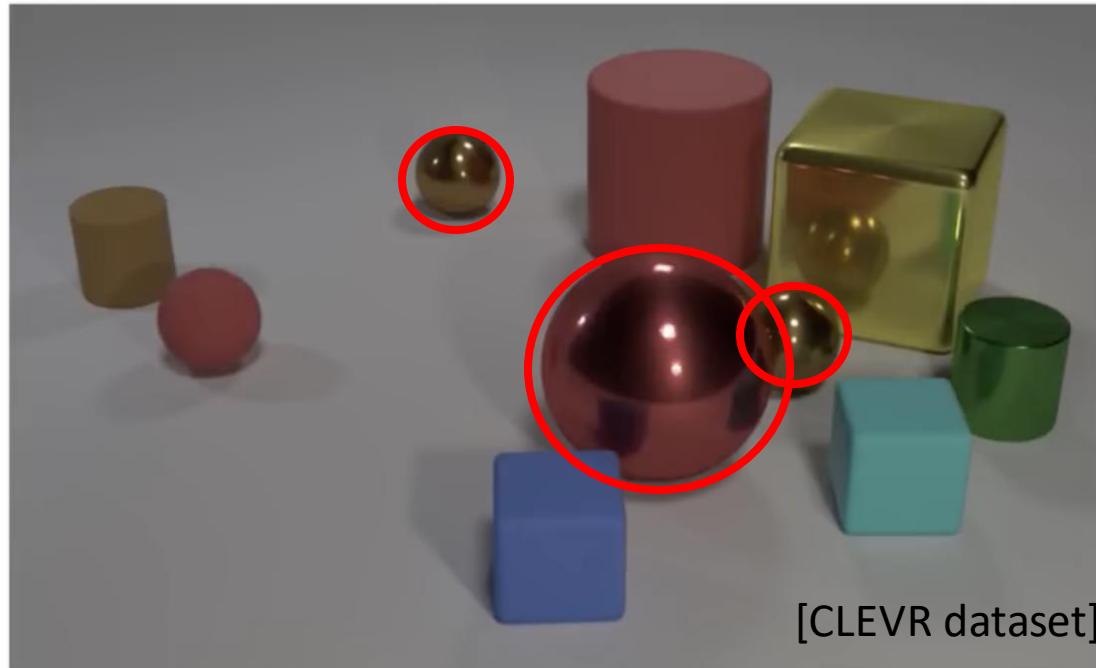
Question: Are there an *equal number* of large things and metal spheres?

3 large things!



Slide Adapted from MIT 6.S191: Neurosymbolic AI

Neuro-Symbolic AI Example: Visual Reasoning



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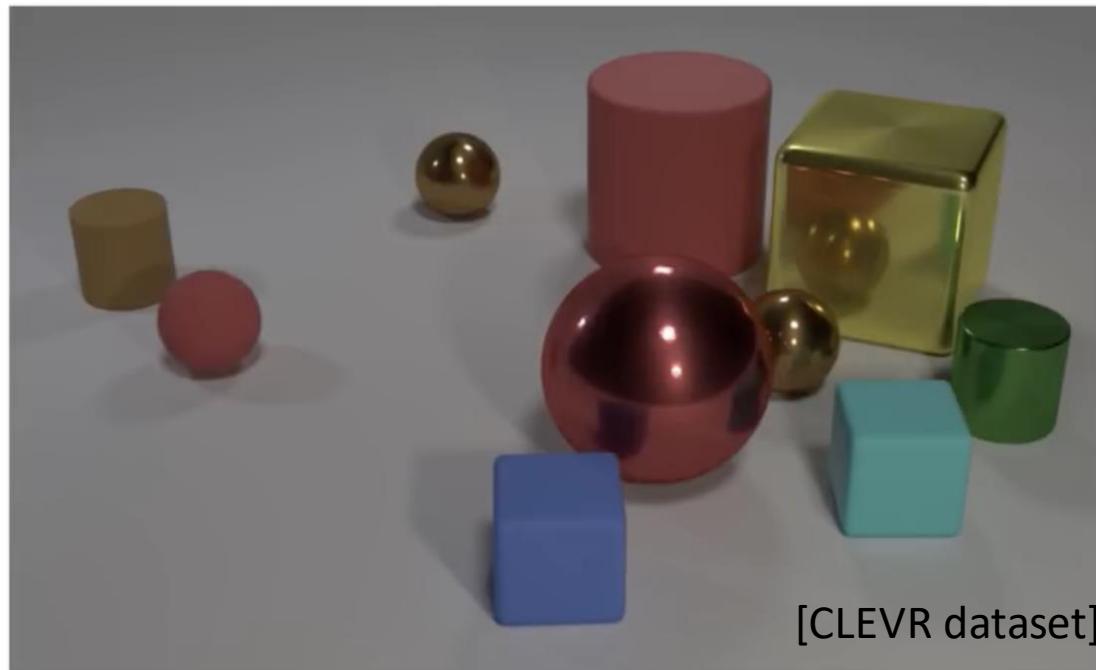
3 large things!

3 metal spheres!

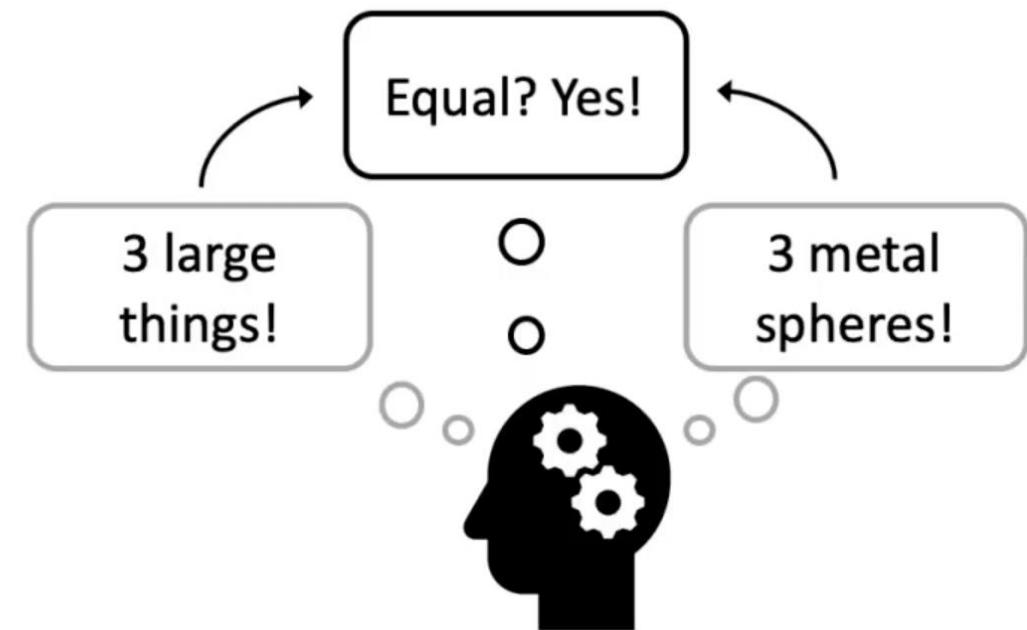


Slide Adapted from MIT 6.S191: Neurosymbolic AI

Neuro-Symbolic AI Example: Visual Reasoning

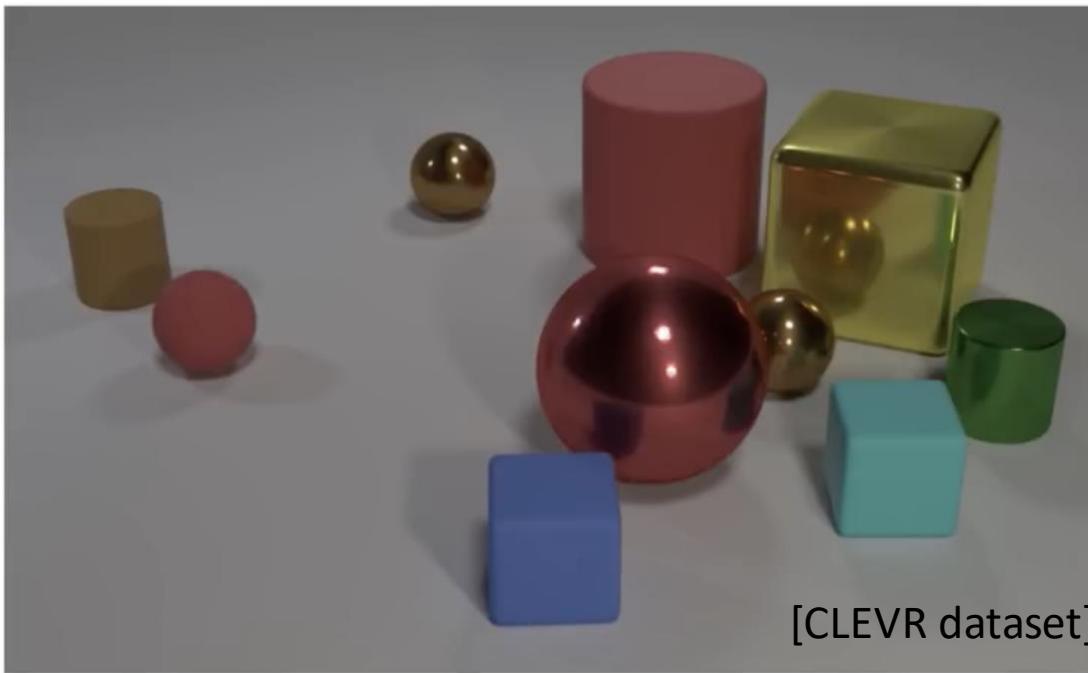


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Slide Adapted from MIT 6.S191: Neurosymbolic AI

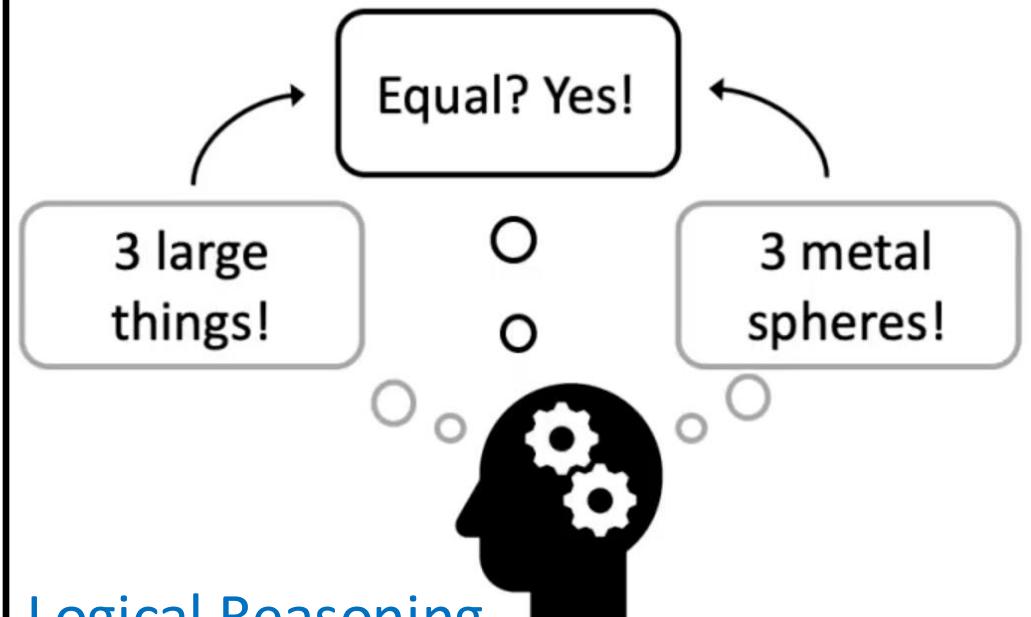
Neuro-Symbolic AI Example: Visual Reasoning



Visual Perception

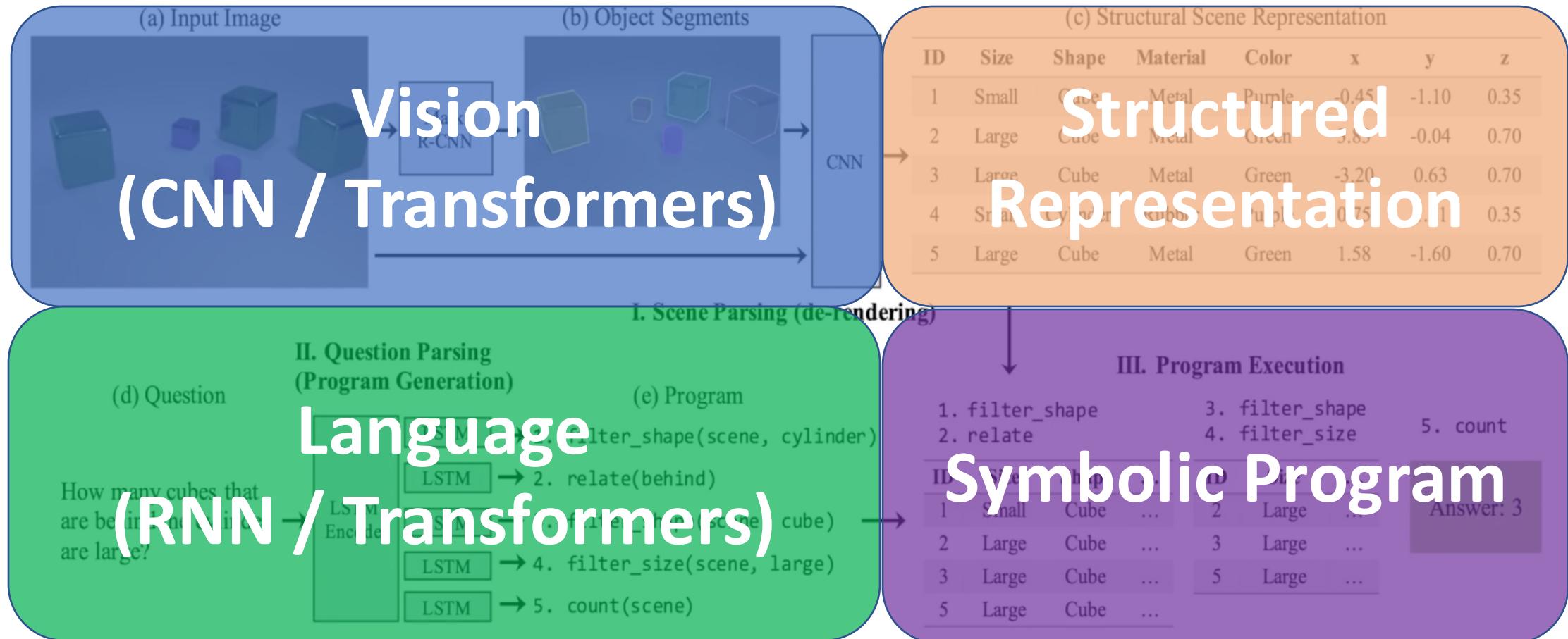
Question Understanding

Question: Are there an equal number of large things and metal spheres?



Logical Reasoning

Neuro-Symbolic AI Example: Visual Reasoning



Advantage 1: High Accuracy

Vision



Language

Q: What's the shape of the red object?

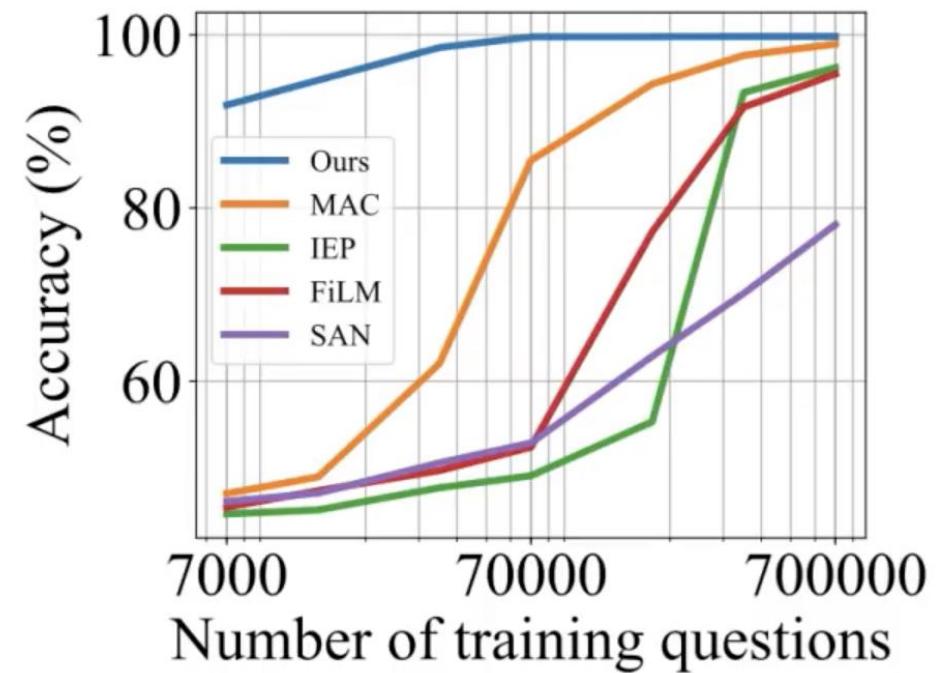
Method	Accuracy (%)
Human	92.6
RN	95.5
IEP	96.9
FiLM	97.6
MAC	98.9
TbD	99.1
NS-VQA (Ours)	99.8

Effectively perfect!

NS-VQA [Yi et al.]

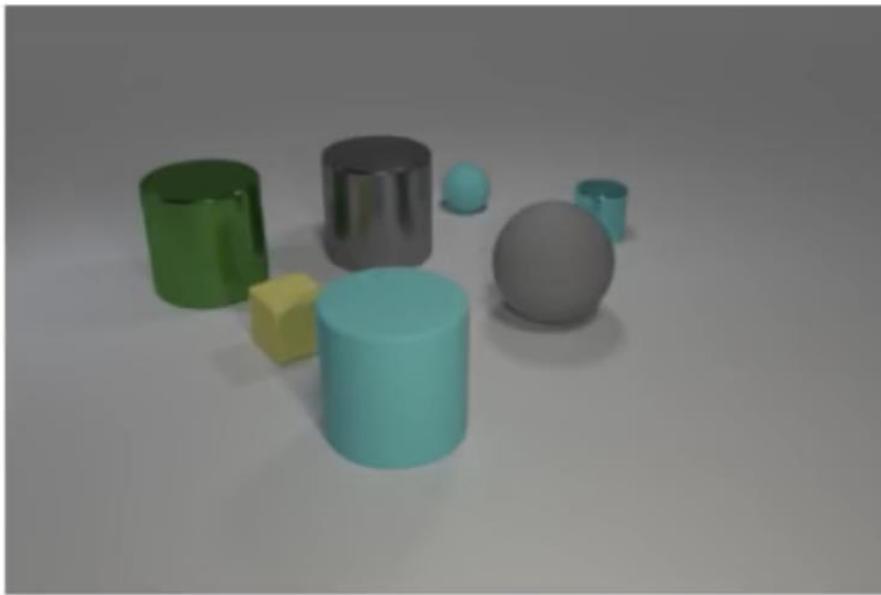
Advantage 2: Data Efficiency

High accuracy when trained with just 1% the of the data that other methods require



NS-VQA [Yi et al.]

Advantage 3: Transparency and Interpretability



Question: Are there more yellow matte things that are right of the gray ball than cyan metallic objects?

```
scene  
filter_cyan  
filter_metal  
count  
... (4 modules)  
scene  
filter_yellow  
filter_rubber  
count  
greater_than
```

Answer: no

NS-VQA [Yi et al.]

Other Examples

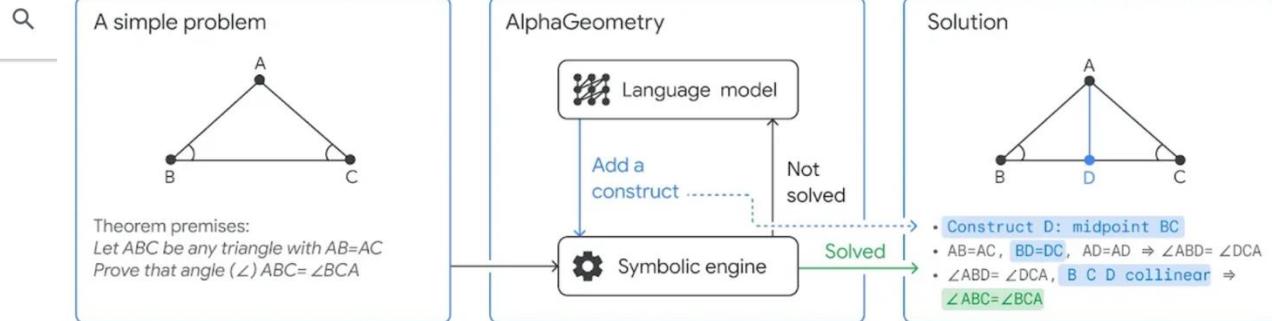
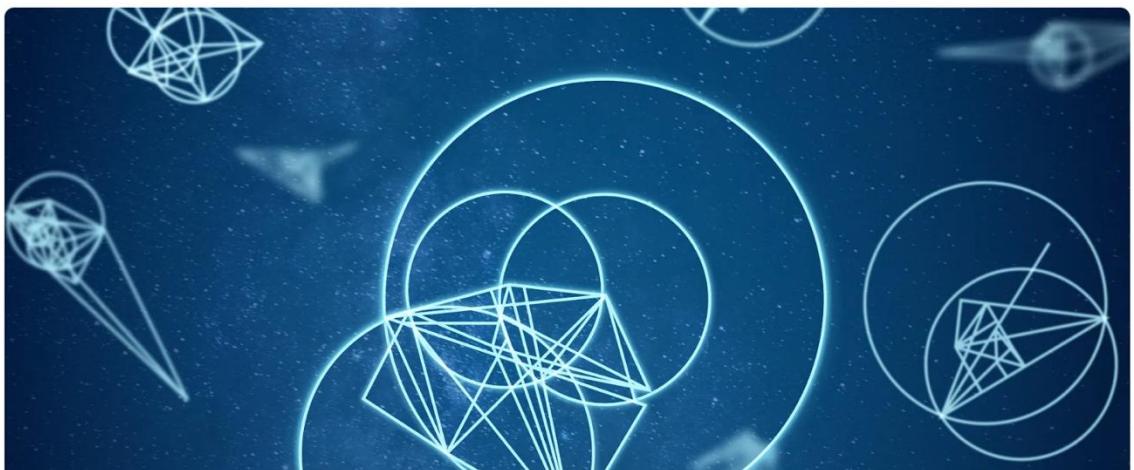
≡ Google DeepMind

AlphaGeometry: An Olympiad-level AI system for geometry

17 JANUARY 2024

Trieu Trinh and Thang Luong

Share



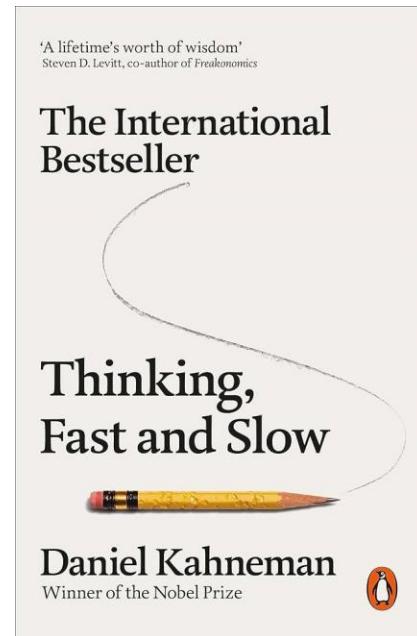
LLM: construct auxiliary points and lines
Symbolic: deductive reasoning

Eval on 30 Int. Math Olympics (IMO) problems:

- GPT-4: 0/30
- AlphaGeometry (Neuro-Symbolic): 25/30
- Human Gold Medalist: 26/30

Trinh et al, "Solving Olympiad Geometry without Human Demonstrations", Nature 2024

Relationship to Human Minds

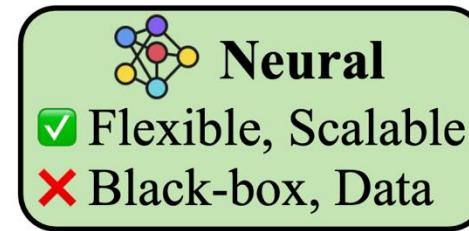
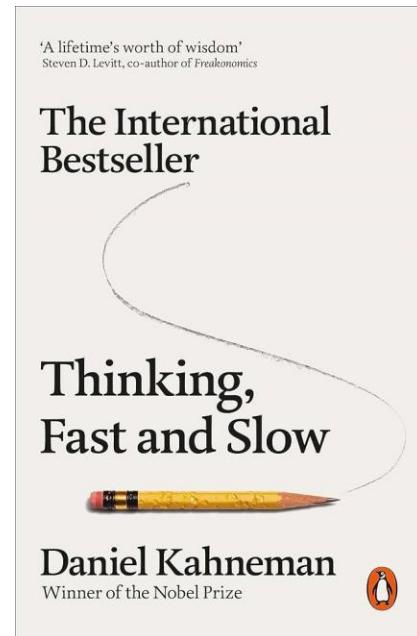


Daniel Kahneman
(1934-2024)

AlphaGeometry adopts a neuro-symbolic approach

AlphaGeometry is a neuro-symbolic system made up of a neural language model and a symbolic deduction engine, which work together to find proofs for complex geometry theorems. Akin to the idea of "[thinking, fast and slow](#)", one system provides fast, "intuitive" ideas, and the other, more deliberate, rational decision-making.

Relationship to Human Minds



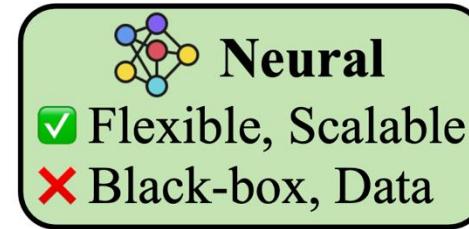
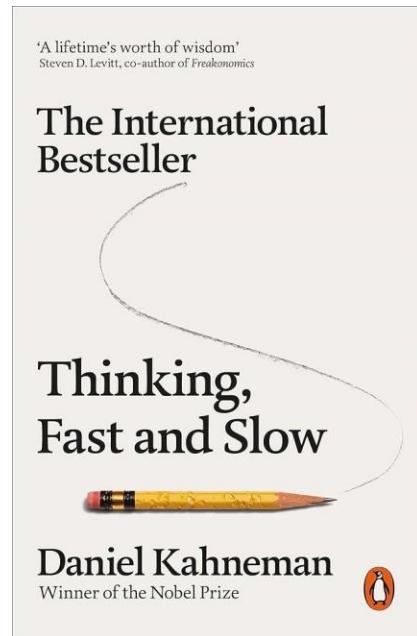
*System 1: thinking fast
(intuitive perception)*

Daniel Kahneman
(1934-2024)

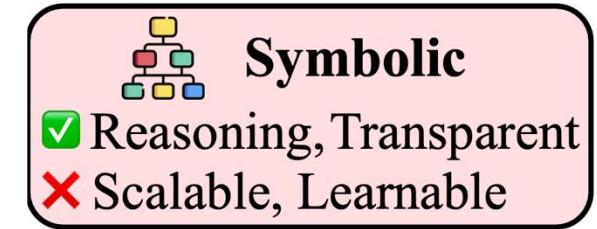
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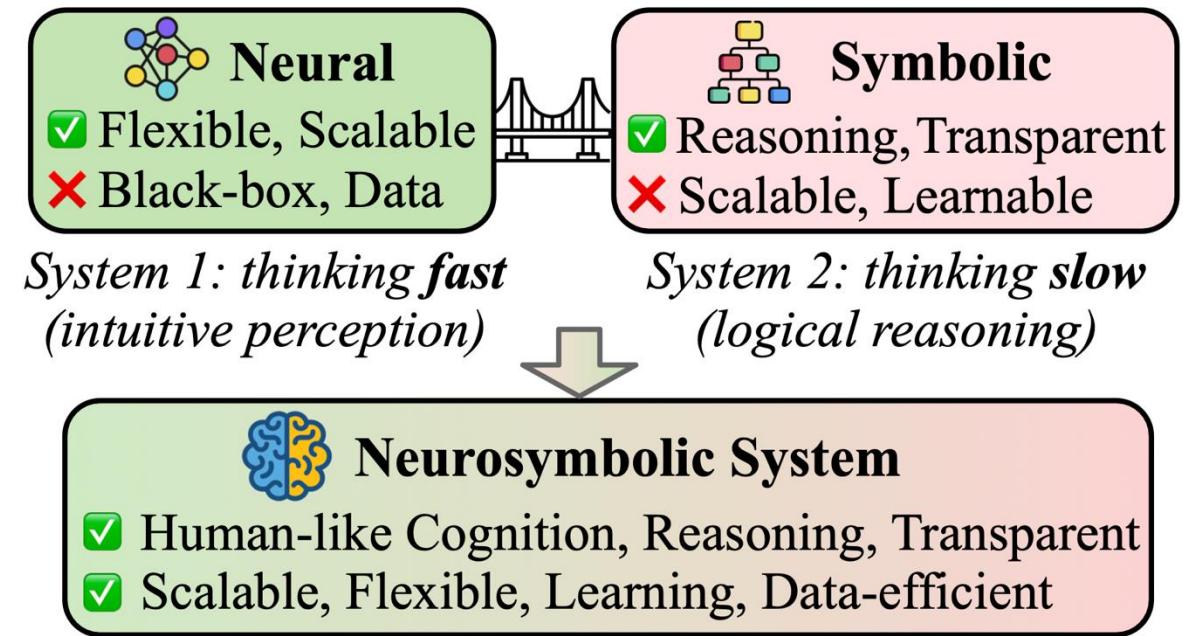
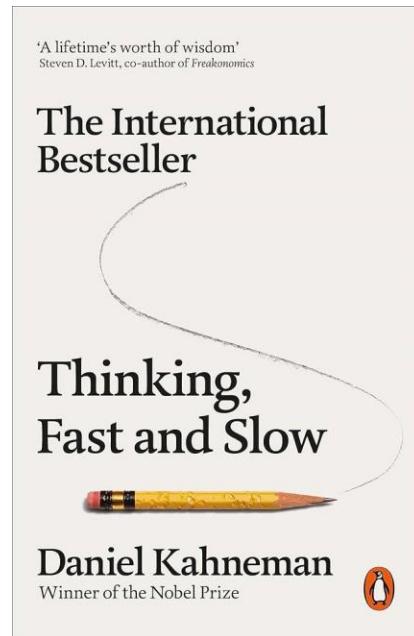


*System 2: thinking slow
(logical reasoning)*

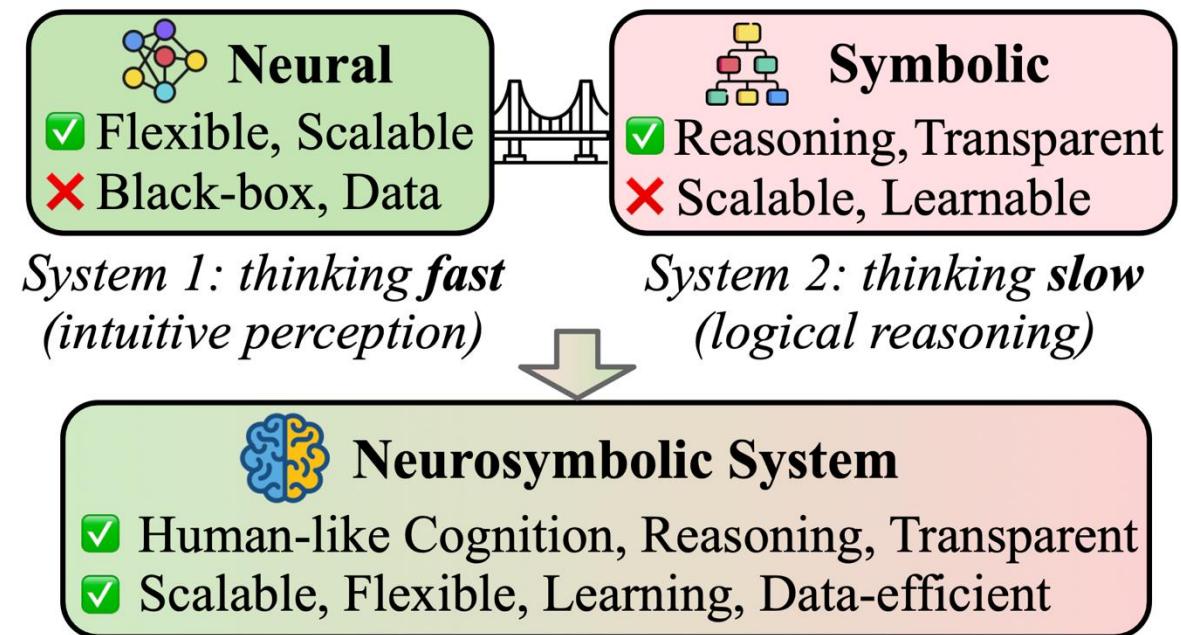
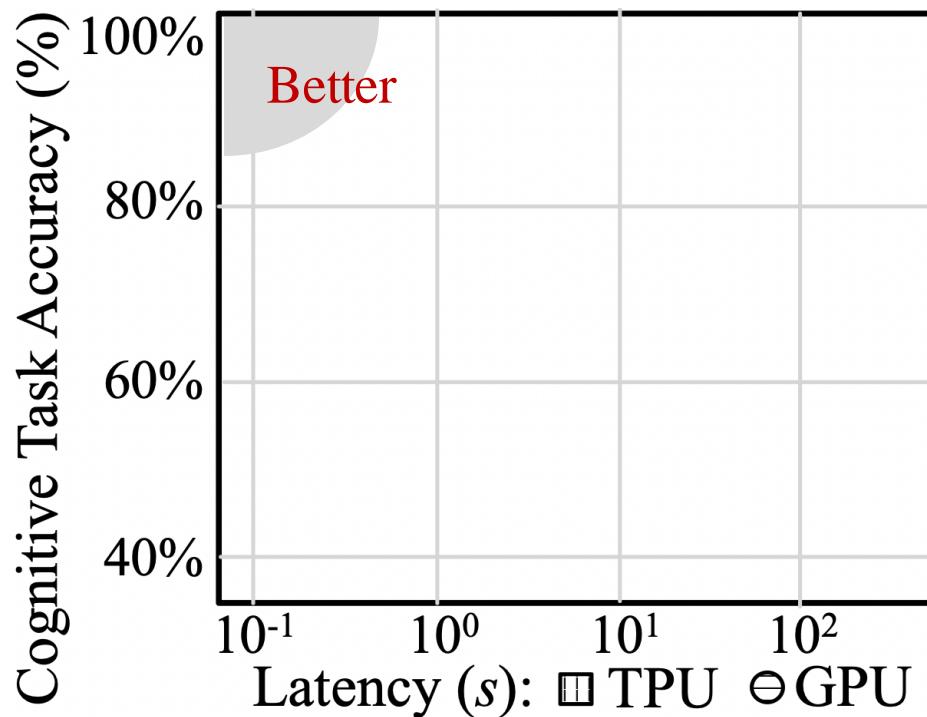
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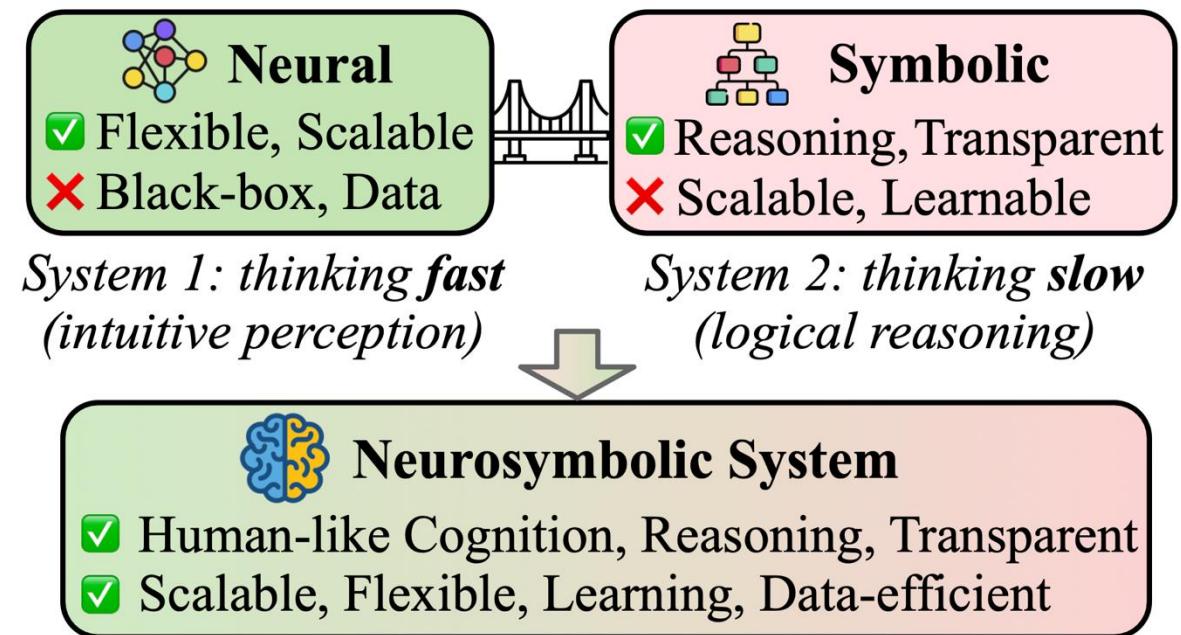
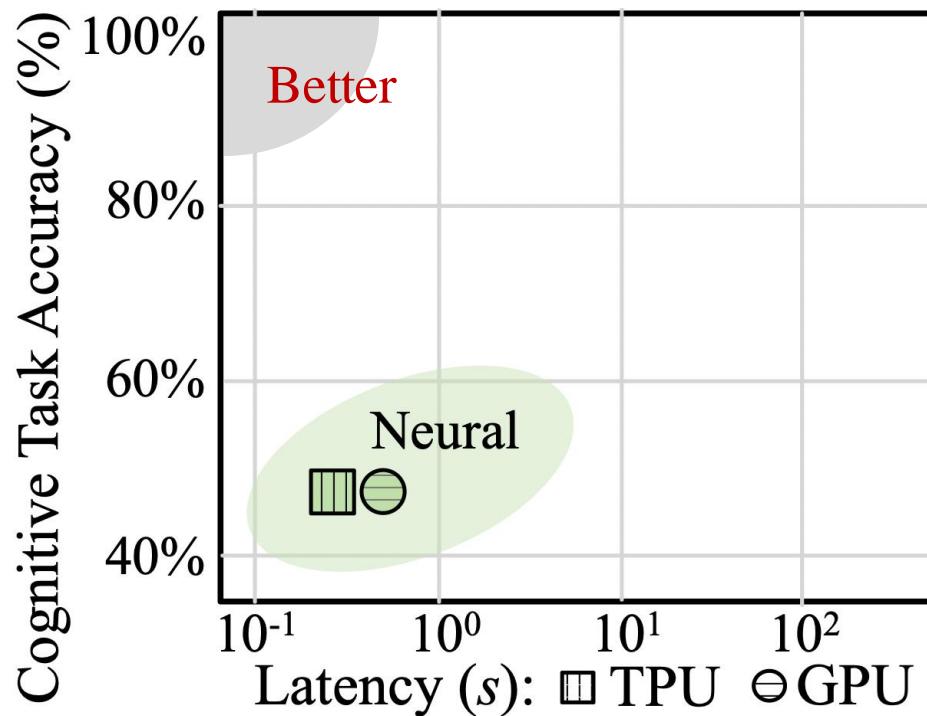
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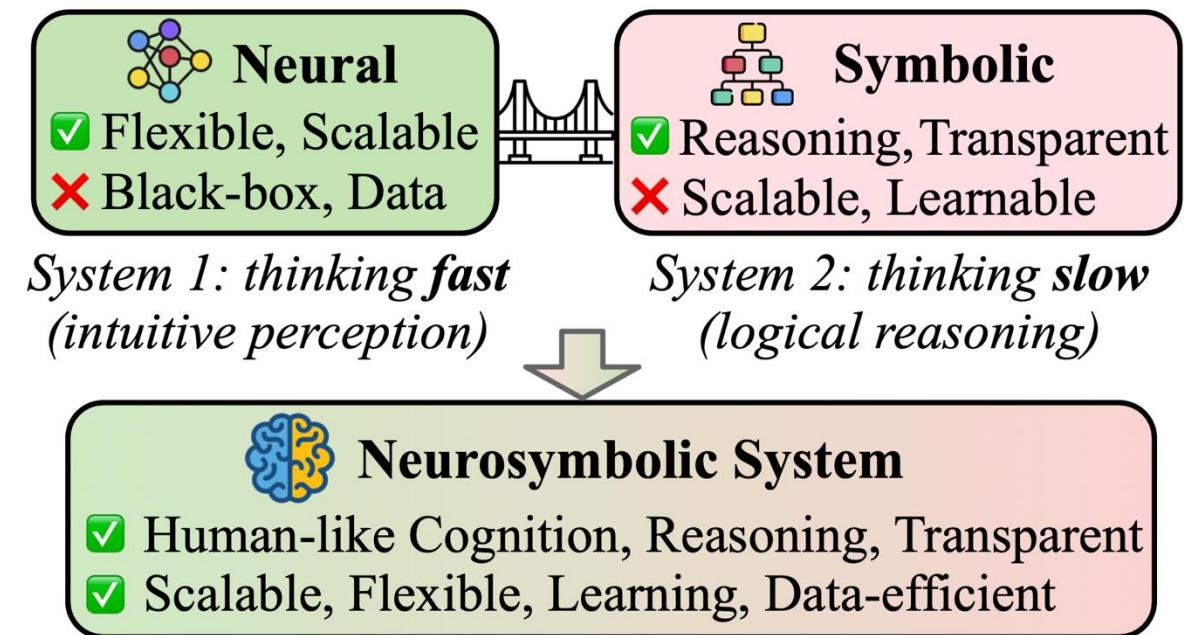
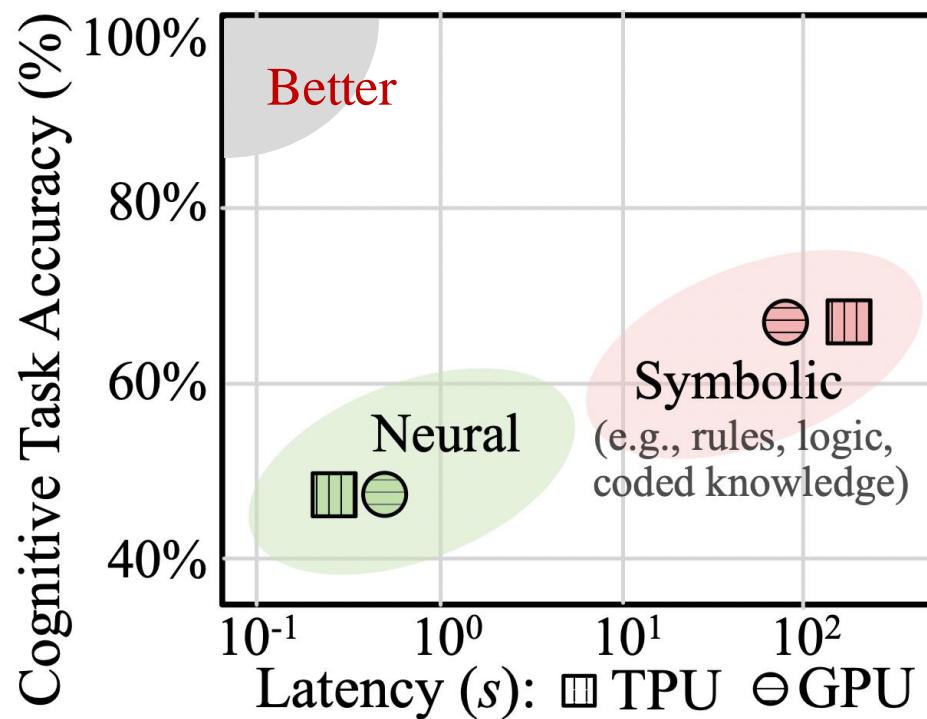
However.. From Computing Perspective



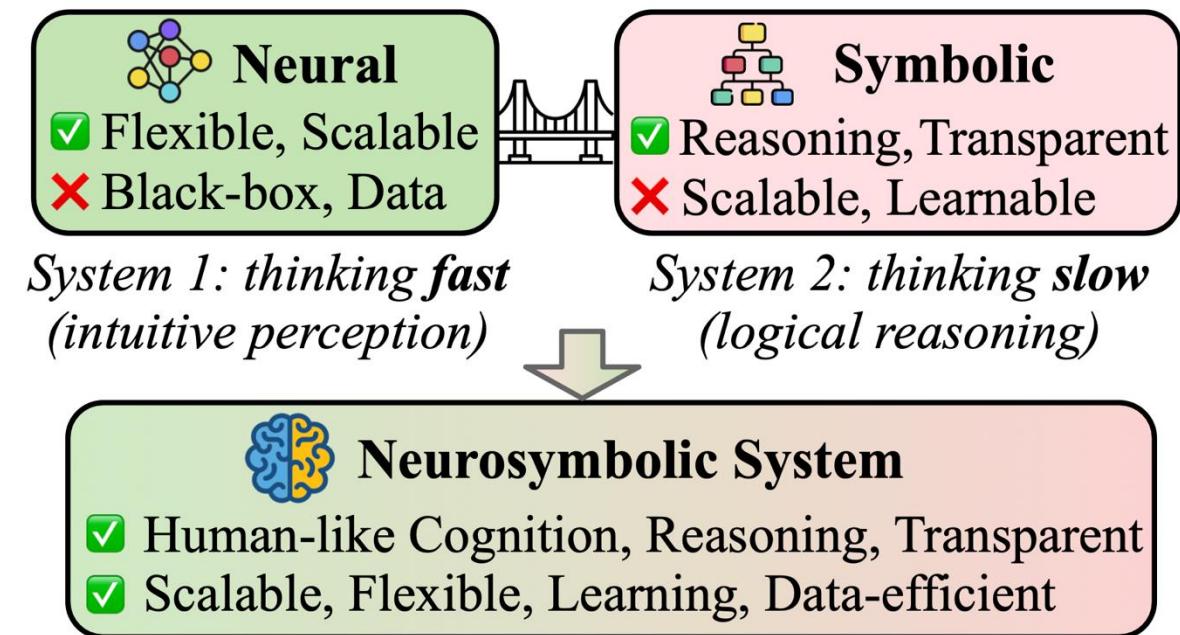
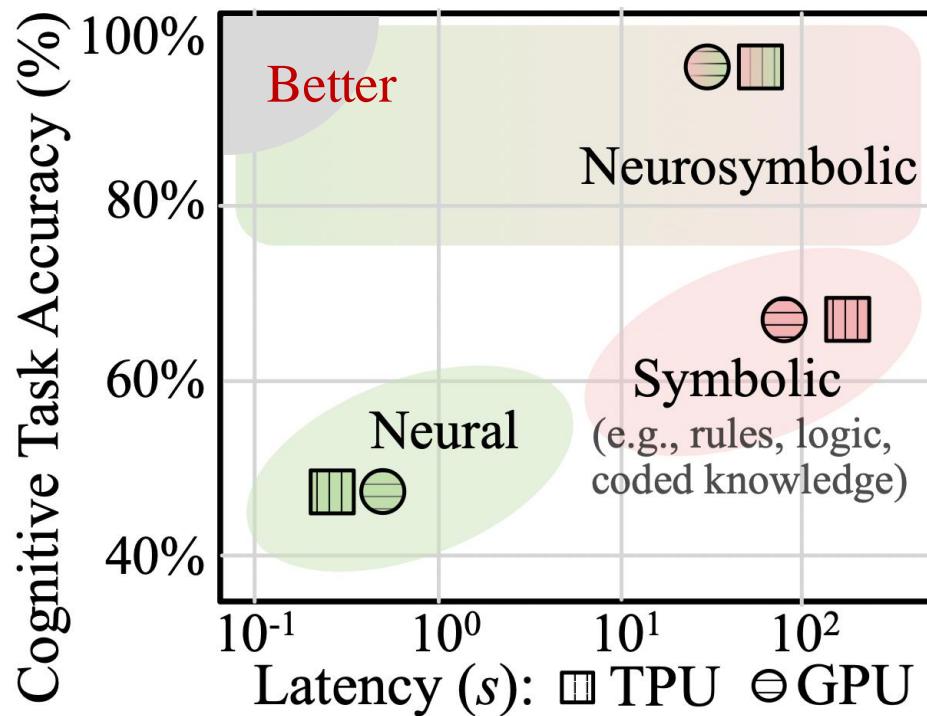
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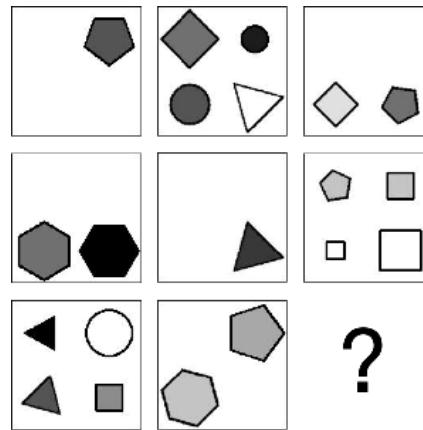
However.. From Computing Perspective



However... From Computing Perspective



These neuro-symbolic approaches are typically very slow

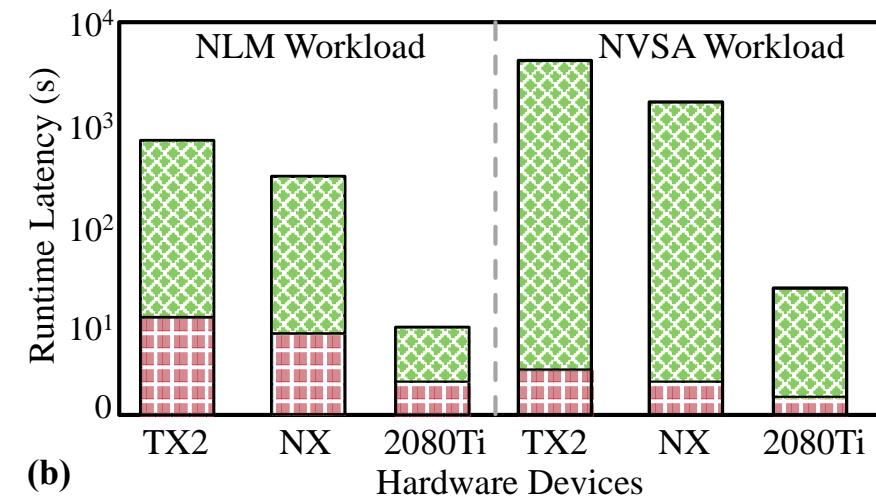
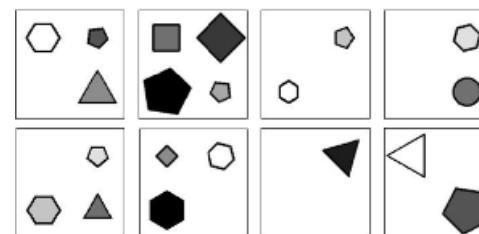


Spatial-Temporal Abstract Reasoning

ResNet accuracy: 53%

GPT-4 accuracy: 84%

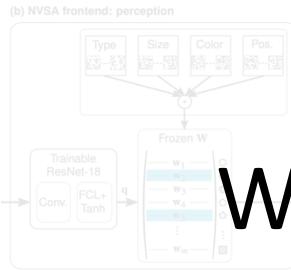
Neuro-Symbolic accuracy: 98%



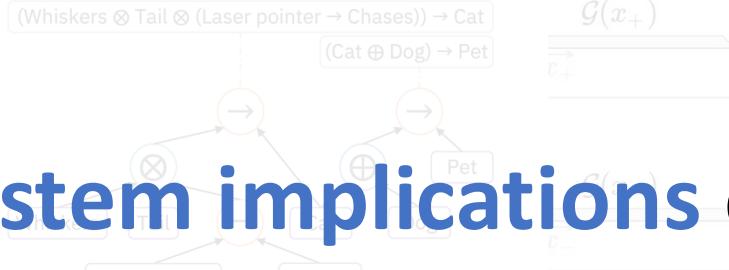
The neuro-symbolic approach takes ~100s even on desktop GPU, ~700s on Jetson TX2

Lots of Neuro-Symbolic Algorithms

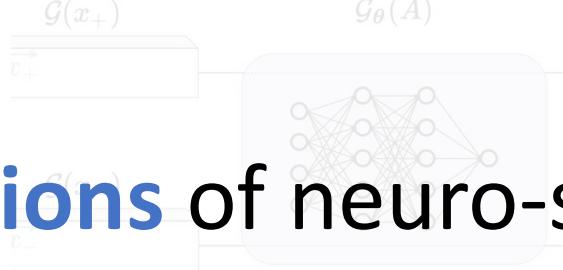
Research Question:



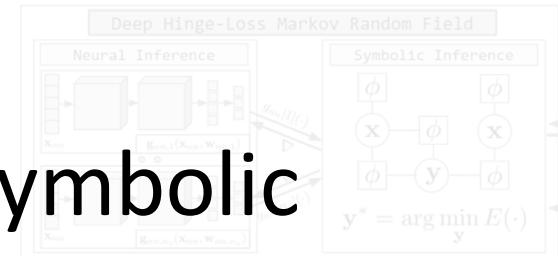
Neuro-Vector-Symbolic Arch^[1]



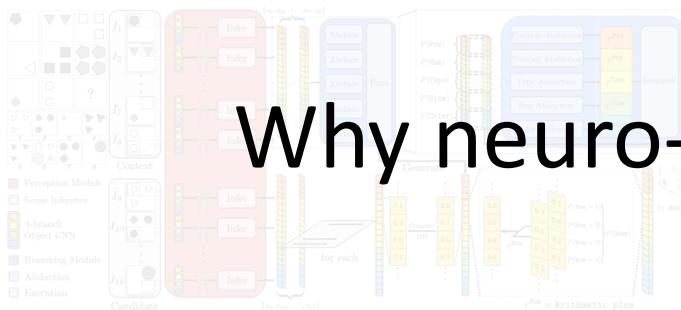
Logical Neural Network^[2]



Logical Tensor Network^[3]



Neural Probabilistic Soft Logic^[4]



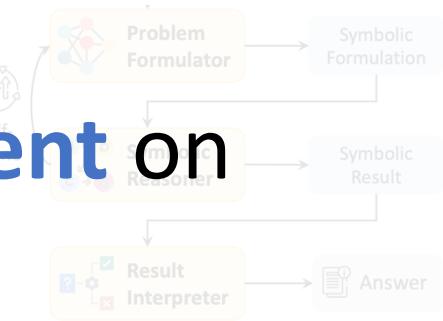
Probabilistic Abduction^[5]



Image Translation via VSA^[6]



AlphaGeometry^[7]



Logic-LM^[8]

[1] Hersche et al, Nature MI 2023; [2] Hoang et al, AAAI 2022; [3] Badreddine et al, AI 2022; [4] Pryor et al, IJCAI 2023

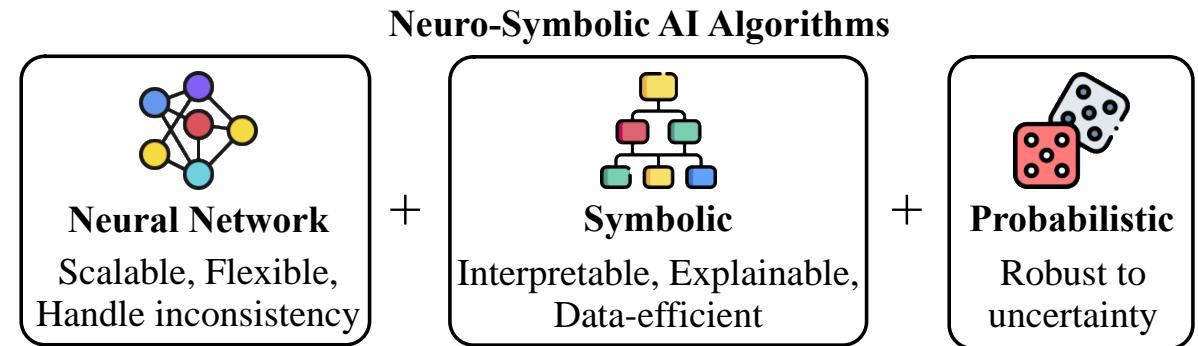
[5] Zhang et al, CVPR 2021; [6] Theiss et al, ECCV 2023; [7] Trinh et al, Nature 2024; [8] Pan el al, EMNLP 2023

Outline

- Neuro-symbolic AI 101
- **Neuro-symbolic AI workload characterization**
- Neuro-symbolic AI hardware architecture
- Final project: neuro-symbolic kernel optimization

Neuro-Symbolic AI Workload and Characterization

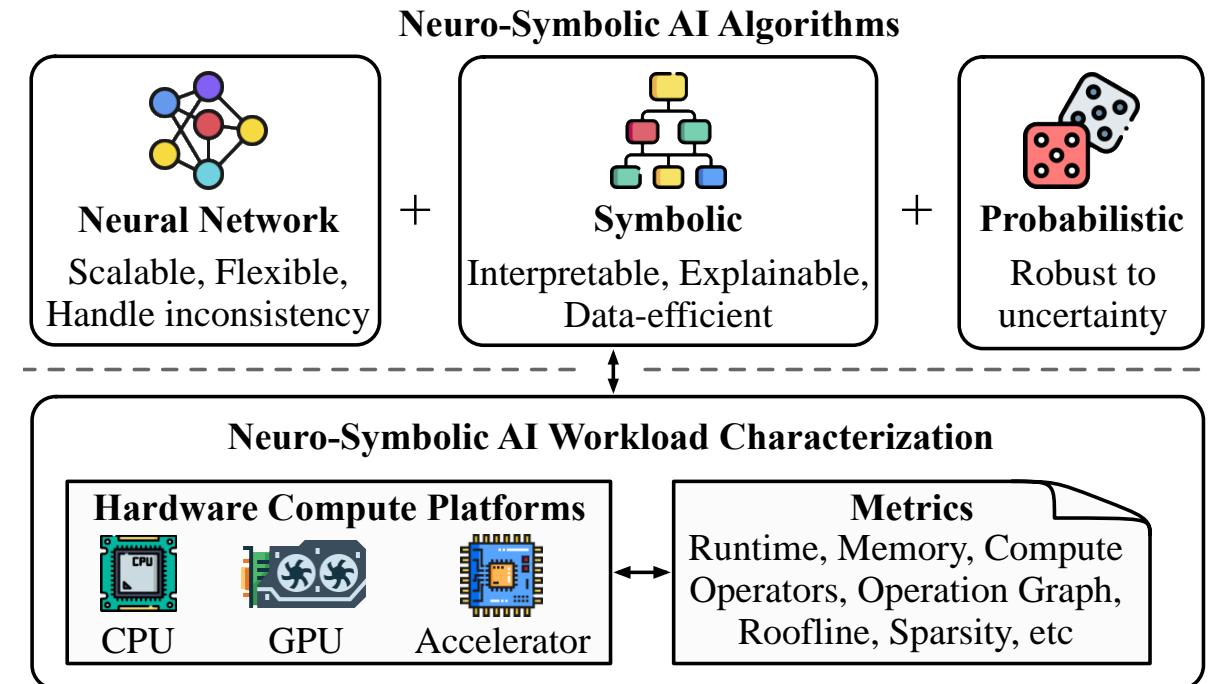
Categorize Neuro-Symbolic Algorithms



Neuro-Symbolic AI Workload and Characterization

Categorize Neuro-Symbolic Algorithms

Understand Computational Behavior of Neuro-Symbolic Workloads

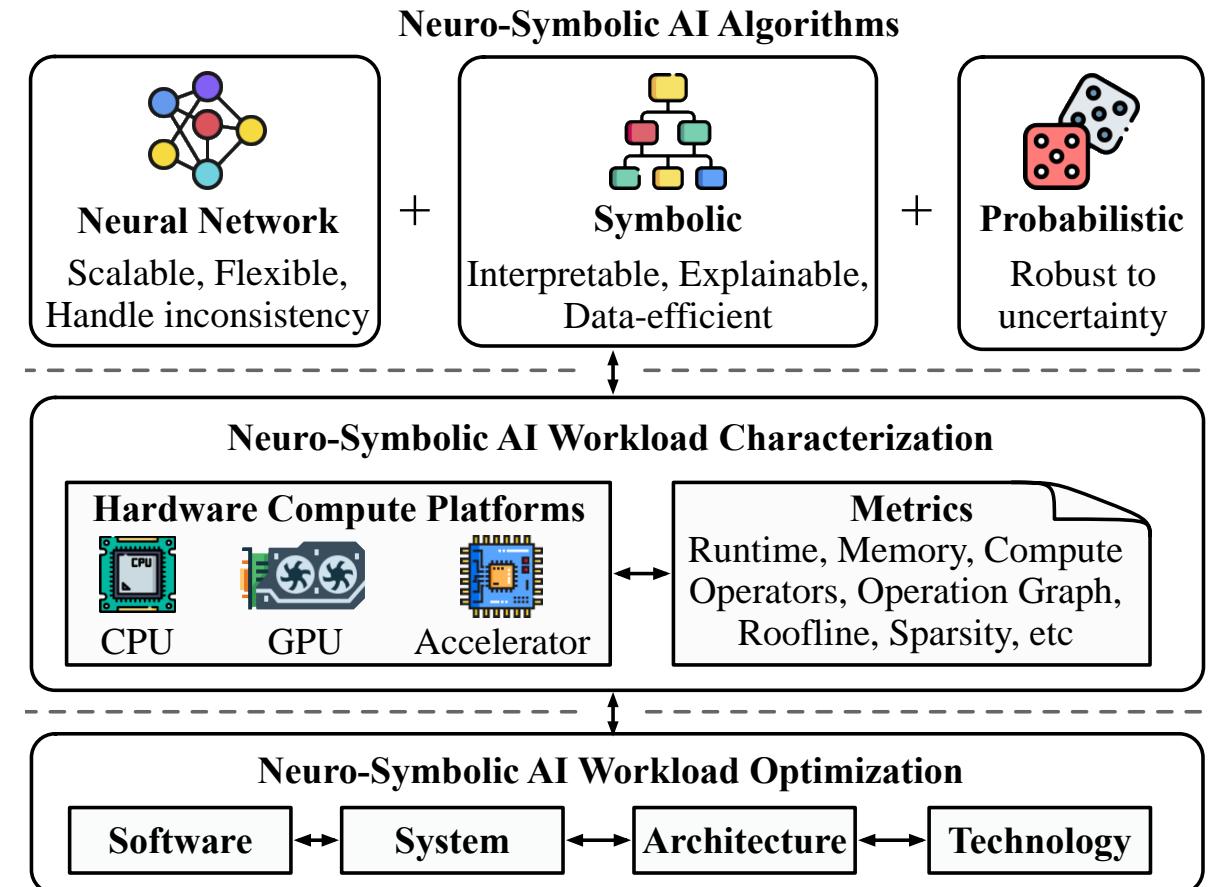


Neuro-Symbolic AI Workload and Characterization

Categorize Neuro-Symbolic Algorithms

Understand Computational Behavior of Neuro-Symbolic Workloads

Identify Co-Design Opportunities

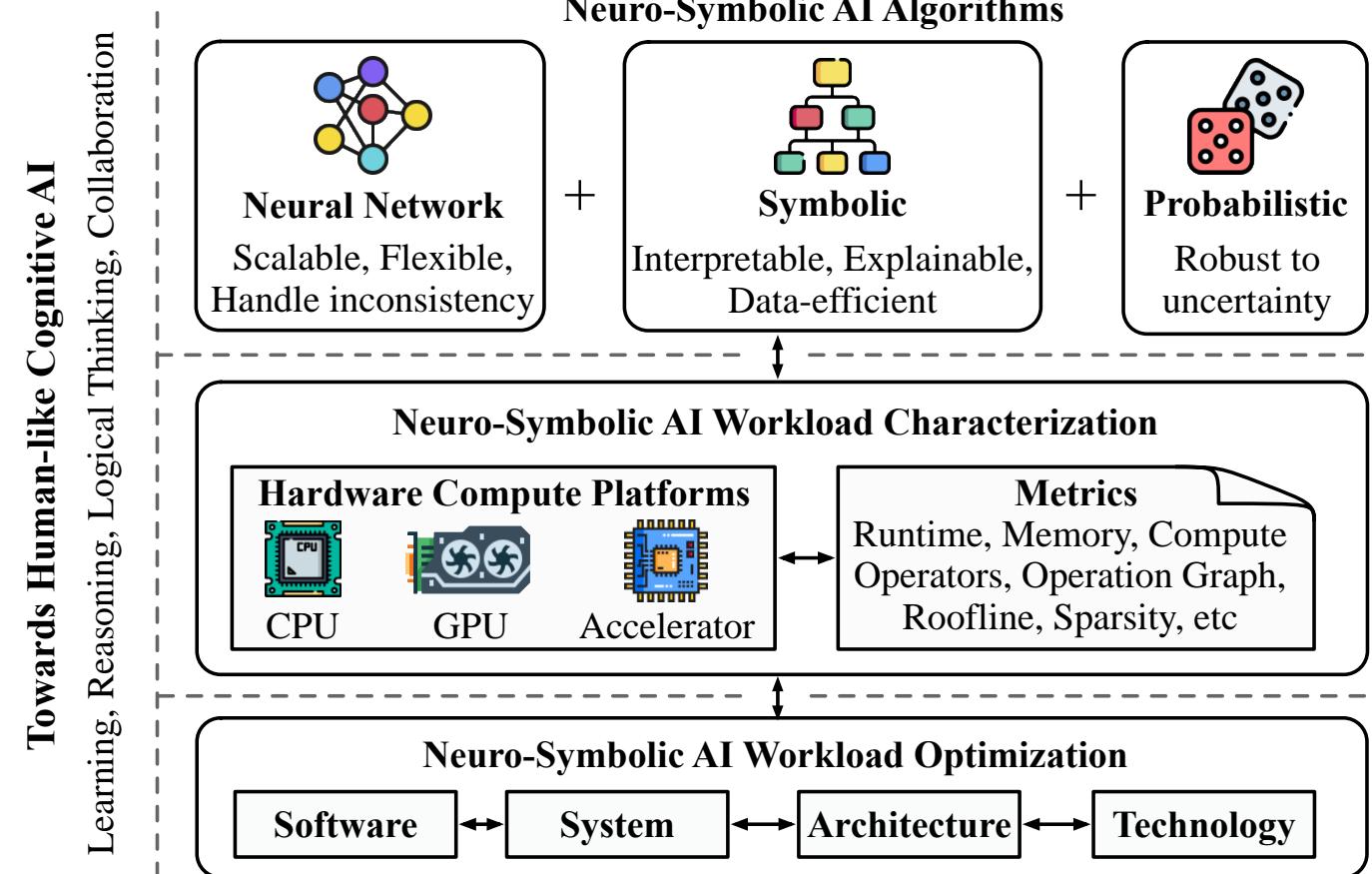


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Understand Computational Behavior of Neuro-Symbolic Workloads

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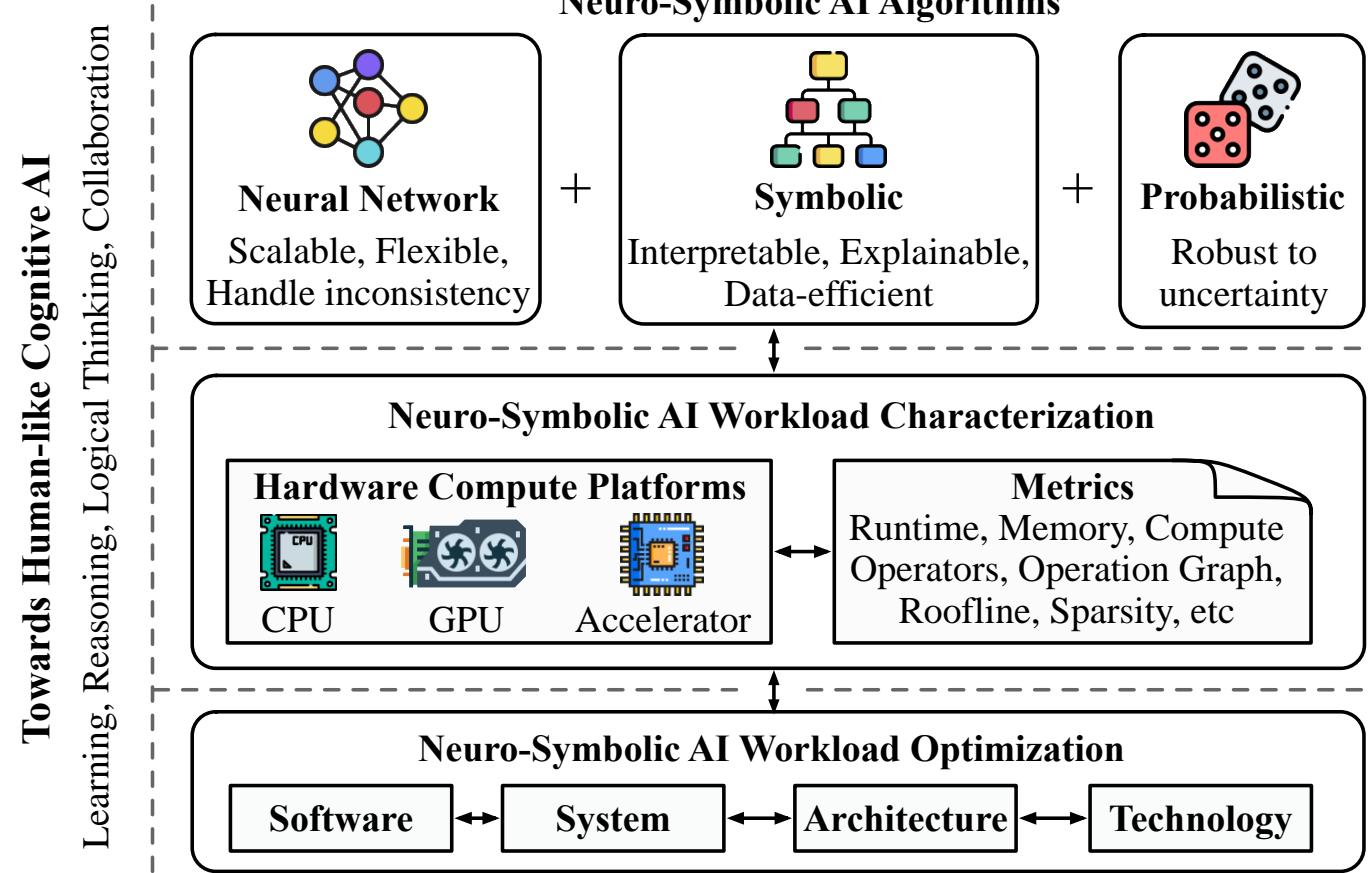


Neuro-Symbolic AI Workload and Characterization

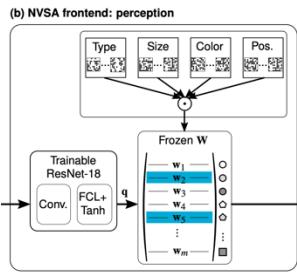
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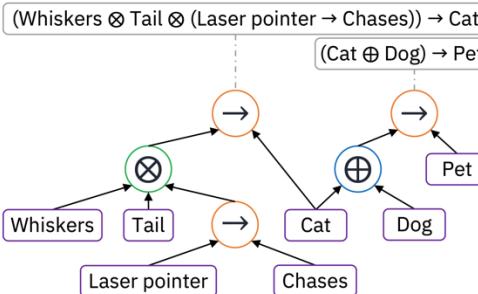
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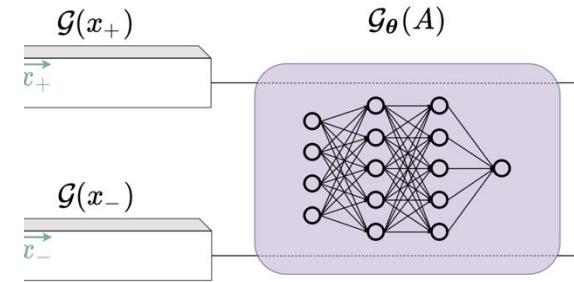
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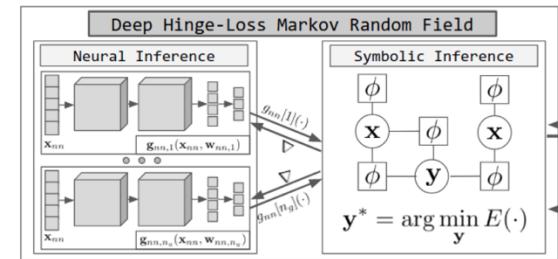
Neuro-Vector-Symbolic Arch



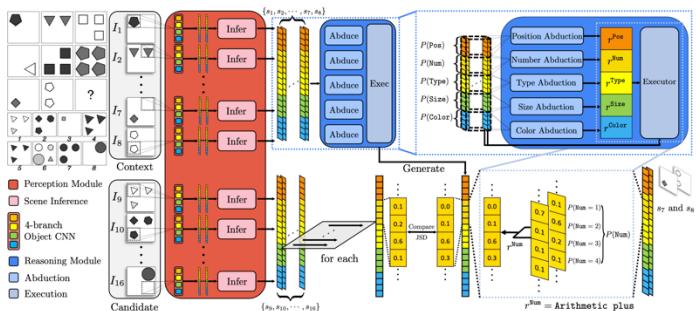
Logical Neural Network



Logical Tensor Network



Neural Probabilistic Soft Logic



Probabilistic Abduction

Neuro

MLP, ConvNet, Transformer, etc

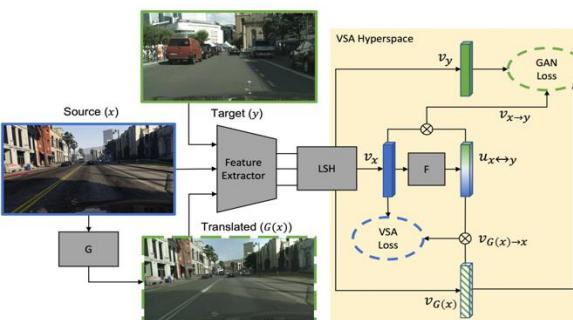
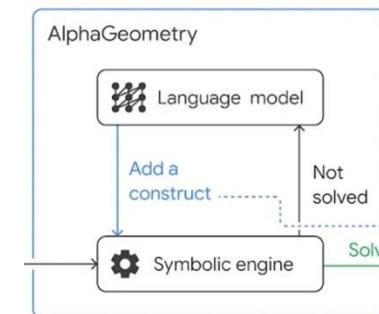
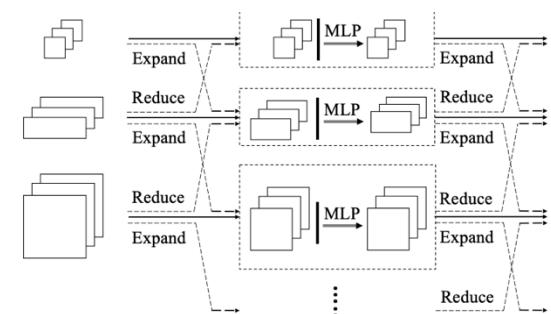


Image Translation via VSA



AlphaGeometry

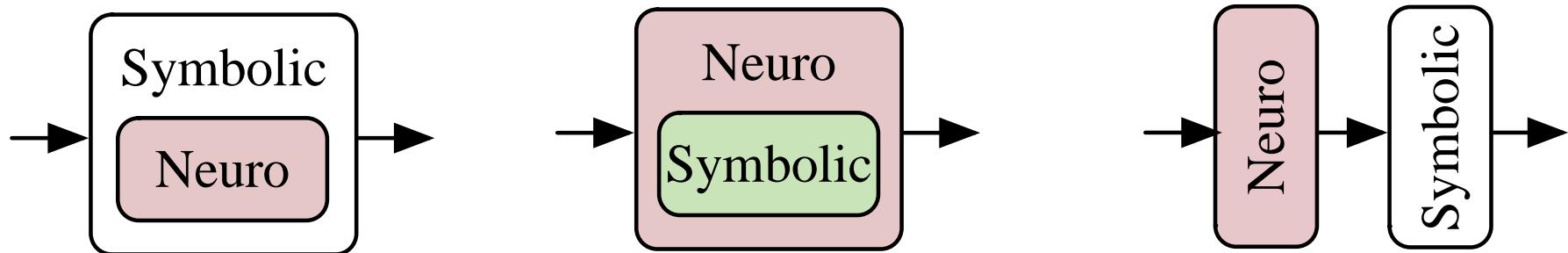


Neural Logical Machine

Guest Lecture @ ECE8893

Zishen Wan | School of ECE | Georgia Institute of Technology

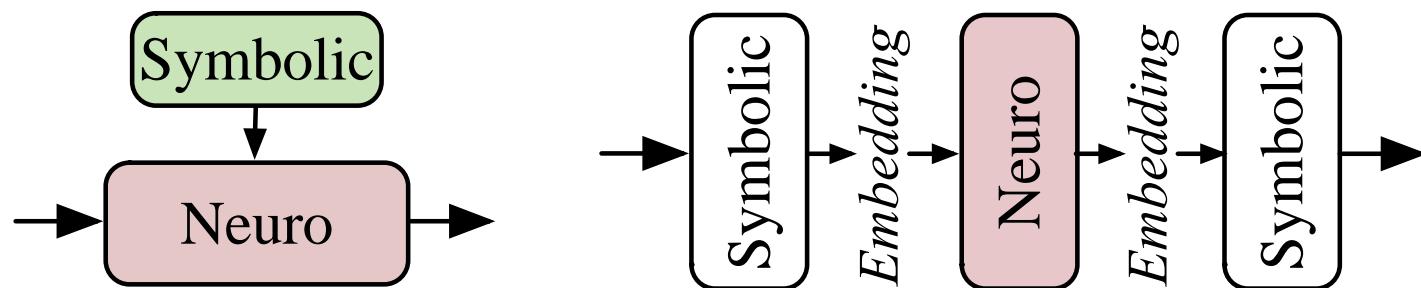
Neuro-Symbolic AI Workload Category



Symbolic [Neuro]

Neuro [Symbolic]

Neuro | Symbolic

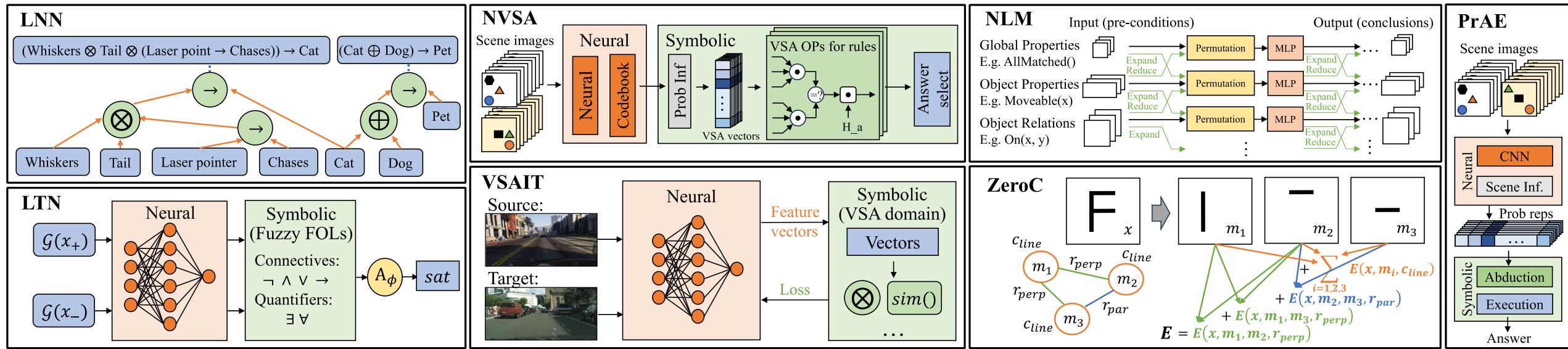


Neuro:Symbolic->Neuro

Neuro_{Symbolic}

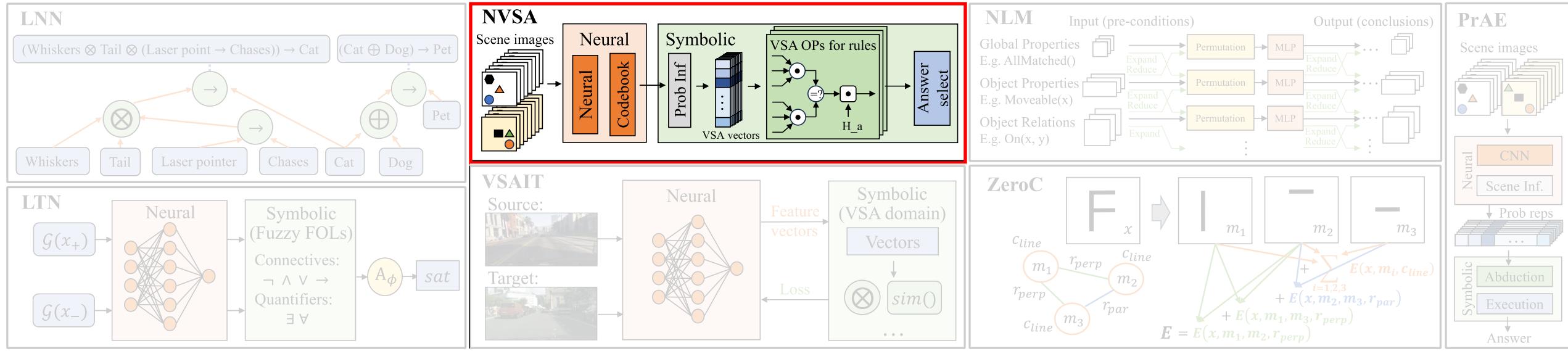
Inspired by Henry Kautz's terminology

Selected Neuro-Symbolic Workloads



Representative Neuro-Symbolic AI Workloads	Logic Neural Network [30]	Logic Tensor Network [34]	Neuro-Vector-Symbolic Architecture [4]	Vector Symbolic Architecture Image2Image Translation [7]	Neural Logic Machine [38]	Zero-shot Concept Recognition and Acquisition [37]	Probabilistic Abduction and Execution [23]
Abbreviation	LNN	LTN	NVSA	VSAIT	NLM	ZeroC	PrAE
Neuro-Symbolic Category	Neuro:Symbolic→Neuro	Neuro Symbolic	Neuro Symbolic	Neuro Symbolic	Neuro[Symbolic]	Neuro[Symbolic]	Neuro Symbolic
Learning Approach	Supervised	Supervised/Unsupervised	Supervised/Unsupervised	Supervised	Supervised/Unsupervised	Supervised	Supervised/Unsupervised
Deployment Scenario	Application	Learning and reasoning, Full theorem prover	Querying, learning, reasoning (relational and embedding learning, query answering)	Fluid intelligence, Abstract reasoning	Unpaired image-to-image translation	Relational reasoning, Decision making	Cross-domain classification and detection, Concept acquisition
	Advantage vs. Neural Model	Higher interoperability, resilience to incomplete knowledge, generalization	Higher data efficiency, comprehensibility, out-of-distribution generalization	Higher joint representations efficiency, abstract reasoning capability, transparency	Address semantic flipping and hallucinations issue in unpaired image translation tasks	Higher generalization, logic reasoning, deduction, explainability capability	Higher generalization, concept acquisition and recognition, compositionality capability
	Dataset	LUBM benchmark [40], TPTP benchmark [41]	UCI [42], Leptograpsus crabs [43], DeepProbLog [44]	RAVEN [21], I-RAVEN [22], PGM [45]	GTA [47], Cityscapes [48], Google Maps dataset [49]	Family graph reasoning, sorting, path finding [46]	Abstraction reasoning [50], Hierarchical-concept corpus [51]
Computation Pattern	Datatype	FP32	FP32	FP32	FP32	FP32	INT64
	Neuro	Graph	MLP	ConvNet	ConvNet	Sequential tensor	Energy-based network
	Symbolic	FOL/Logical operation	FOL/Logical operation	VSA/Vector operation	VSA/Vector operation	FOL/Logical operation	Graph, vector operation

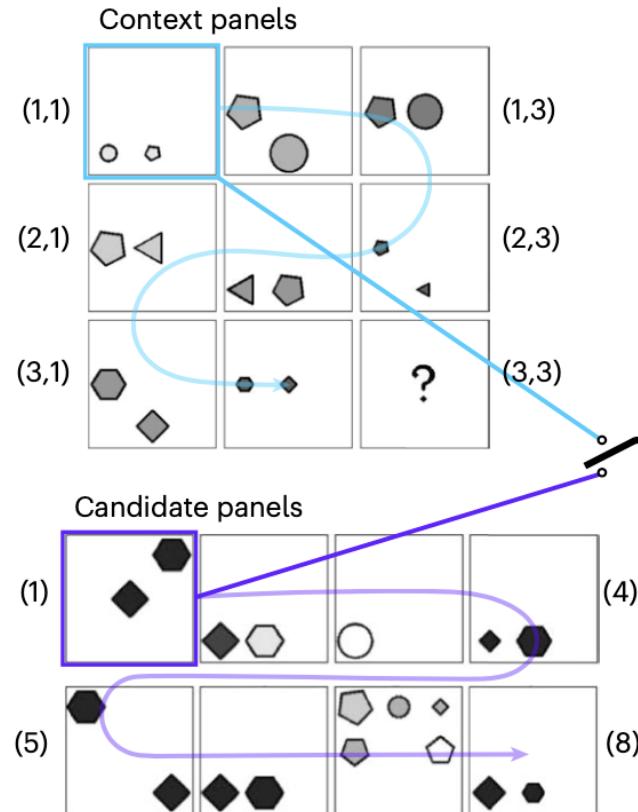
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	Advantage vs. Neural Model	Higher interoperability, resilience to incomplete knowledge, generalization	Higher joint representations efficiency, abstract reasoning capability, transparency	Address semantic flipping and hallucinations issue in unpaired image translation tasks	Higher generalization, logic reasoning, deduction, explainability capability	Higher generalization, concept acquisition and recognition, compositionality capability	Higher generalization, transparency, interpretability, and robustness
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	Neuro	Graph	ConvNet	ConvNet	Sequential tensor	Energy-based network	ConvNet
	Symbolic	FOL/Logical operation	VSA/Vector operation	VSA/Vector operation	FOL/Logical operation	Graph, vector operation	VSA/Vector operation

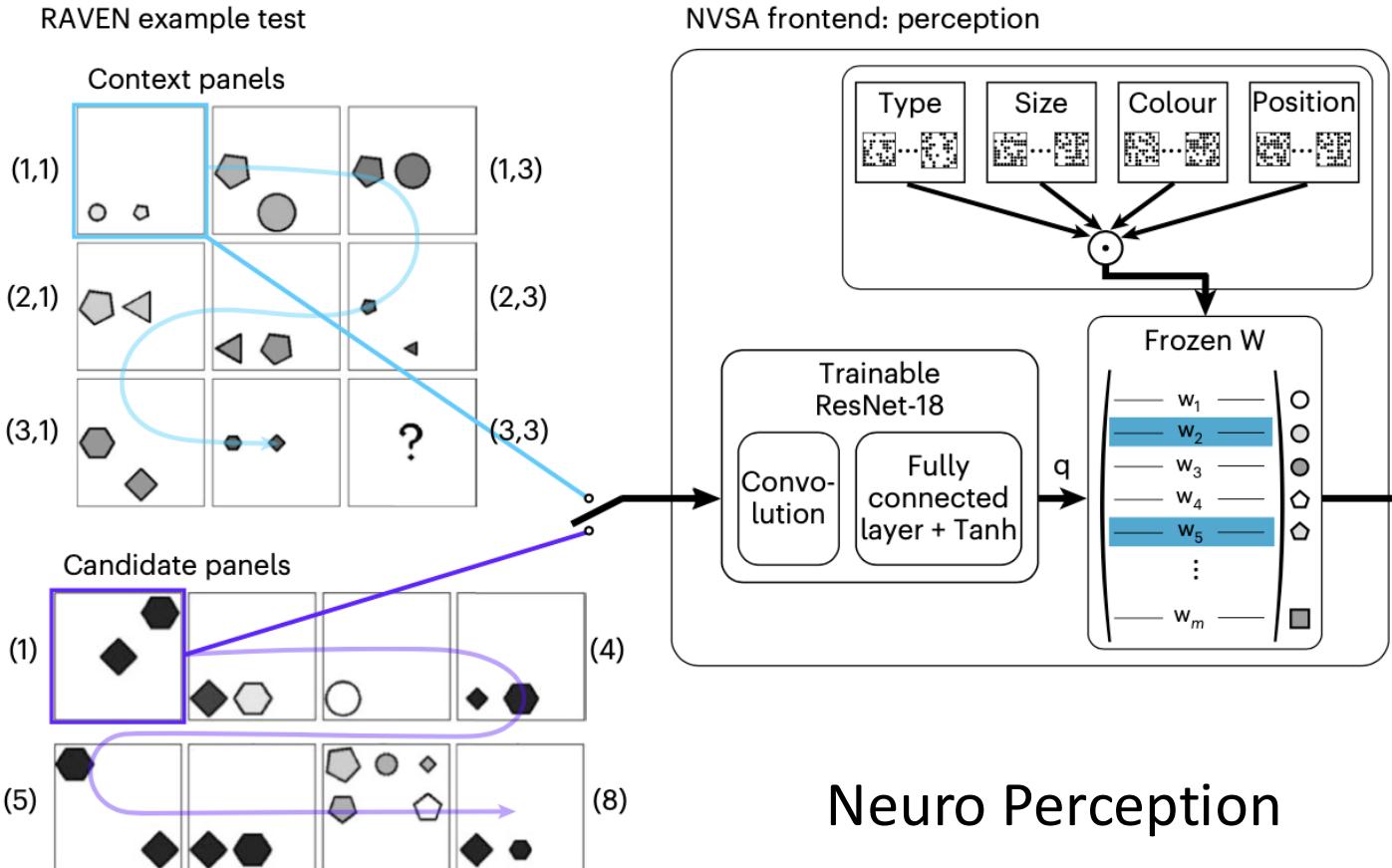
Example: Neuro-Vector-Symbolic Architecture

RAVEN example test



"A neuro-vector-symbolic architecture for solving Raven's progressive matrices". In Nature Machine Intelligence, 2023

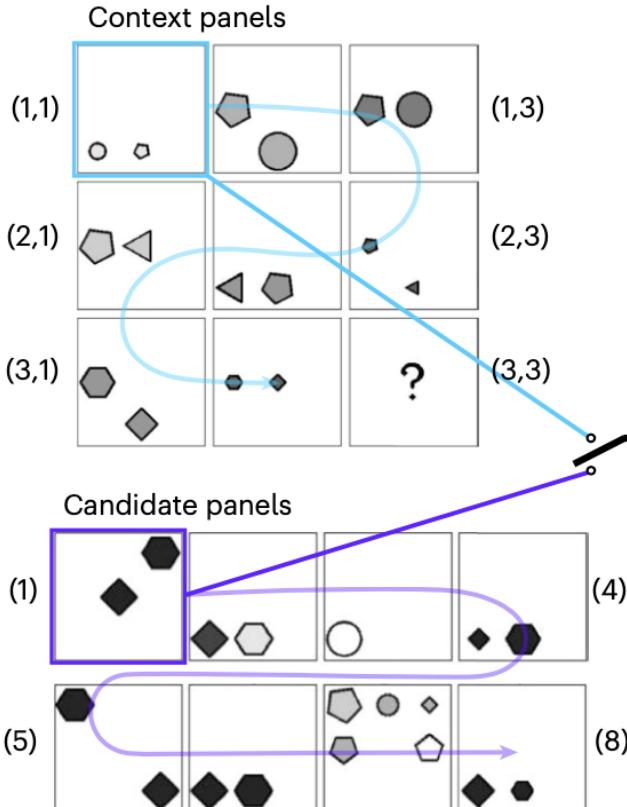
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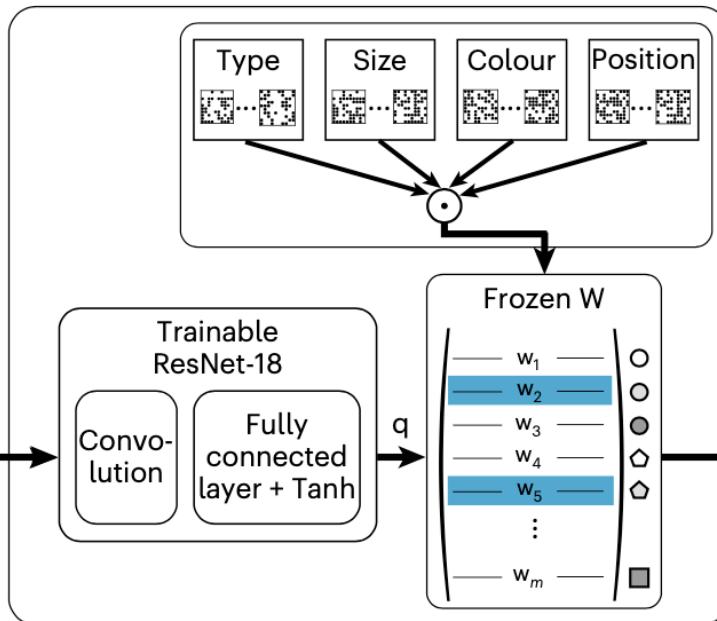
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Example: Neuro-Vector-Symbolic Architecture

RAVEN example test

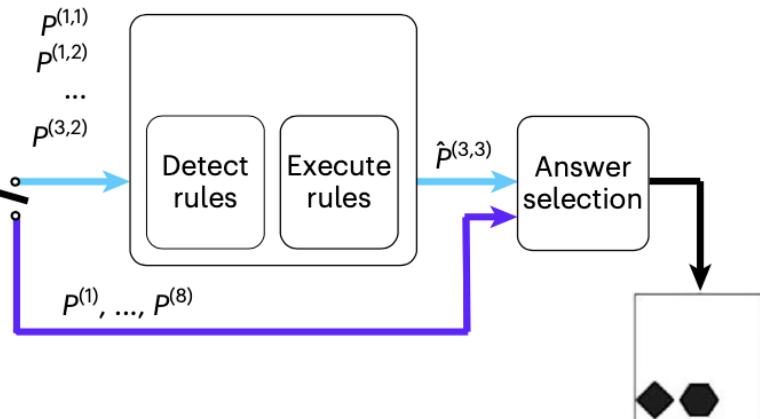


NVSA frontend: perception



Neuro Perception

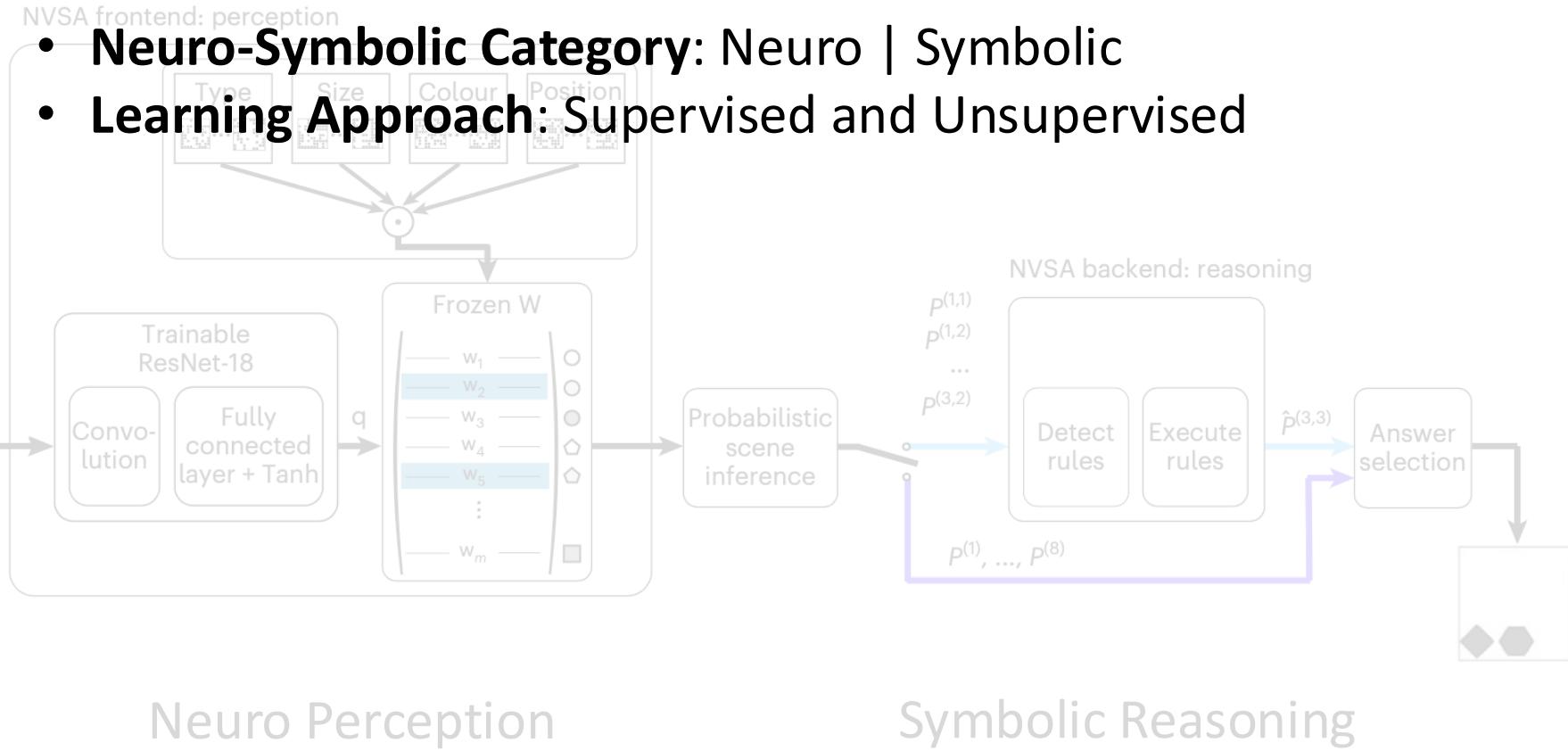
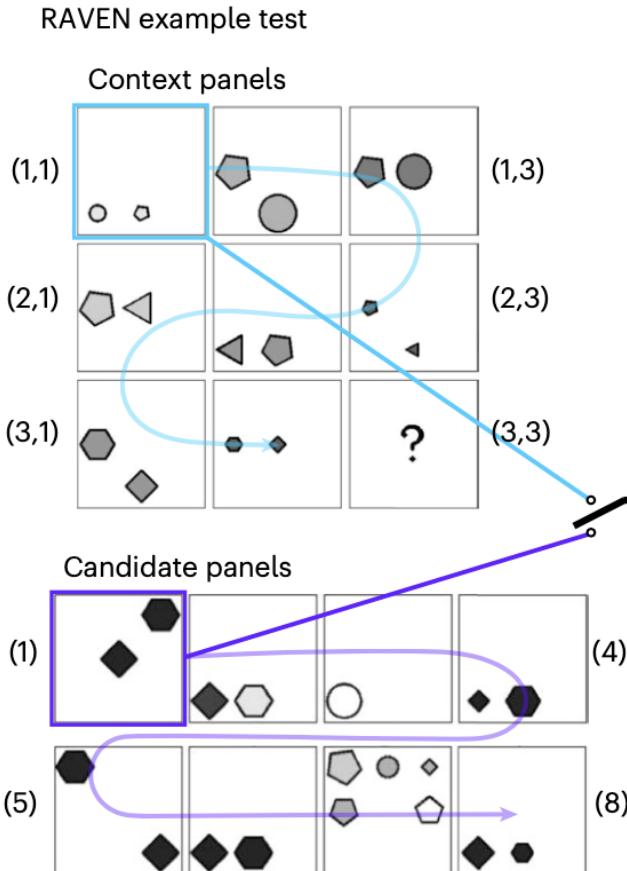
NVSA backend: reasoning



Symbolic Reasoning

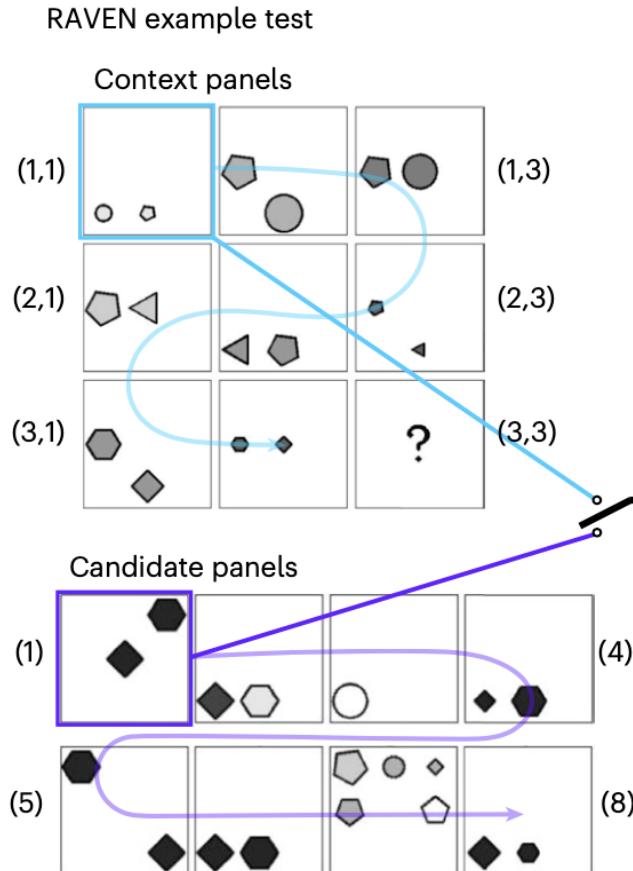
"A neuro-vector-symbolic architecture for solving Raven's progressive matrices". In Nature Machine Intelligence, 2023

Example: Neuro-Vector-Symbolic Architecture



"A neuro-vector-symbolic architecture for solving Raven's progressive matrices". In Nature Machine Intelligence, 2023

Example: Neuro-Vector-Symbolic Architecture

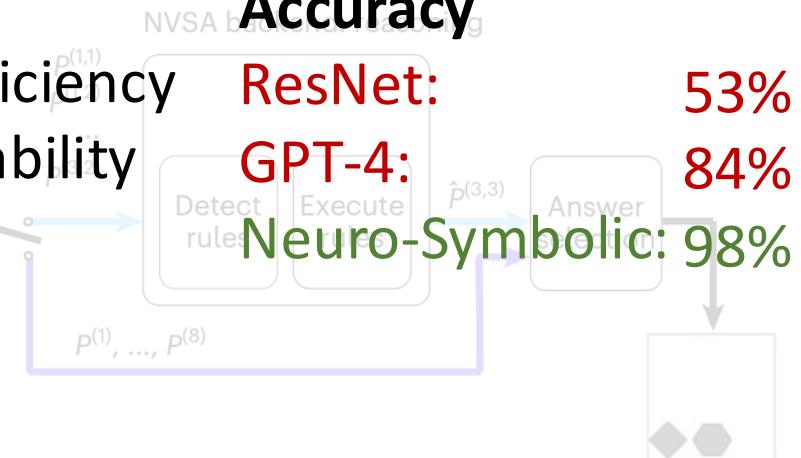


NVSA frontend: perception

- **Neuro-Symbolic Category:** Neuro | Symbolic
- **Learning Approach:** Supervised and Unsupervised
- **Application:** Fluid Intelligence, Abstract reasoning
- **Advantages over Neural Model:**
 - Higher joint representation efficiency
 - Higher abstract reasoning capability
 - Higher transparency
- **Dataset:** RAVEN, I-RAVEN, PGM

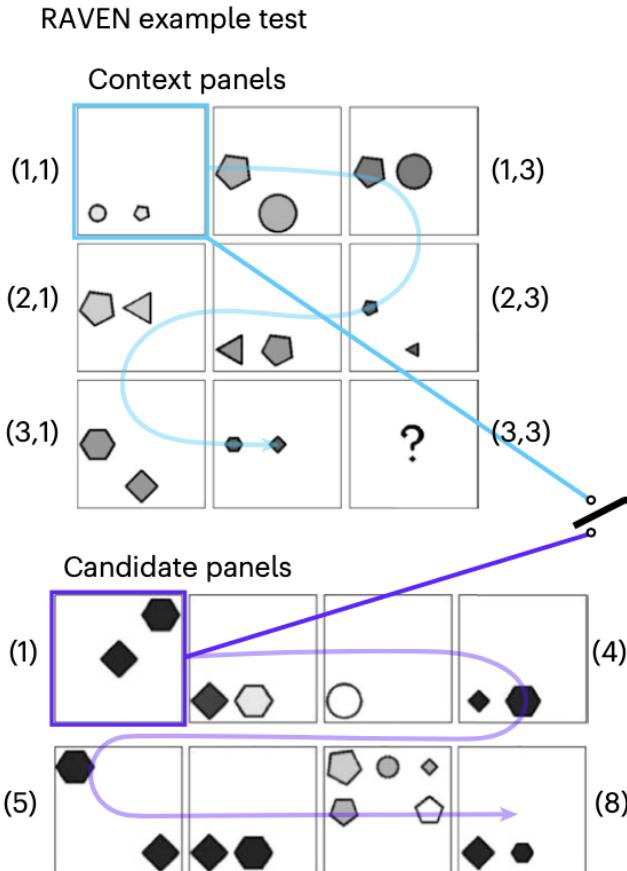
Neuro Perception

Symbolic Reasoning



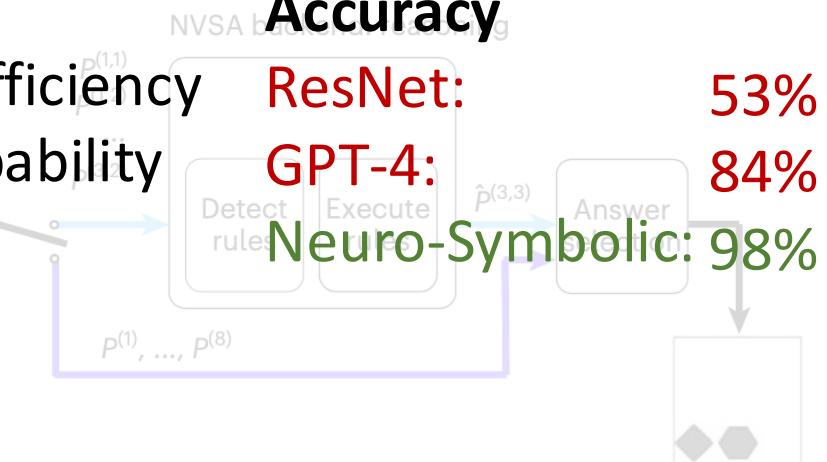
"A neuro-vector-symbolic architecture for solving Raven's progressive matrices". In Nature Machine Intelligence, 2023

Example: Neuro-Vector-Symbolic Architecture



NVSA frontend: perception

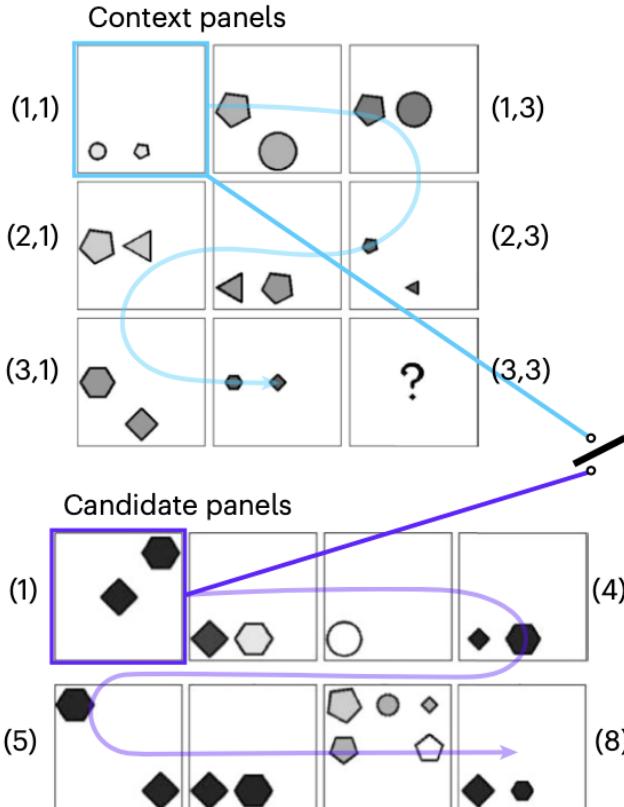
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 - Higher transparency
- **Dataset:** RAVEN, I-RAVEN, PGM
- **Computational Components:**
 - Neuro: ConvNet
 - Symbolic: vector-symbolic operation, circular convolution



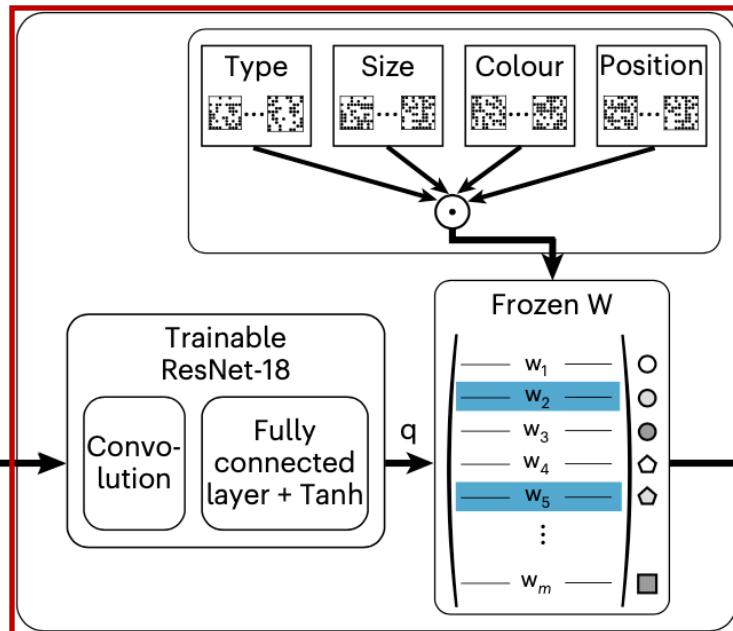
"A neuro-vector-symbolic architecture for solving Raven's progressive matrices". In Nature Machine Intelligence, 2023

Example: Neuro-Vector-Symbolic Architecture

RAVEN example test

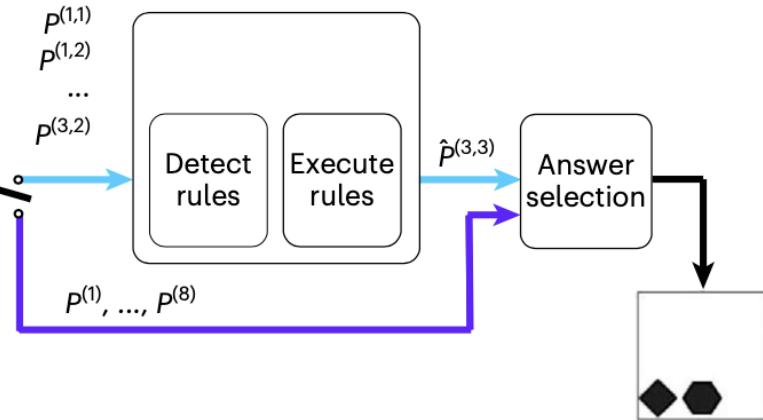


NVSA frontend: perception



Neuro Perception

NVSA backend: reasoning

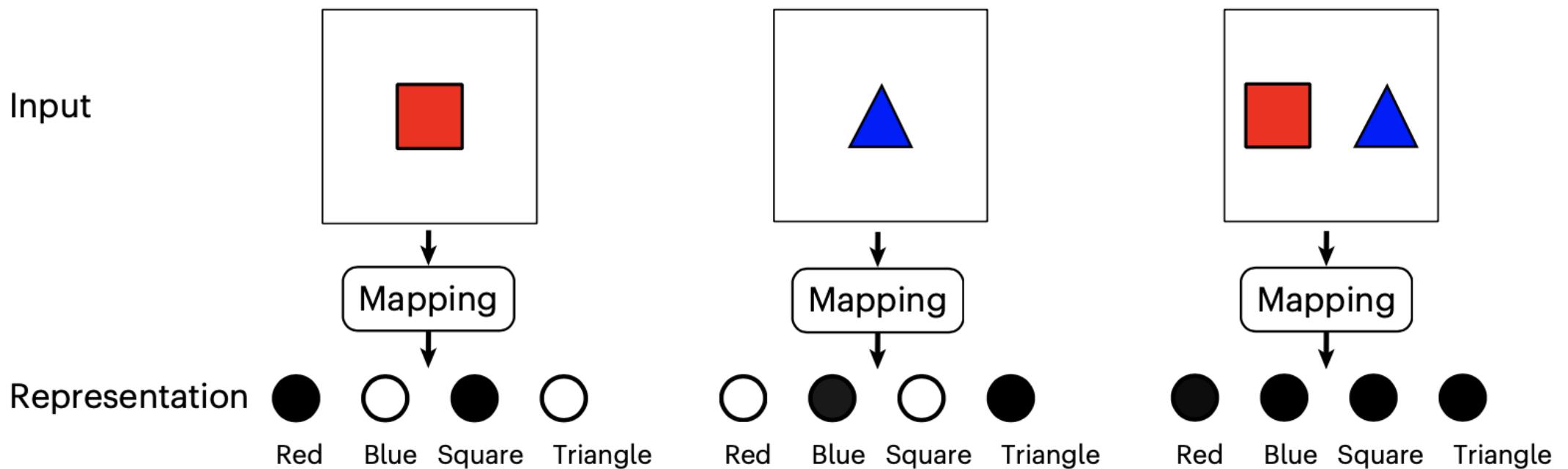


Symbolic Reasoning

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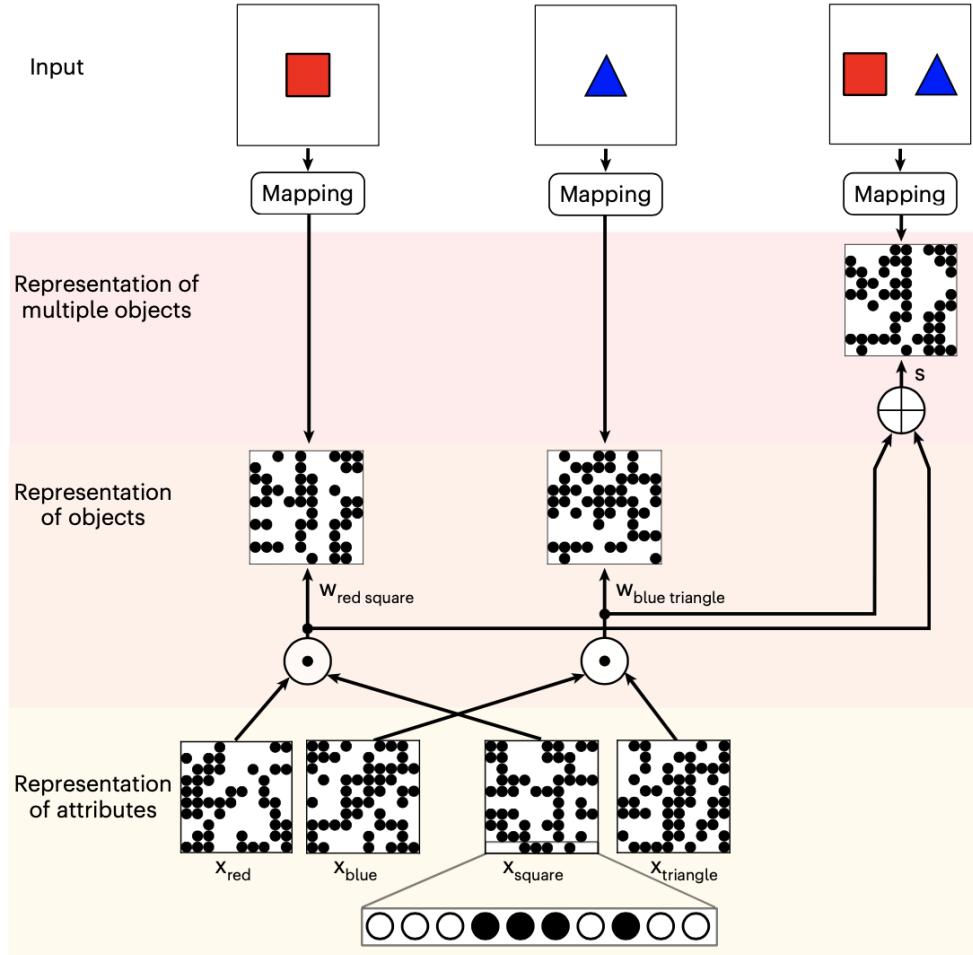
Binding Problem in Neural Network

Localist distributed representation



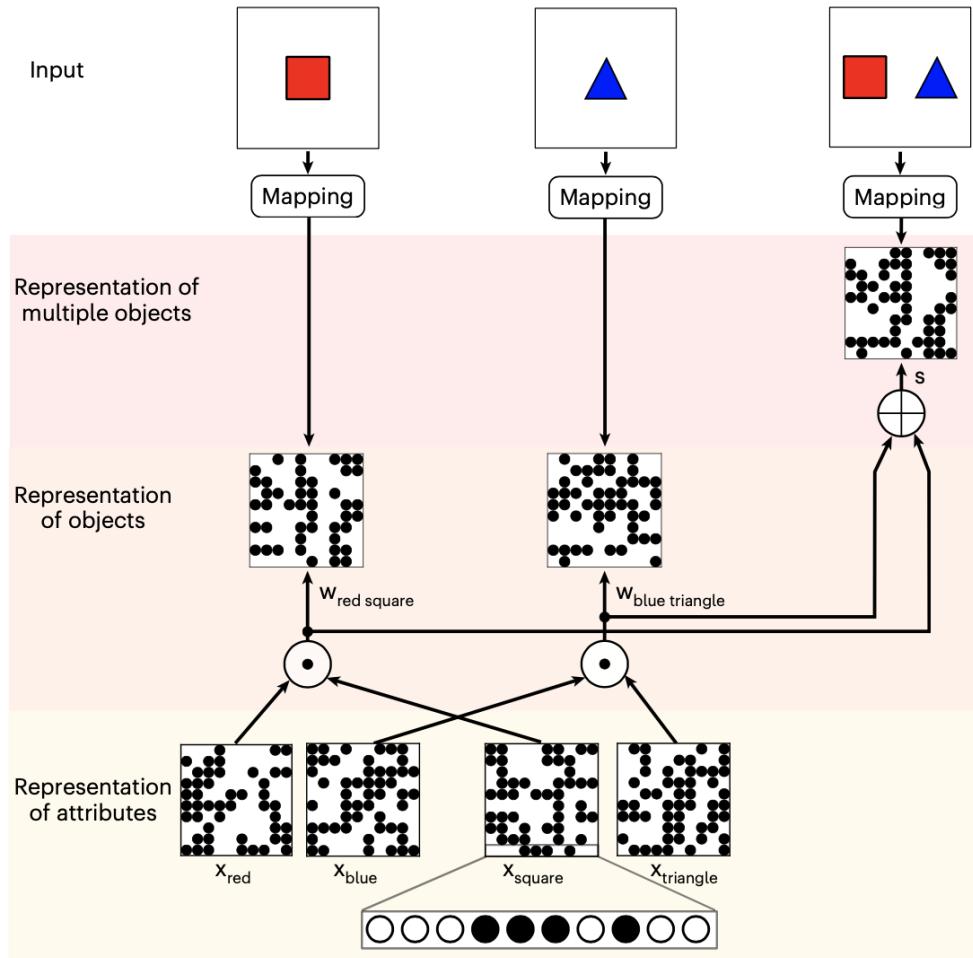
Solutions in Vector-Symbolic Architecture

High-dimensional VSA representations and operators

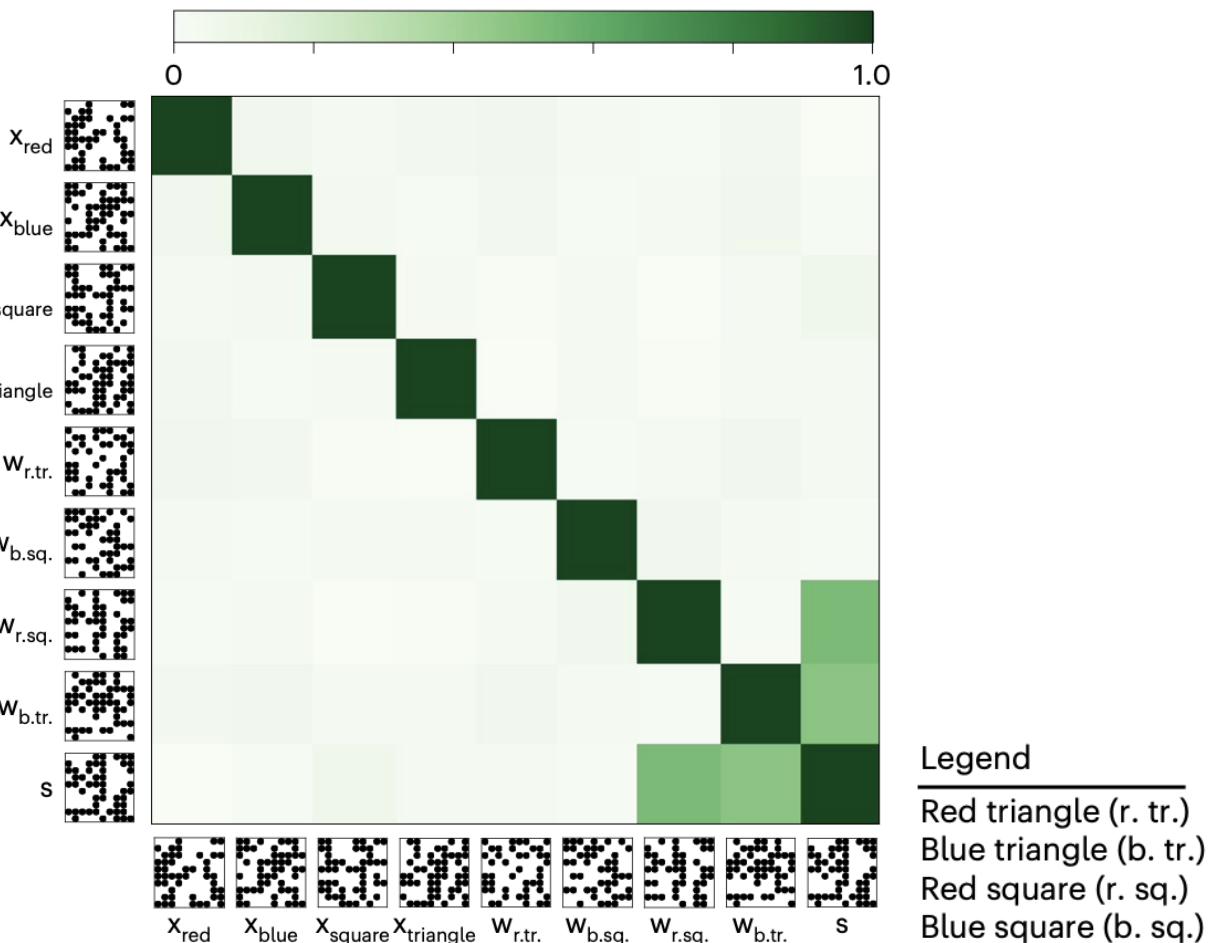


Solutions in Vector-Symbolic Architecture

High-dimensional VSA representations and operators



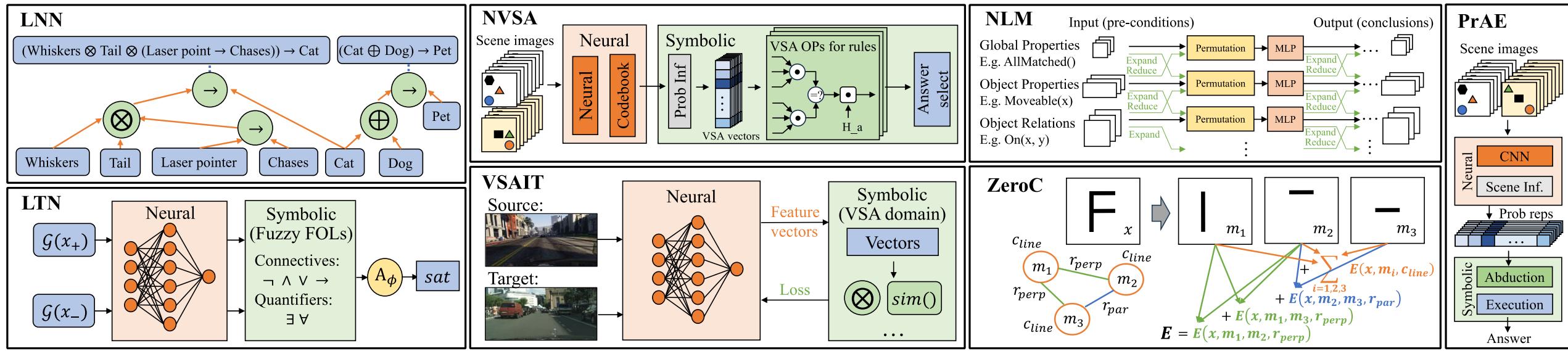
Absolute pairwise cosine similarity between VSA representations



Legend

- Red triangle (r. tr.)
- Blue triangle (b. tr.)
- Red square (r. sq.)
- Blue square (b. sq.)

Selected Neuro-Symbolic Workloads



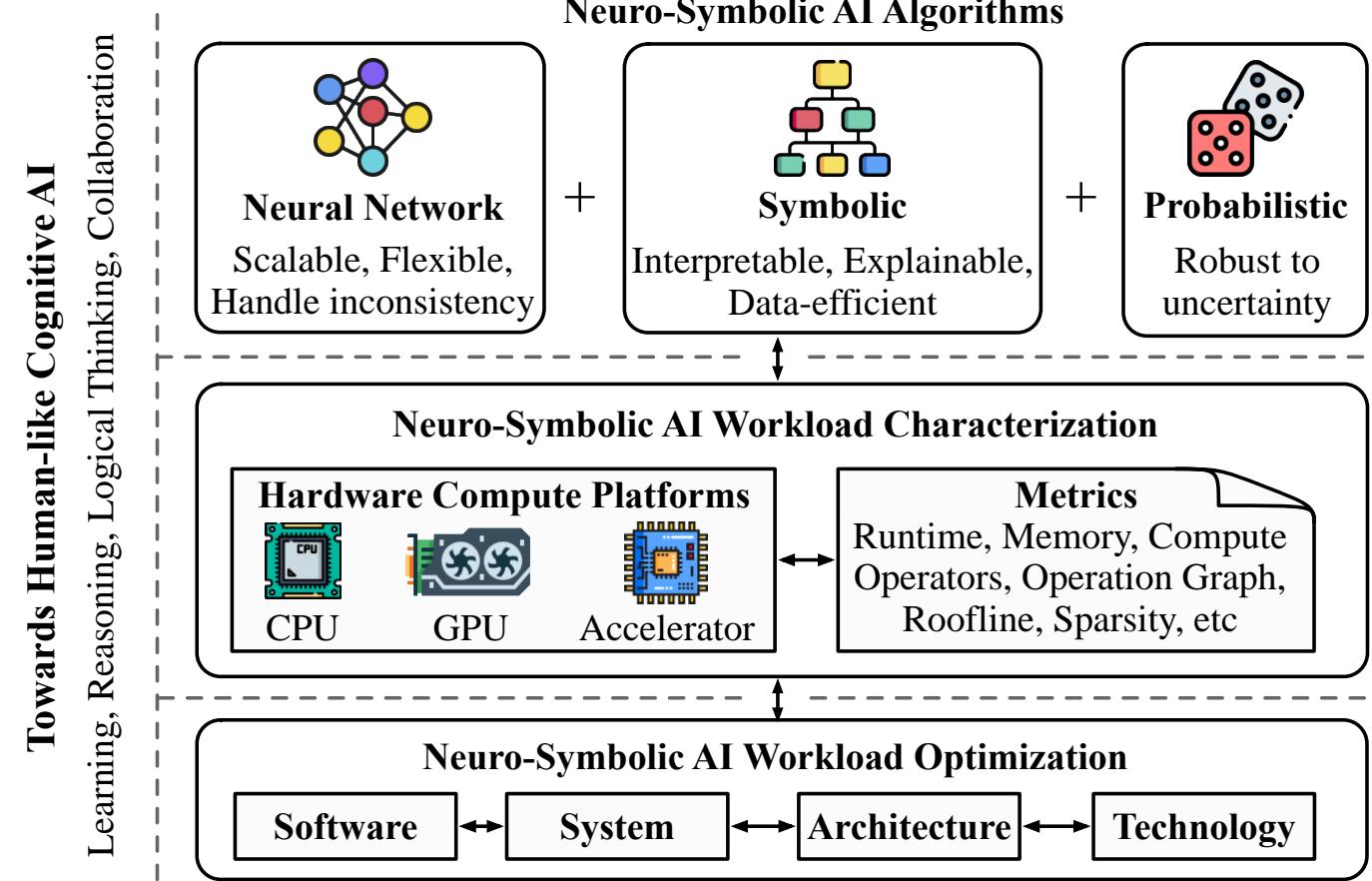
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Neuro-Symbolic AI Workload and Characterization

Categorize Neuro-Symbolic Algorithms

Understand Computational Behavior of Neuro-Symbolic Workloads

Identify Co-Design Opportunities



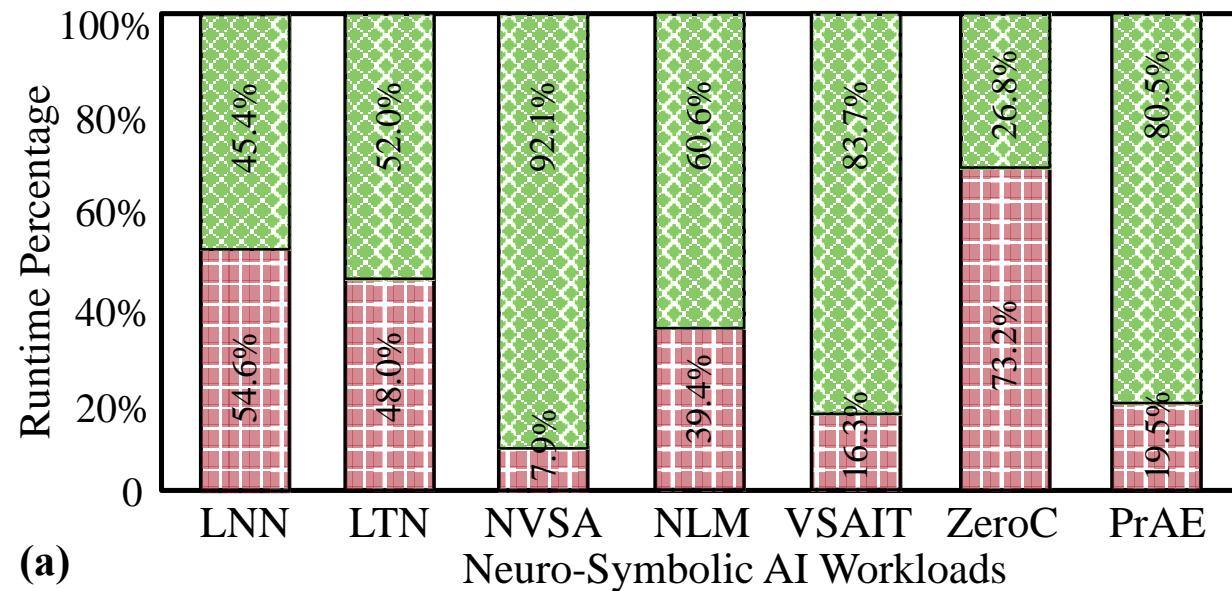
Neuro-Symbolic Workload Characterization

Profiling setup: CPU+GPU system, using pytorch profiler, seven neuro-symbolic workloads

Neuro-Symbolic Workload Characterization

Profiling setup: CPU+GPU system, using pytorch profiler, seven neuro-symbolic workloads

- End-to-end runtime latency analysis:

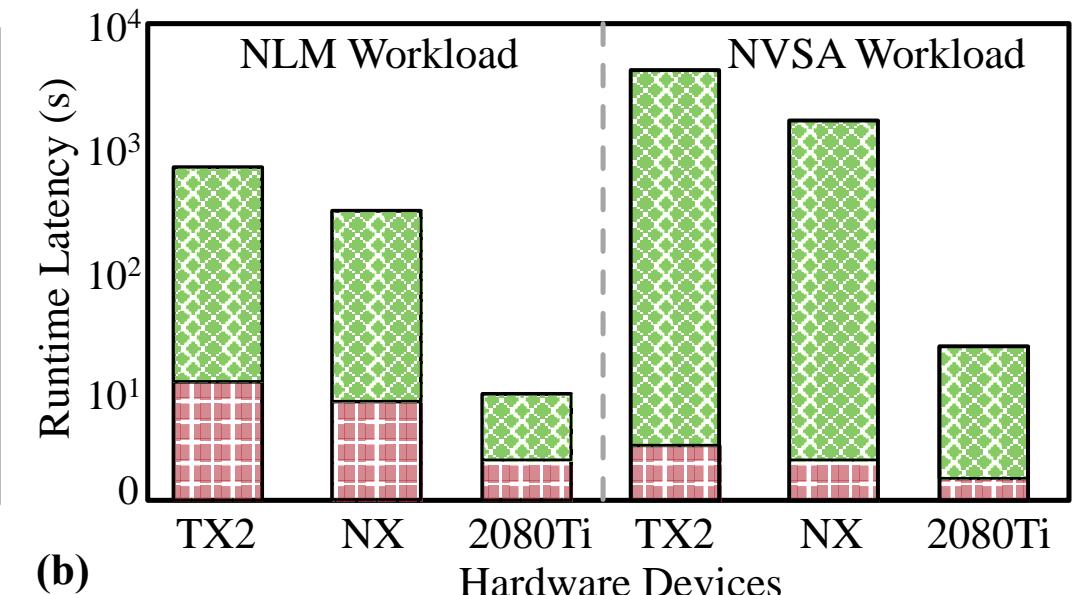
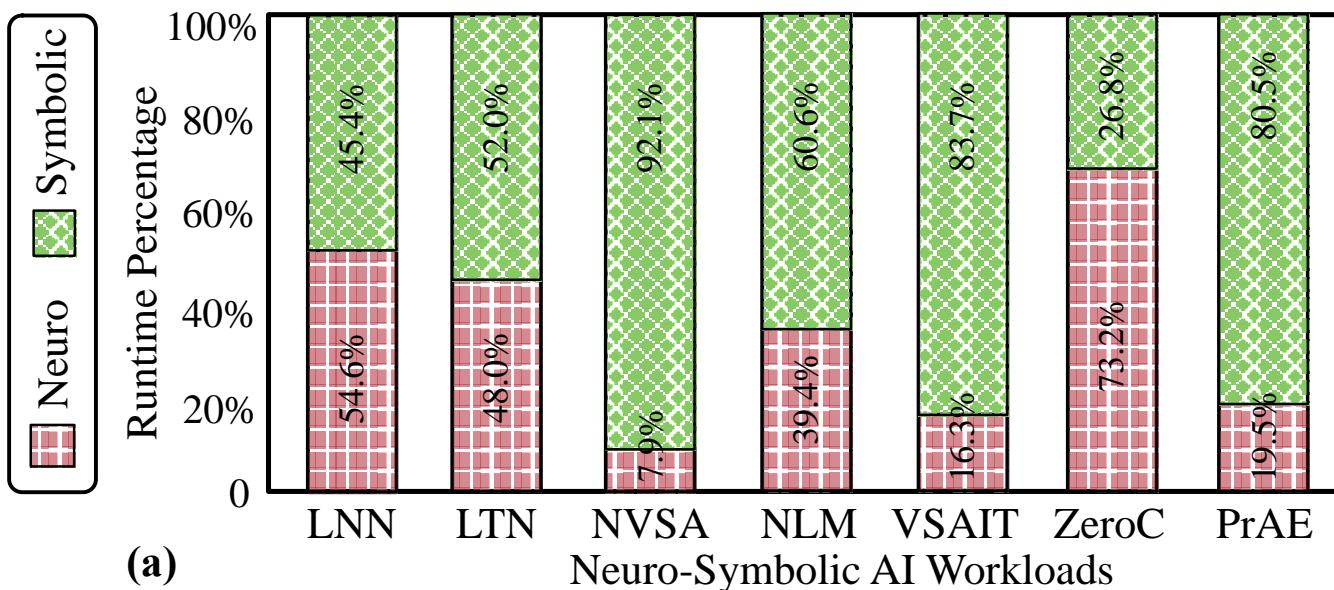


Neuro-symbolic workload exhibits high latency compared to neural models;
Symbolic component is processed inefficiently on off-the-shelf CPU/GPUs

Neuro-Symbolic Workload Characterization

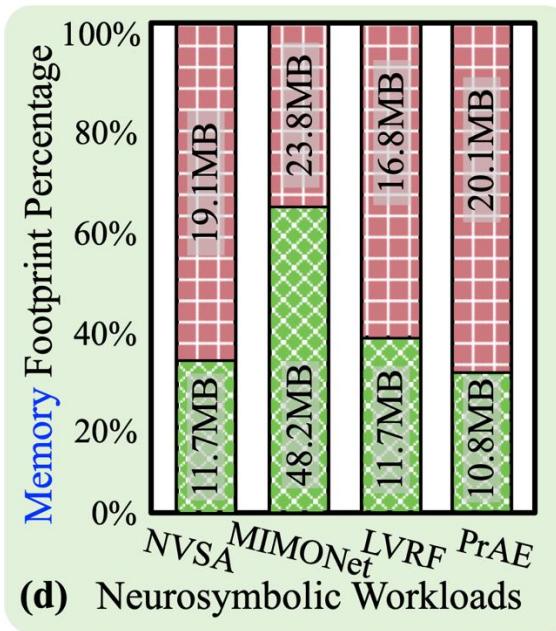
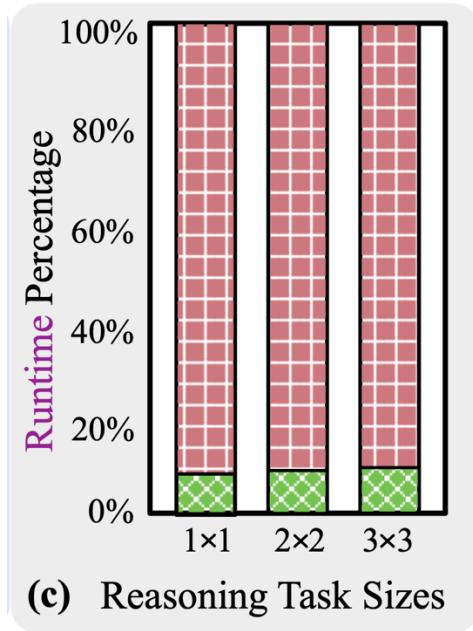
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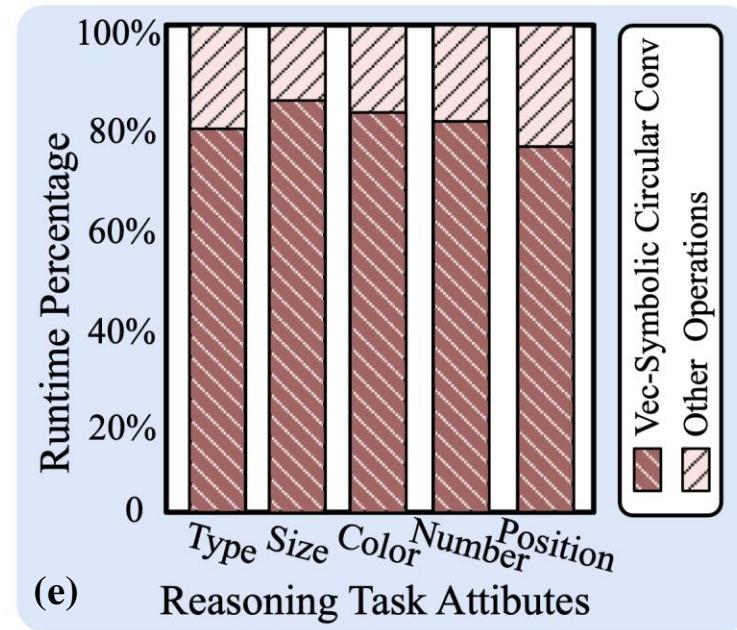
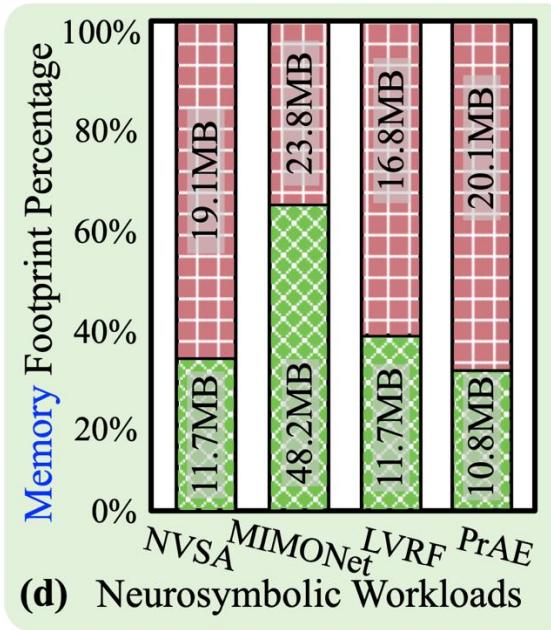
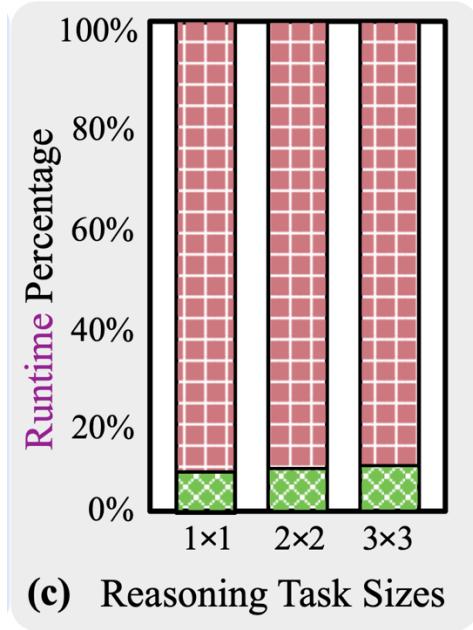
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Neuro-Symbolic Workload Characterization



Symbolic components exhibit **large memory footprint**;

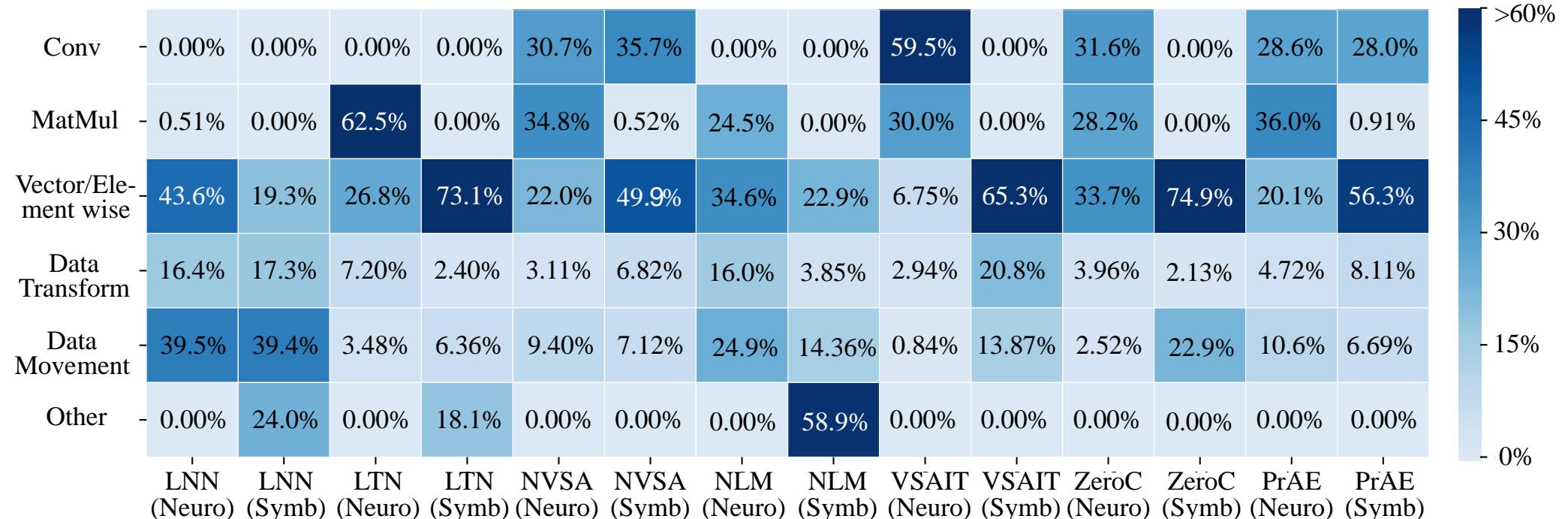
Neuro-Symbolic Workload Characterization



Symbolic components exhibit **large memory footprint**;
Symbolic operations are dominated by **vector-symbolic circular convolutions**

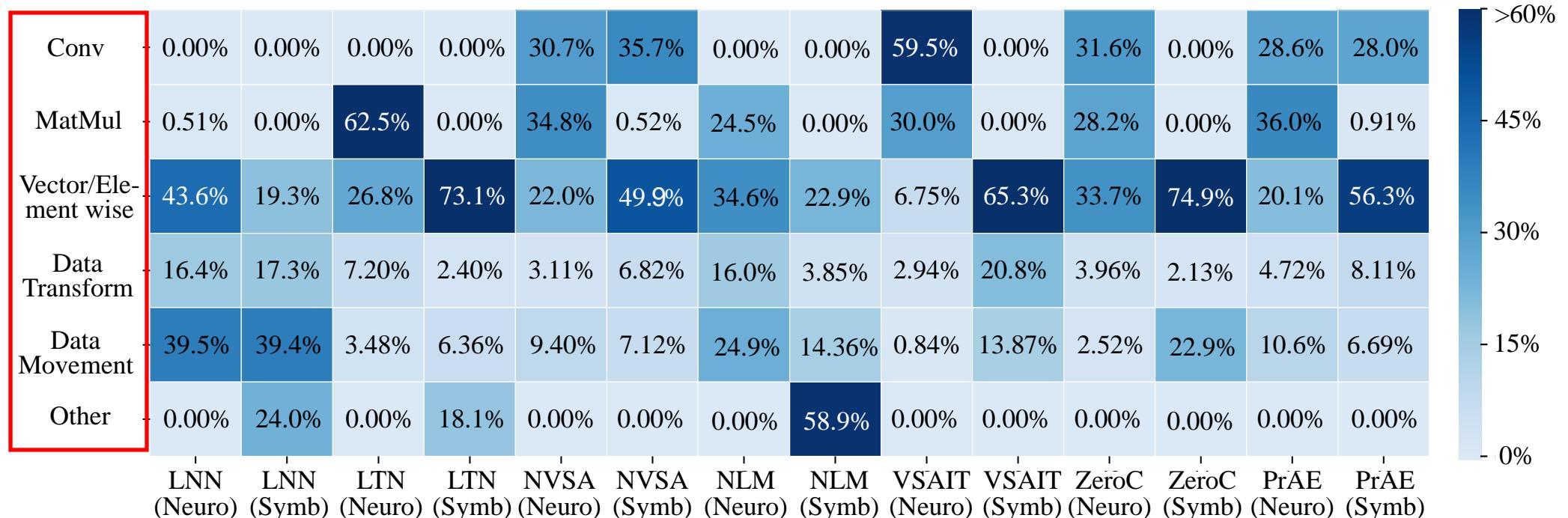
Neuro-Symbolic Workload Characterization

- Compute operator analysis:



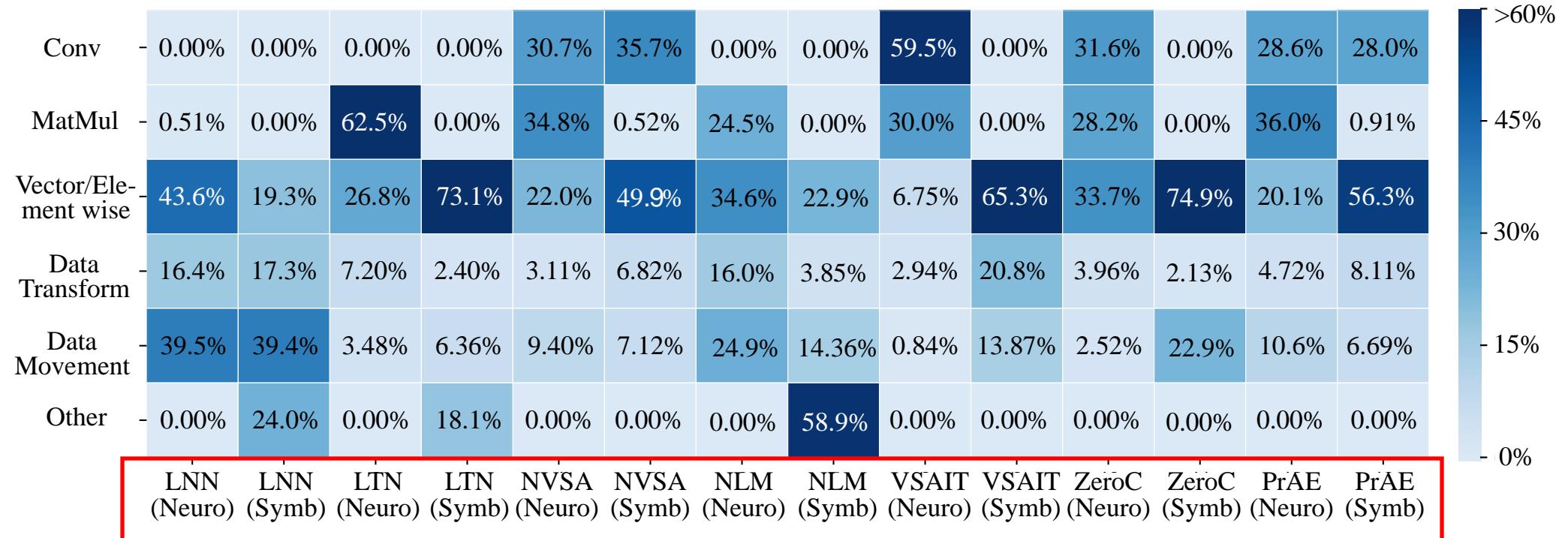
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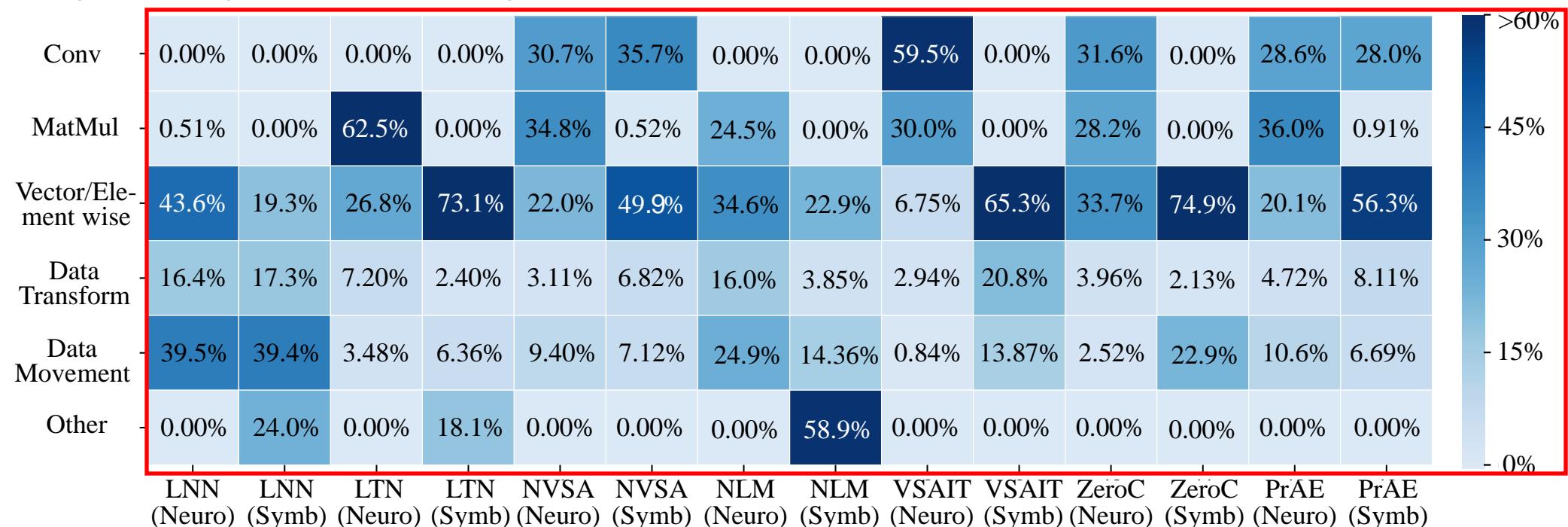
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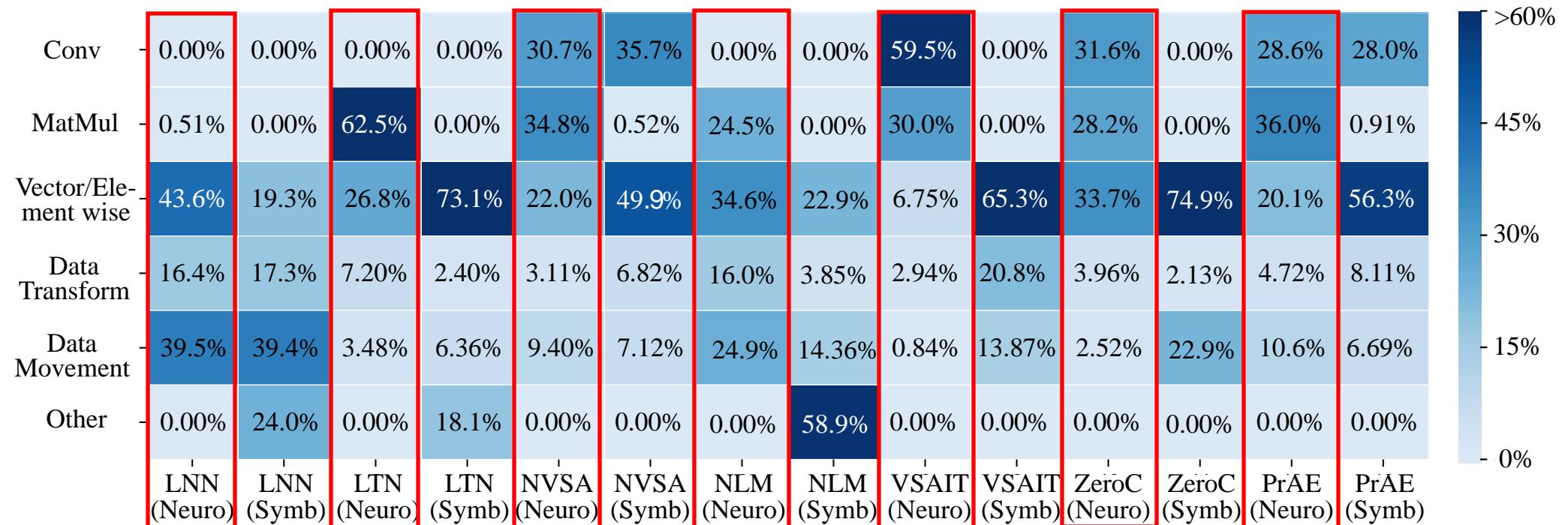
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- Compute operator analysis:



Neuro-Symbolic Workload Characterization

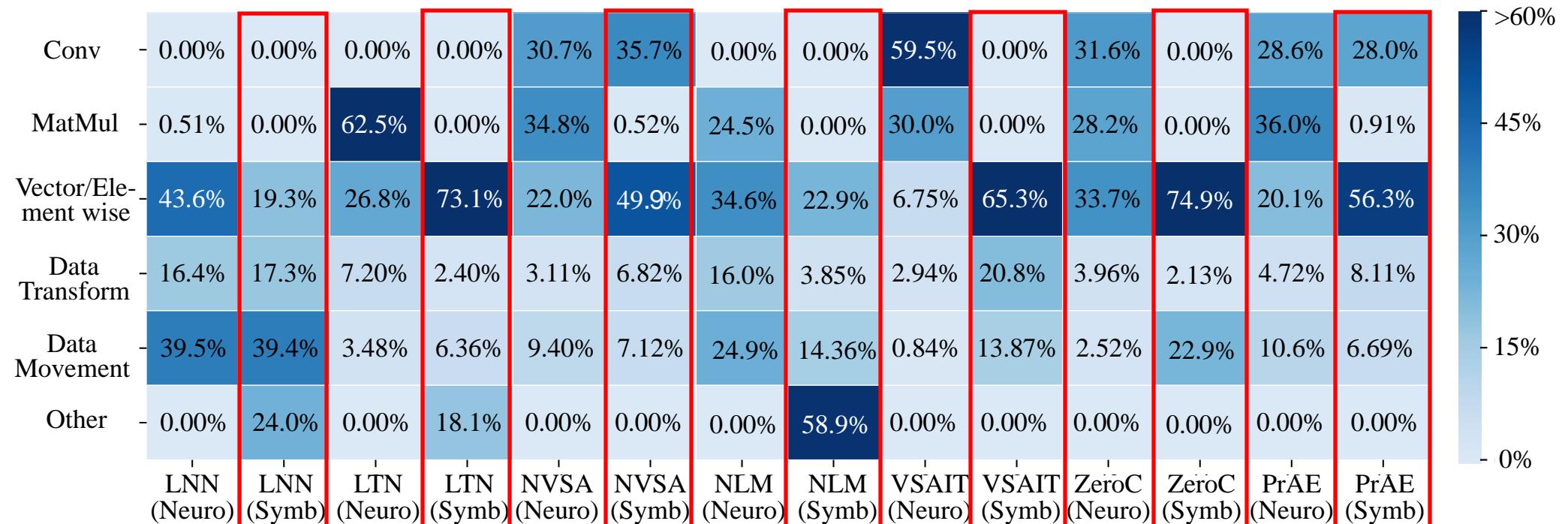
- Compute operator analysis:



Neural dominated by MatMul and Conv;

Neuro-Symbolic Workload Characterization

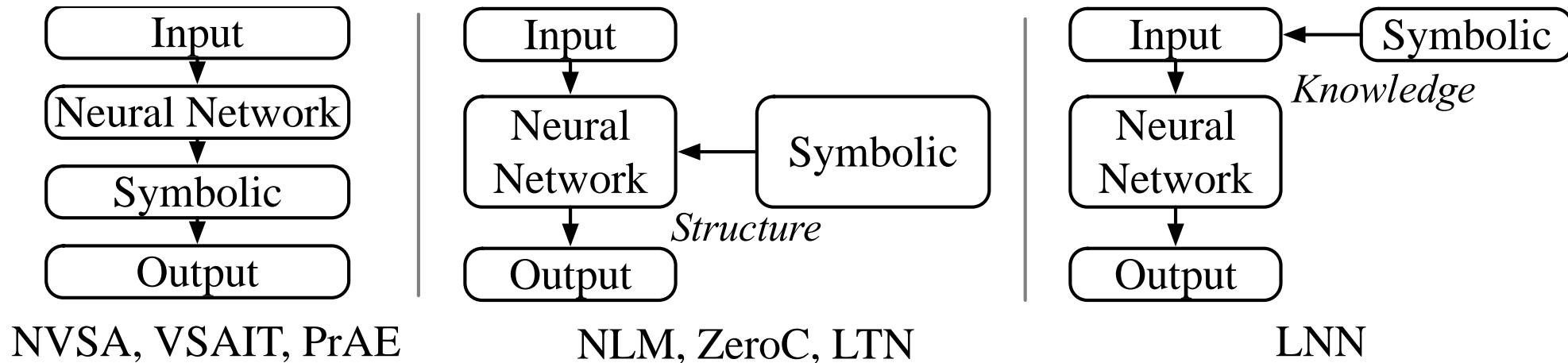
- Compute operator analysis:



Neural dominated by MatMul and Conv; Symbolic dominated by vector/element/logical operations;

Neuro-Symbolic Workload Characterization

- Data Dependence Graph analysis:



Neural dominated by MatMul and Conv; Symbolic dominated by vector/element/logical operations; Complex control flow of neuro-symbolic interaction

Neuro-Symbolic Workload Characterization

	Neuro Kernel		Symbolic Kernel	
	segmm_nn	relu_nn	vectorized	elementwise
Runtime Percentage (%)				
Compute Throughput (%)				
ALU Utilization (%)				
L1 Cache Hit Rate (%)				
L2 Cache Hit Rate (%)				
L1 Cache Throughput (%)				
L2 Cache Throughput (%)				
DRAM BW Utilization (%)				

Why system Inefficiency?

Neuro-Symbolic Workload Characterization

	Neuro Kernel		Symbolic Kernel	
	segmm_nn	relu_nn	vectorized	elementwise
Runtime Percentage (%)	18.2	10.4	37.5	12.4
Compute Throughput (%)	95.1	92.9	3.0	2.3
ALU Utilization (%)	90.1	48.3	5.9	4.5
L1 Cache Hit Rate (%)				
L2 Cache Hit Rate (%)				
L1 Cache Throughput (%)				
L2 Cache Throughput (%)				
DRAM BW Utilization (%)				

Symbolic exhibits low ALU utilization,

Neuro-Symbolic Workload Characterization

	Neuro Kernel		Symbolic Kernel	
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Runtime Percentage (%)	18.2	10.4	37.5	12.4
Compute Throughput (%)	95.1	92.9	3.0	2.3
ALU Utilization (%)	90.1	48.3	5.9	4.5
L1 Cache Hit Rate (%)	1.6	51.6	29.5	33.3
L2 Cache Hit Rate (%)	86.8	65.5	48.6	34.3
L1 Cache Throughput (%)				
L2 Cache Throughput (%)				
DRAM BW Utilization (%)				

Symbolic exhibits low ALU utilization, low cache hit rate,

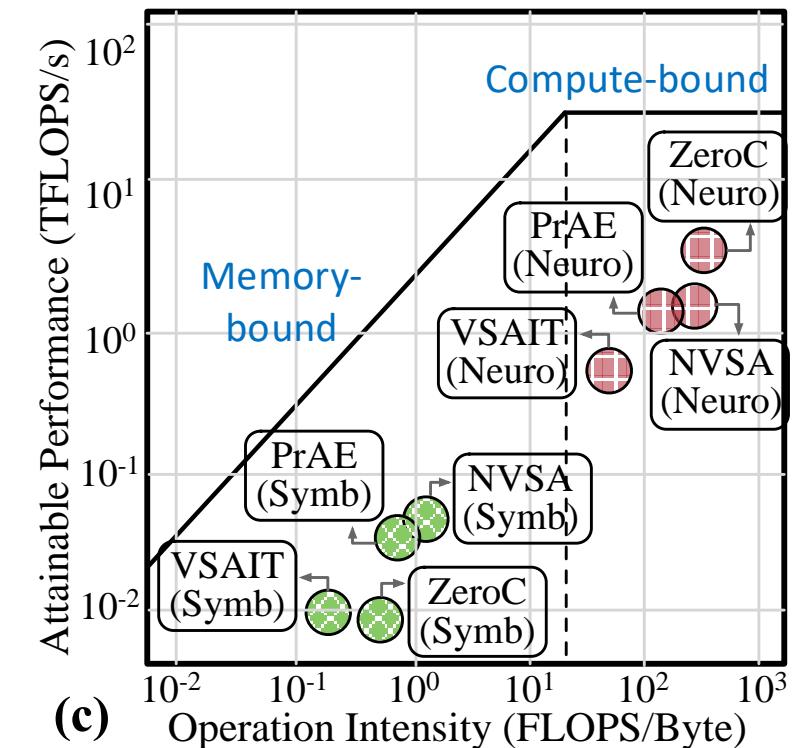
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L1 Cache Hit Rate (%)	1.6	51.6	29.5	33.3
L2 Cache Hit Rate (%)	86.8	65.5	48.6	34.3
L1 Cache Throughput (%)	79.7	82.6	28.4	10.8
L2 Cache Throughput (%)	19.2	17.5	29.8	22.8
DRAM BW Utilization (%)	14.9	24.2	90.9	78.4

Symbolic exhibits low ALU utilization, low cache hit rate, massive data transfer, resulting in hardware underutilization and inefficiency

Neuro-Symbolic Workload Characterization

	Neuro Kernel		Symbolic Kernel	
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Neuro operations are compute-bounded, symbolic operations are memory-bounded.

Neural Network vs. Neuro-Symbolic

	Neural Network	Neuro-Symbolic

Neural Network vs. Neuro-Symbolic

	Neural Network	Neuro-Symbolic
Runtime	[Neural Network] < [Neuro-Symbolic]	

Neural Network vs. Neuro-Symbolic

	Neural Network	Neuro-Symbolic
Runtime	[Neural Network] < [Neural-Symbolic]	
Compute Kernels	Neural kernels (Conv, MatMul, etc)	Heterogenous neural and symbolic kernels (vector, element, MatMul, graph, logic, etc)

Neural Network vs. Neuro-Symbolic

	Neural Network	Neuro-Symbolic
Runtime	[Neural Network] < [Neuro-Symbolic]	
Compute Kernels	Neural kernels (Conv, MatMul, etc)	Heterogenous neural and symbolic kernels (vector, element, MatMul, graph, logic, etc)
Hardware Efficiency	Efficient on GPU/TPU	Inefficient on CPU/GPU/TPU (low ALU utilization, low L1 cache hit rate, high data movement, etc)

Neural Network vs. Neuro-Symbolic

	Neural Network	Neuro-Symbolic
Runtime	[Neural Network] < [Neural-Symbolic]	
Compute Kernels	Neural kernels (Conv, MatMul, etc)	Heterogenous neural and symbolic kernels (vector, element, MatMul, graph, logic, etc)
Hardware Efficiency	Efficient on GPU/TPU	Inefficient on CPU/GPU/TPU (low ALU utilization, low L1 cache hit rate, high data movement, etc)
System Bound	Compute-bound / Memory-bound	Memory-bound

Neural Network vs. Neuro-Symbolic

	Neural Network	Neuro-Symbolic
Runtime	[Neural Network] < [Neural-Symbolic]	
Compute Kernels	Neural kernels (Conv, MatMul, etc)	Heterogenous neural and symbolic kernels (vector, element, MatMul, graph, logic, etc)
Hardware Efficiency	Efficient on GPU/TPU	Inefficient on CPU/GPU/TPU (low ALU utilization, low L1 cache hit rate, high data movement, etc)
System Bound	Compute-bound / Memory-bound	Memory-bound
Dataflow	Simple flow control, High parallelism	Complex flow control, Low parallelism



Research Question:

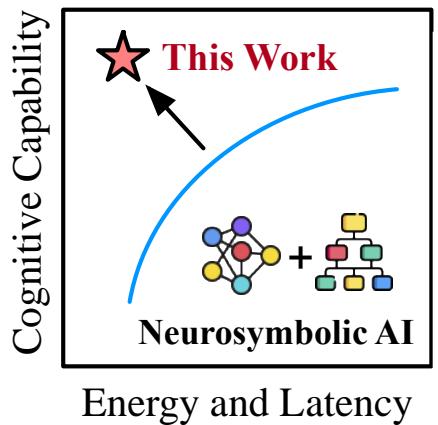
How to enhance the **efficiency and scalability** of neuro-symbolic systems?

Outline

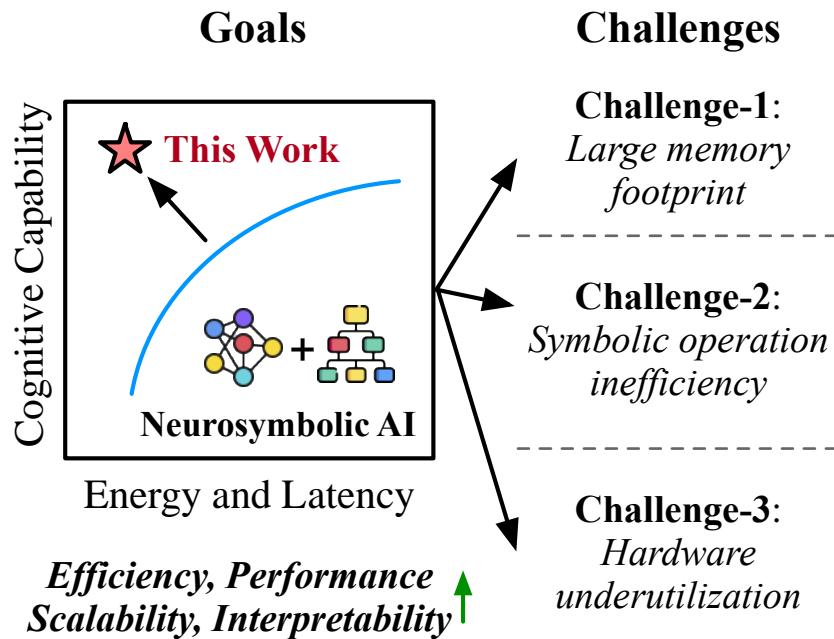
- Neuro-symbolic AI 101
- Neuro-symbolic AI workload characterization
- **Neuro-symbolic AI hardware architecture**
- Final project: neuro-symbolic kernel optimization

Our Methodology

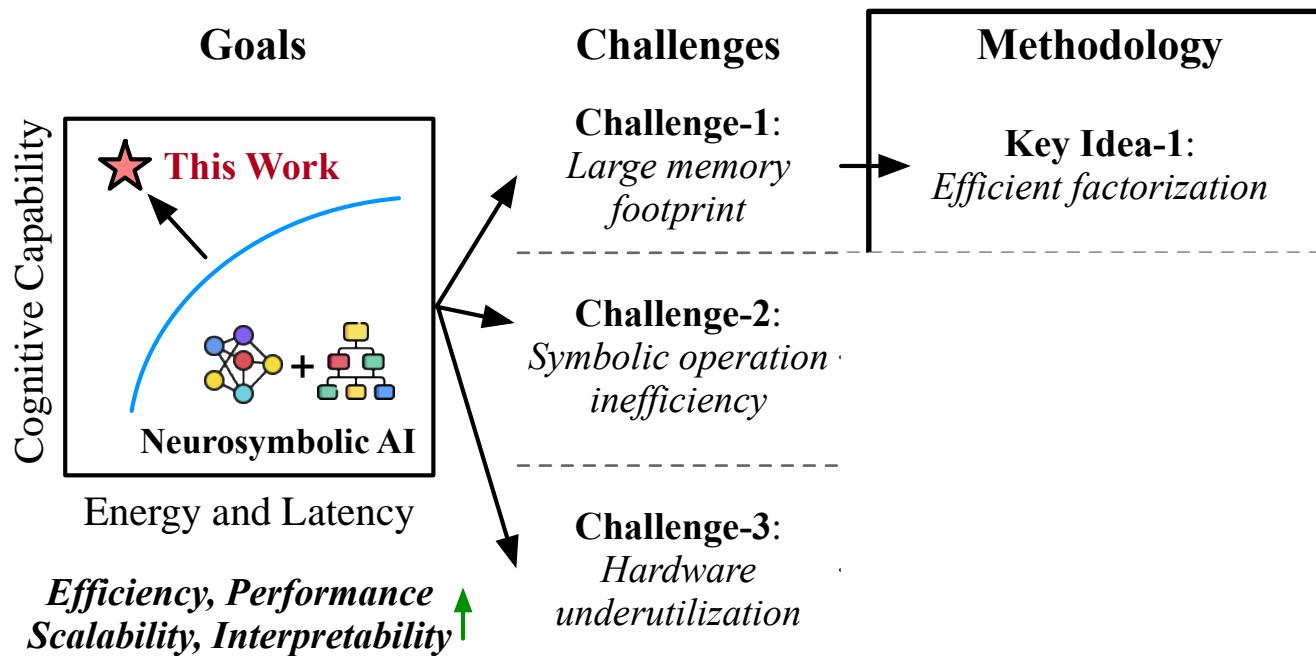
Goals



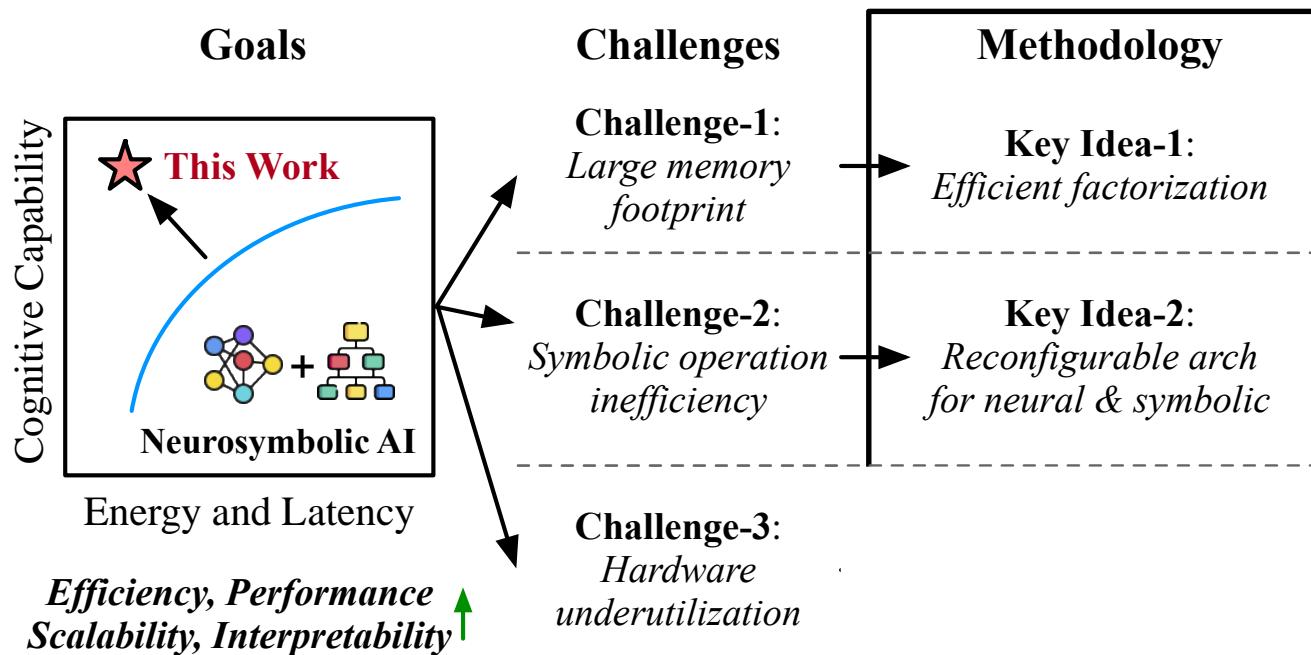
Our Methodology



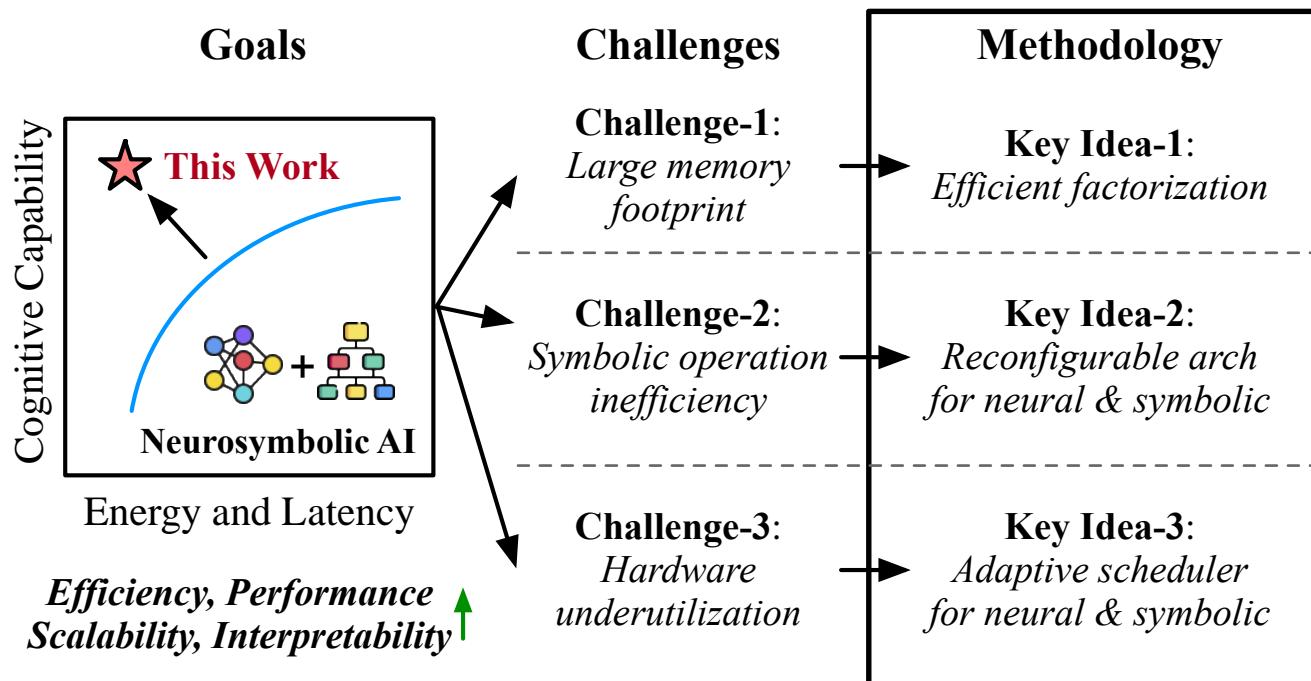
Our Methodology



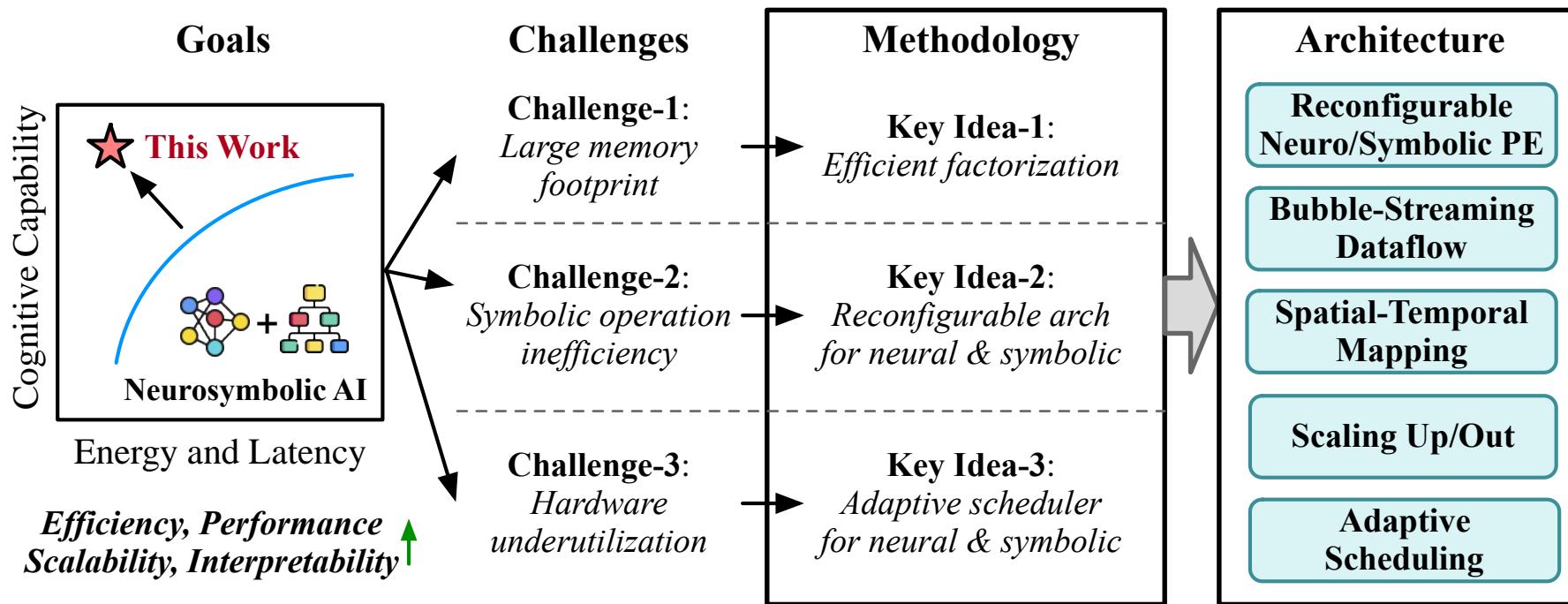
Our Methodology



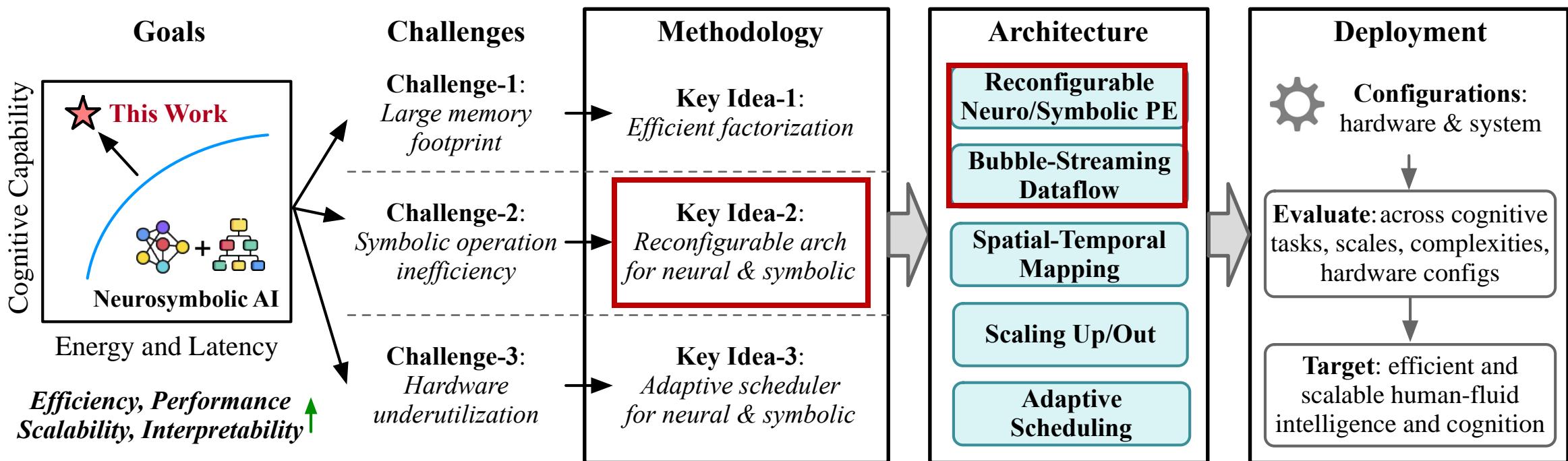
Our Methodology



Our Methodology



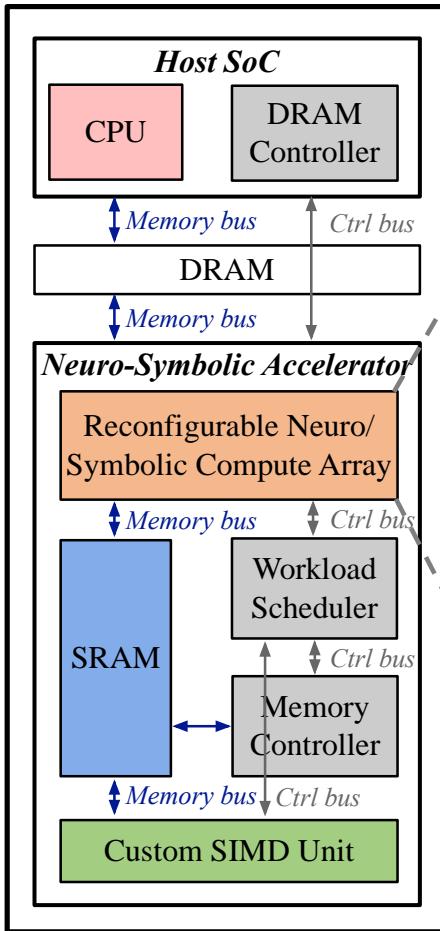
Our Methodology



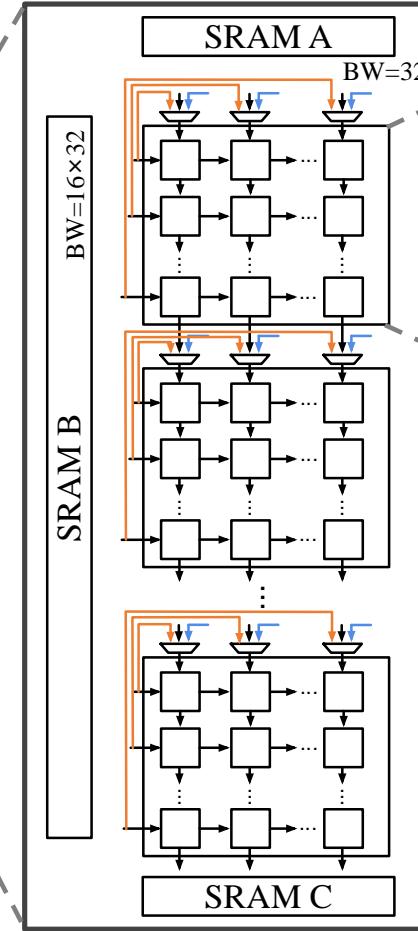
"CogSys: Efficient and Scalable Neurosymbolic Cognition System via Algorithm-Hardware Co-Design", in HPCA 2025 [[PDF](#)]

Hardware Architecture Overview

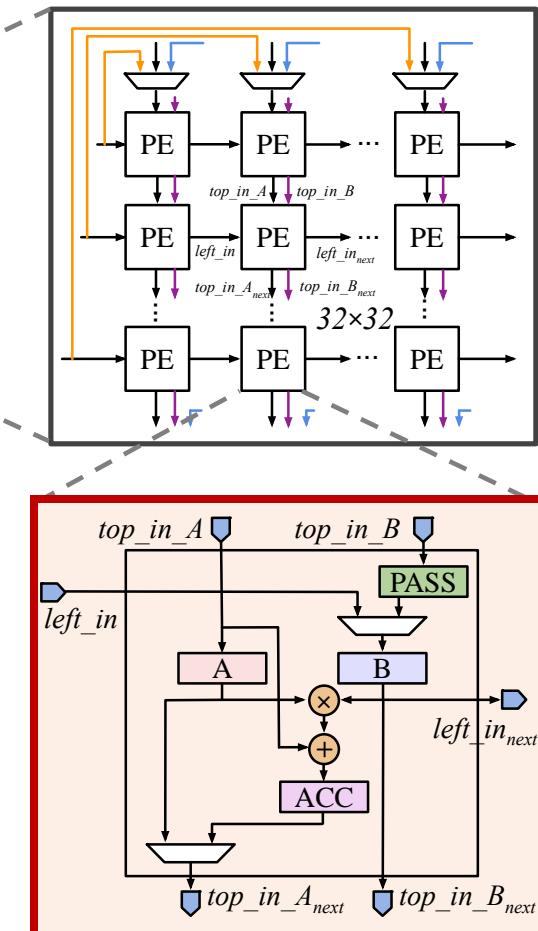
(a) Overall Architecture



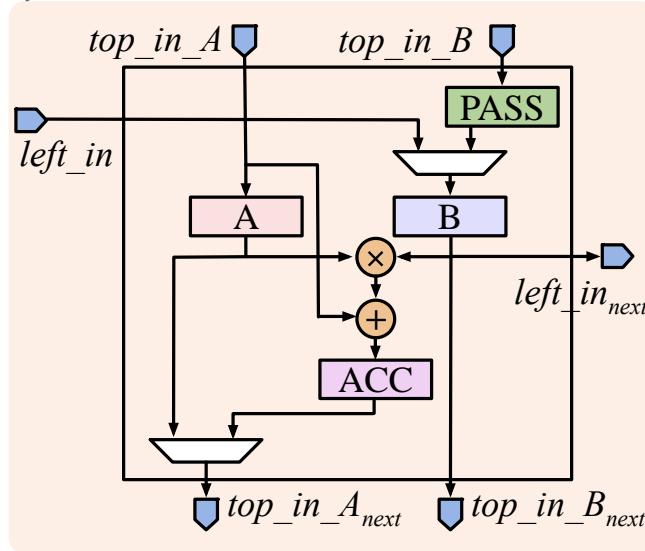
(b) Scalable Compute Array



(c) Reconfig. Neuro/Symbolic PE



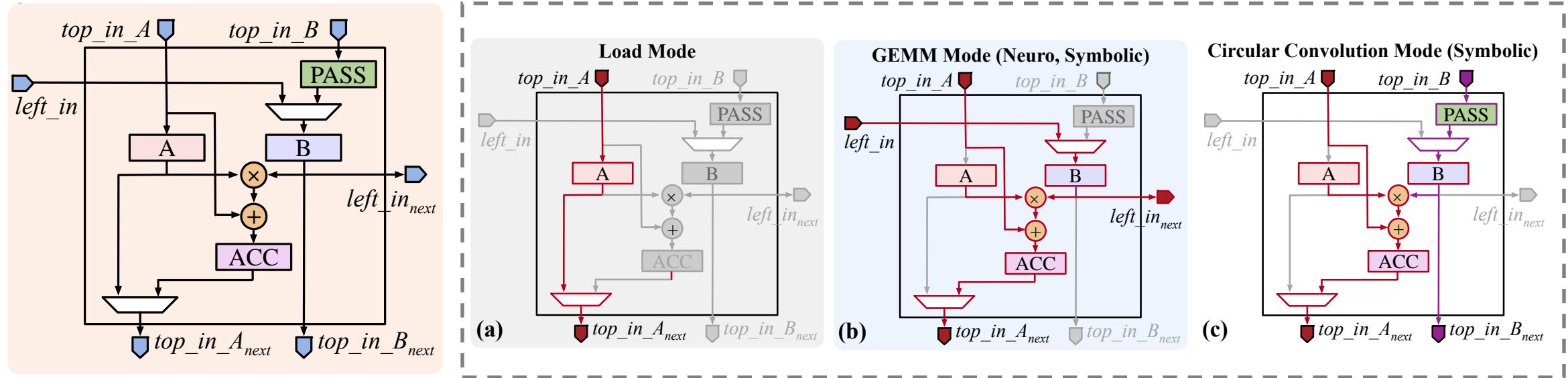
Reconfigurable Neuro/Symbolic PE



Micro-architecture of reconfigurable neuro/symbolic PE

Reconfigurable neuro/symbolic PE incurs **low area overhead** compared to systolic array PE;

Reconfigurable Neuro/Symbolic PE



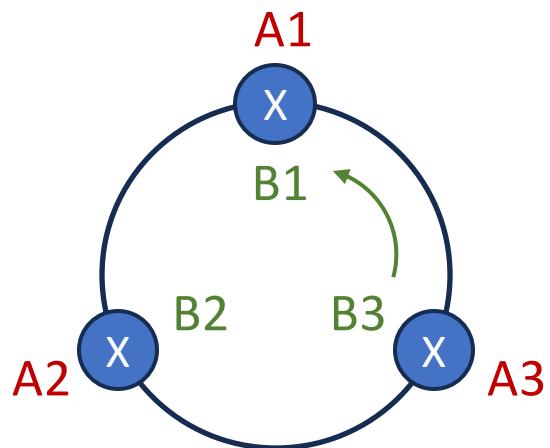
Micro-architecture of
reconfigurable neuro/symbolic PE

Operation mode of
reconfigurable neuro/symbolic PE

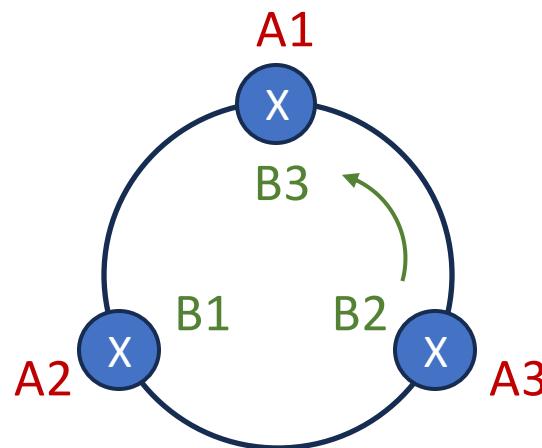
Reconfigurable neuro/symbolic PE incurs **low area overhead** compared to systolic array PE;
The PE is reconfigurable for **three operation modes**: load, neuro, symbolic

What is Circular Convolution?

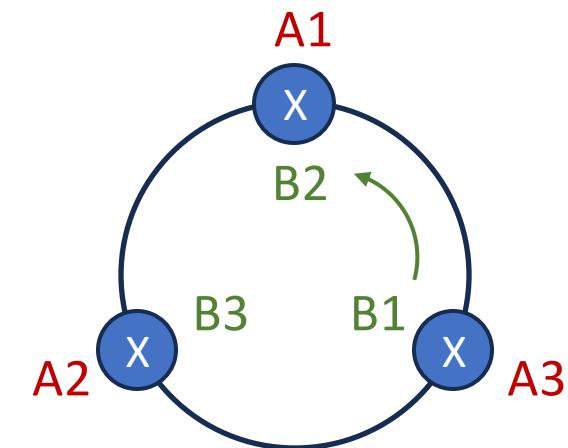
$$\begin{bmatrix} A1 \\ A2 \\ A3 \end{bmatrix} \odot \begin{bmatrix} B1 \\ B2 \\ B3 \end{bmatrix} = \begin{bmatrix} A1B1+A2B2+A3B3 \\ A1B3+A2B1+A3B2 \\ A1B2+A2B3+A2B1 \end{bmatrix}$$



$$A1B1+A2B2+A3B3$$



$$A1B3+A2B1+A3B2$$



$$A1B2+A2B3+A3B1$$

Bubble Streaming Dataflow

Vector-Symbolic Circular Convolution Example (3 CircConv):

$$\text{CircConv \#1: } (A_1, A_2, A_3) \odot (B_1, B_2, B_3)$$

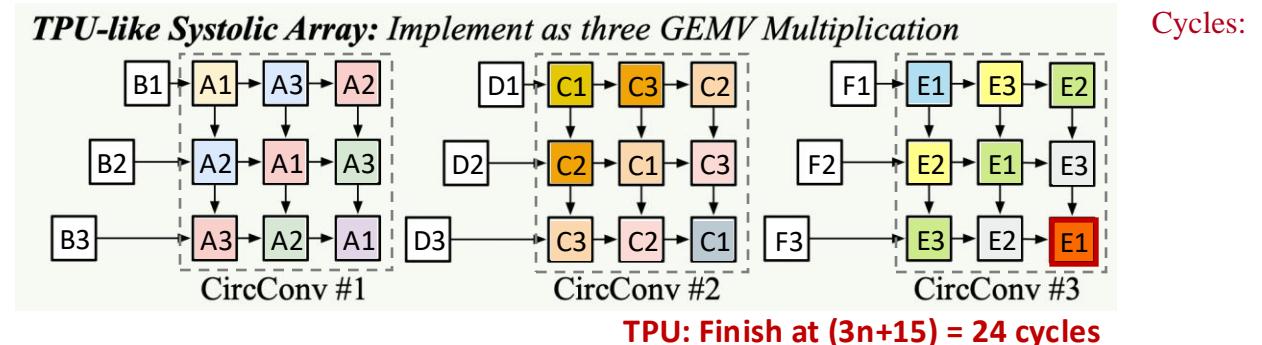
$$\text{CircConv \#2: } (C_1, C_2, C_3) \odot (D_1, D_2, D_3)$$

$$\text{CircConv \#3: } (E_1, E_2, E_3) \odot (F_1, F_2, F_3)$$

CircConv #1 Computation:

$$(A_1, A_2, A_3) \odot (B_1, B_2, B_3) =$$

$$(A_1B_1 + A_2B_2 + A_3B_3, A_1B_3 + A_2B_1 + A_3B_2, A_1B_2 + A_2B_3 + A_3B_1)$$



For symbolic operation:

- TPU-like array **suffers from** low parallelism & high memory access;

Bubble Streaming Dataflow

Vector-Symbolic Circular Convolution Example (3 CircConv):

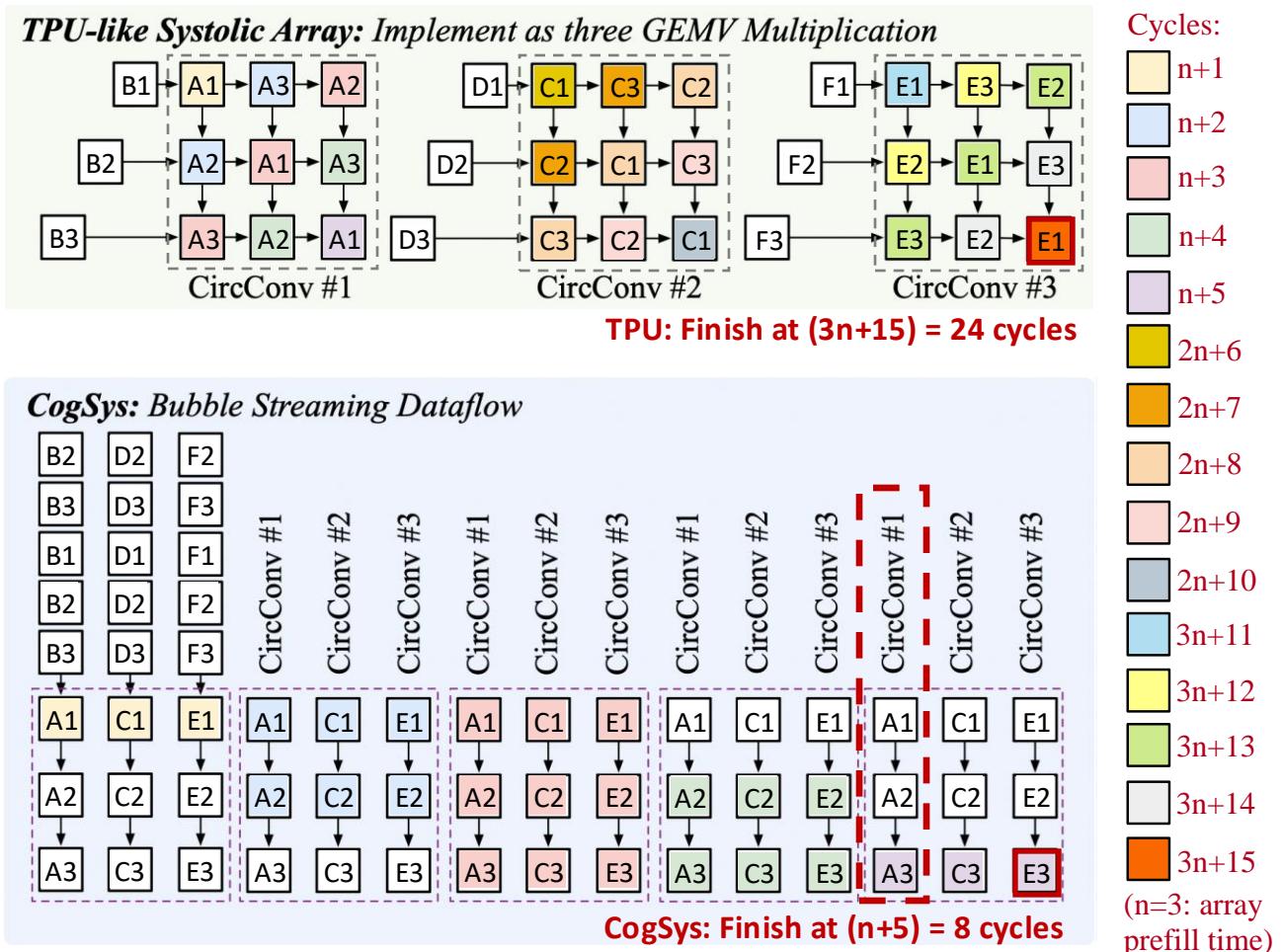
- CircConv #1: $(A_1, A_2, A_3) \odot (B_1, B_2, B_3)$
- CircConv #2: $(C_1, C_2, C_3) \odot (D_1, D_2, D_3)$
- CircConv #3: $(E_1, E_2, E_3) \odot (F_1, F_2, F_3)$

CircConv #1 Computation:

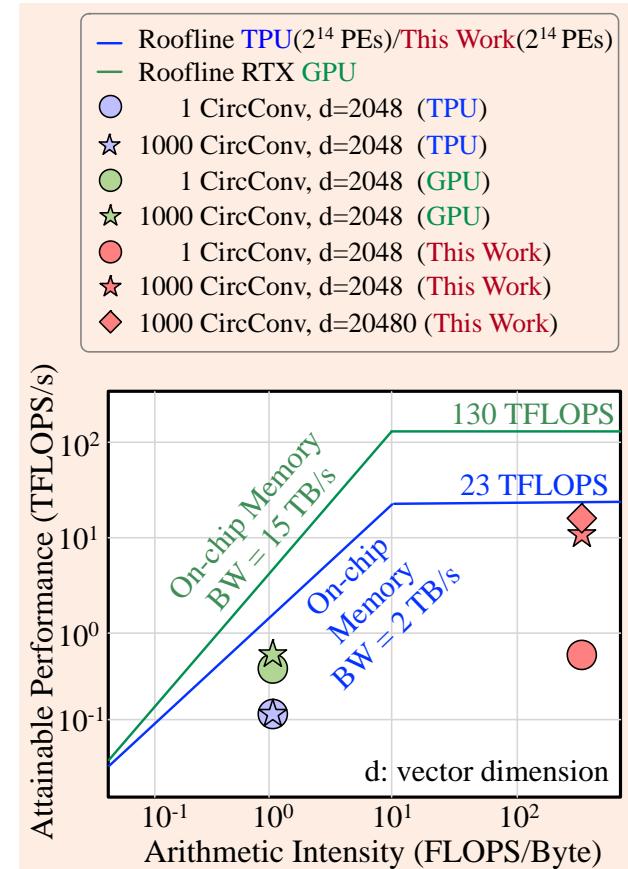
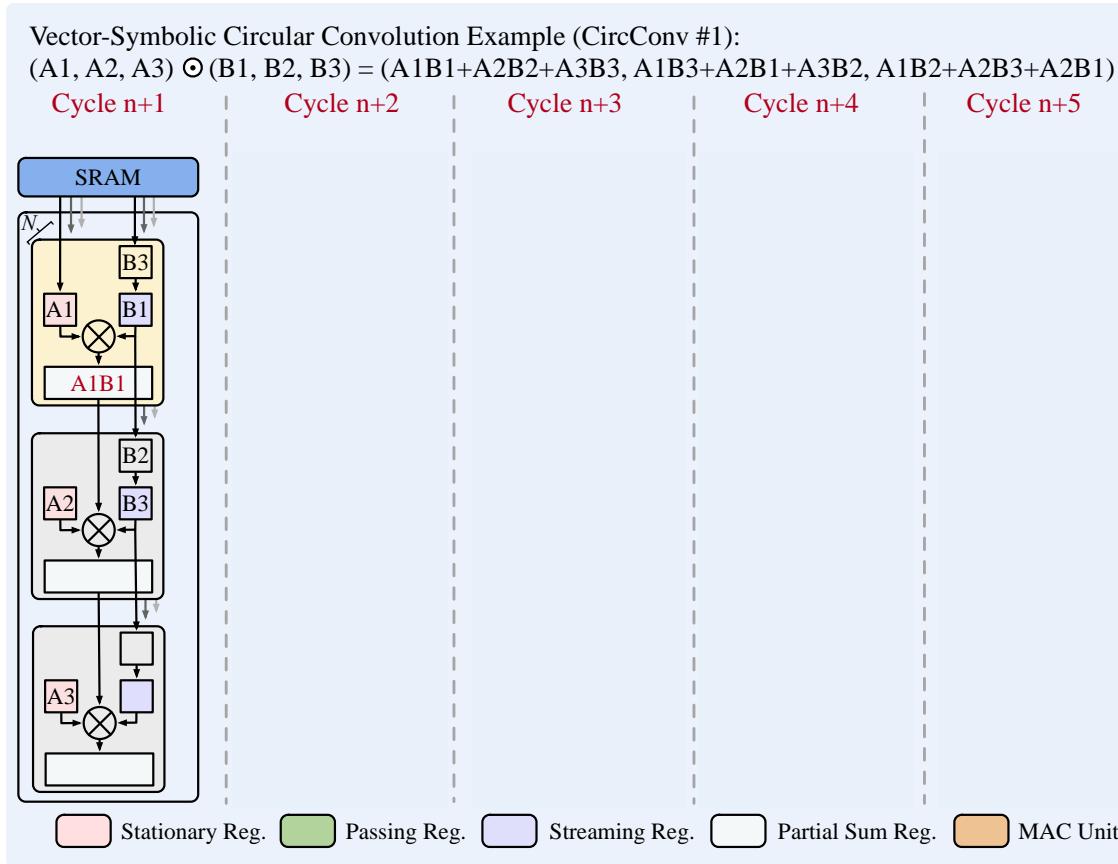
$$(A_1, A_2, A_3) \odot (B_1, B_2, B_3) = \\ (A_1B_1 + A_2B_2 + A_3B_3, A_1B_3 + A_2B_1 + A_3B_2, A_1B_2 + A_2B_3 + A_3B_1)$$

For symbolic operation:

- TPU-like array **suffers from** low parallelism & high memory access;
- Bubble streaming dataflow **improve parallelism, arithmetic intensity, and data reuse.**

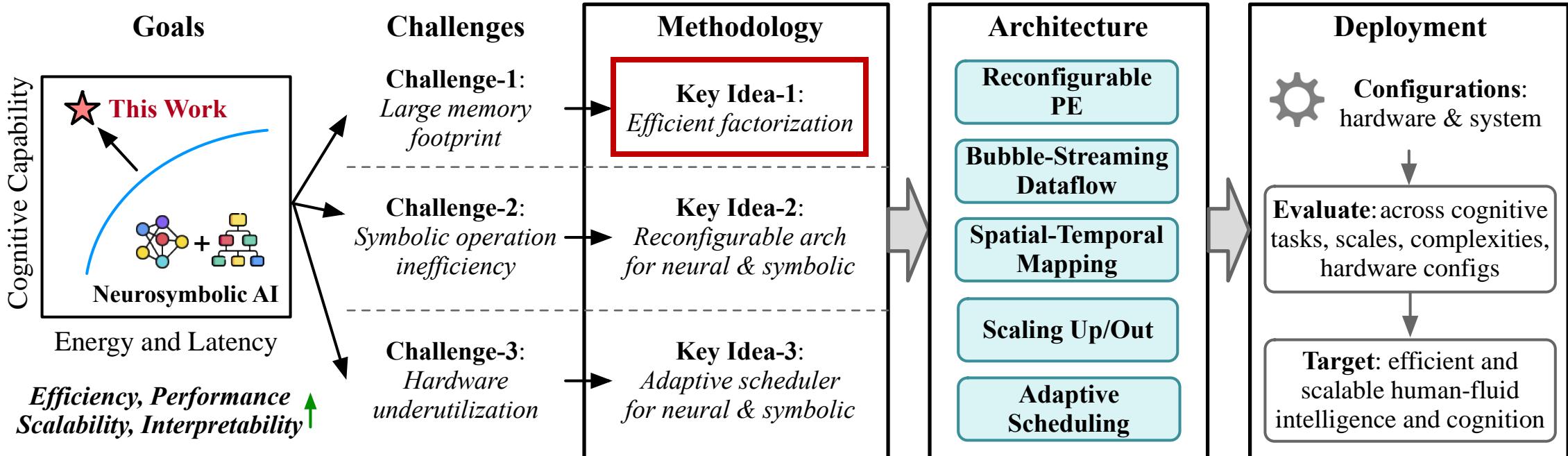


Bubble Streaming Dataflow

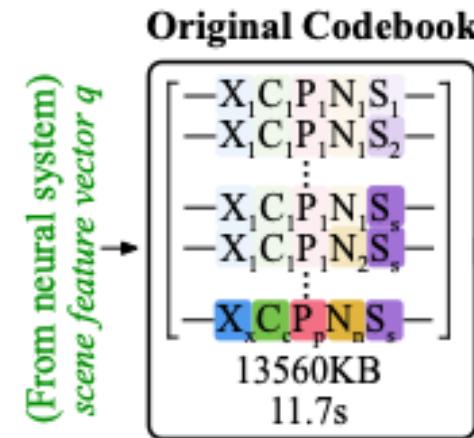


Bubble streaming dataflow flow improve parallelism, arithmetic intensity, and data reuse

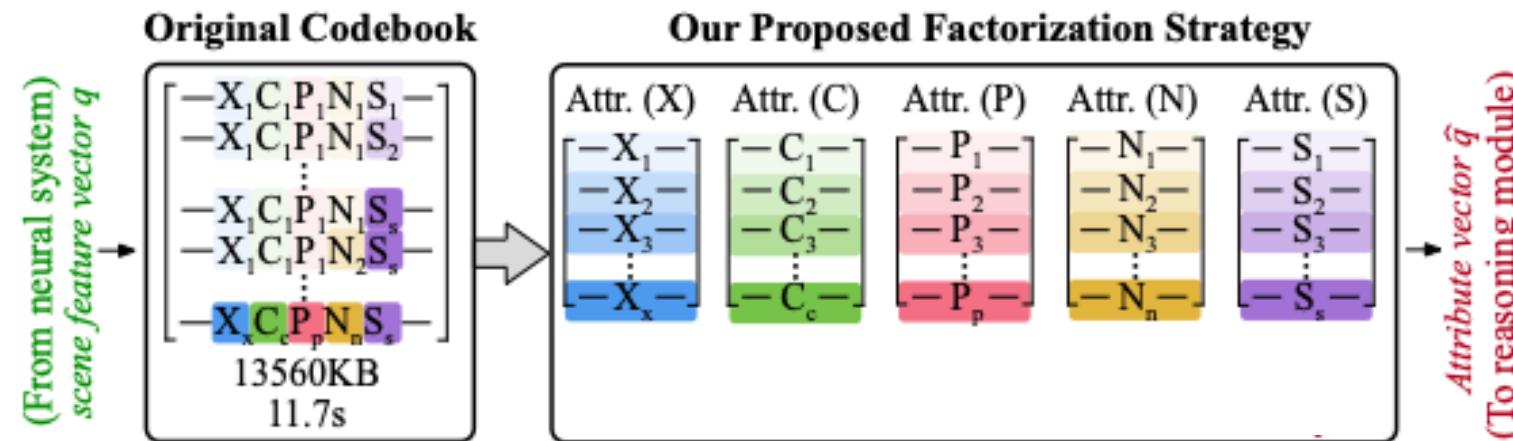
Our Methodology



Algorithm Optimization – Efficient Factorization

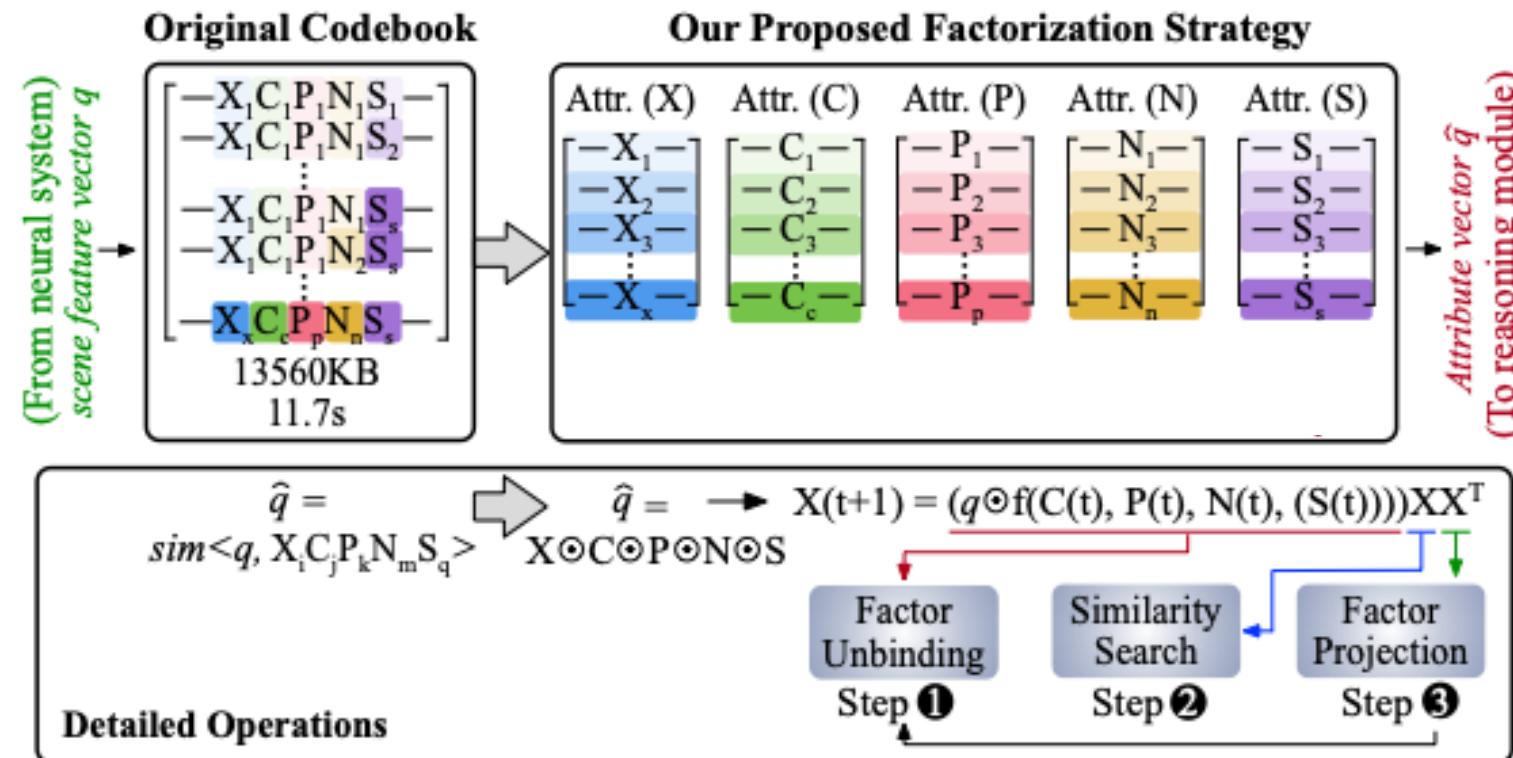


Algorithm Optimization – Efficient Factorization



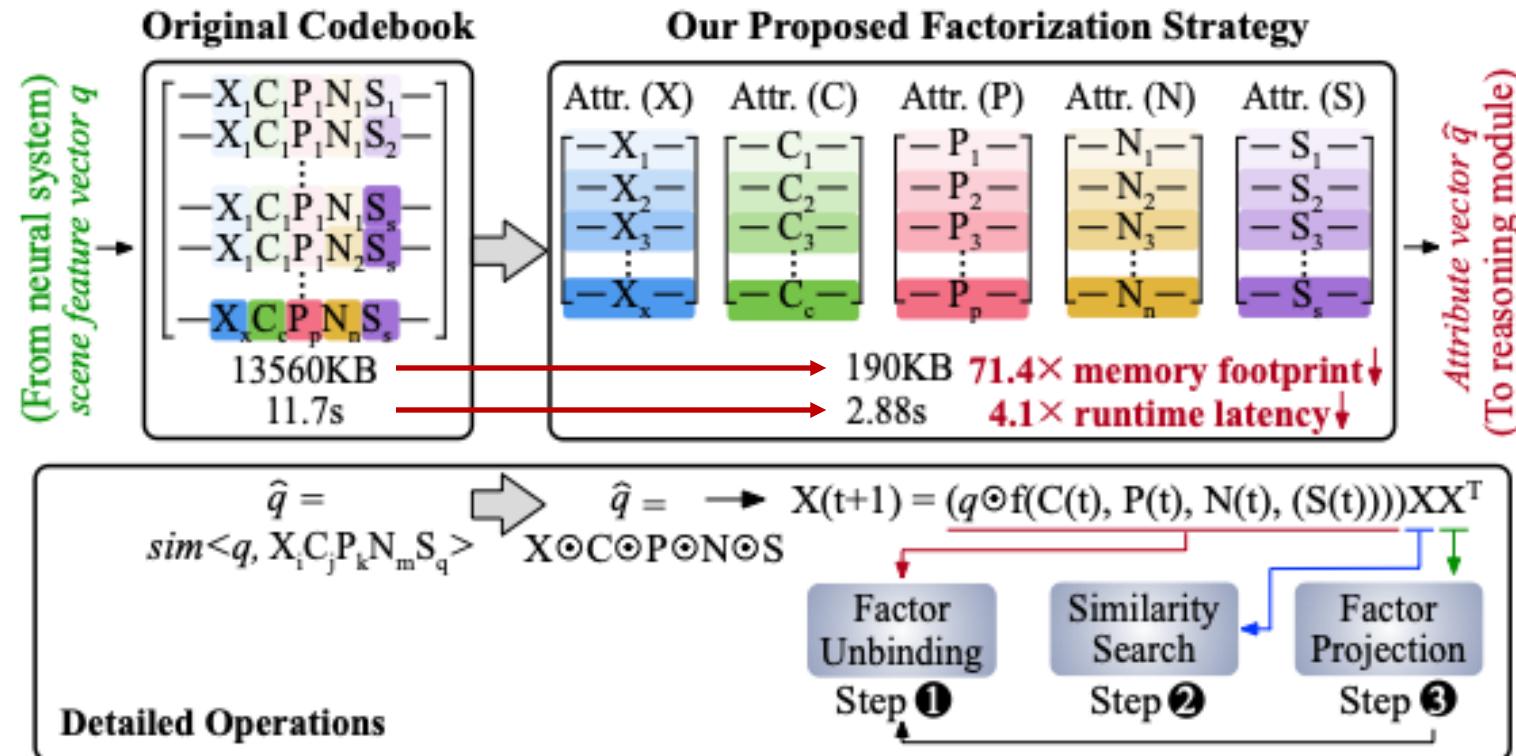
Factorization **disentangles** large symbolic knowledge codebook into small volume of attributes

Algorithm Optimization – Efficient Factorization



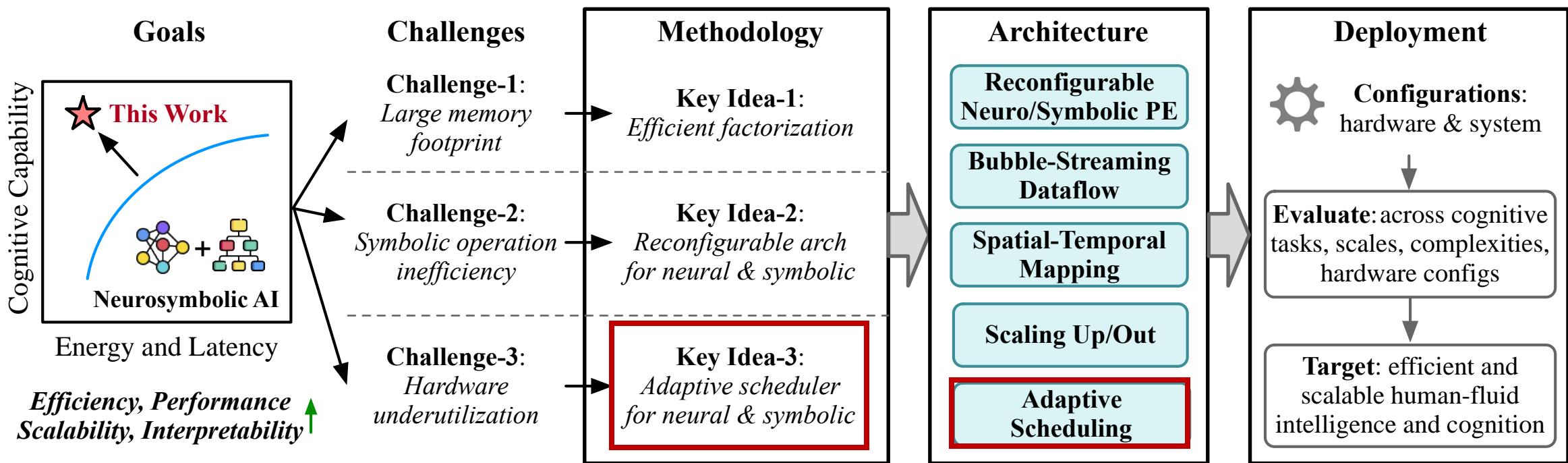
Factorization **disentangles** large symbolic knowledge codebook into small volume of attributes

Algorithm Optimization – Efficient Factorization

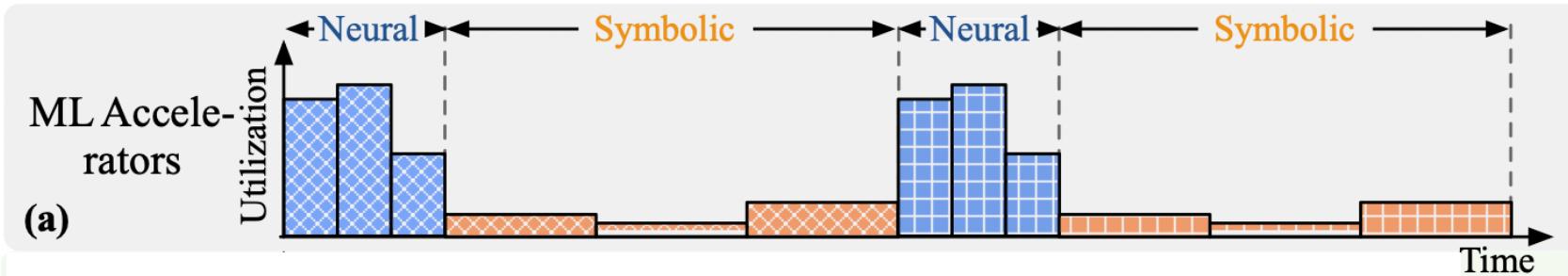


Factorization **disentangles** large symbolic knowledge codebook into small volume of attributes, thus **reducing computational time and space complexity**

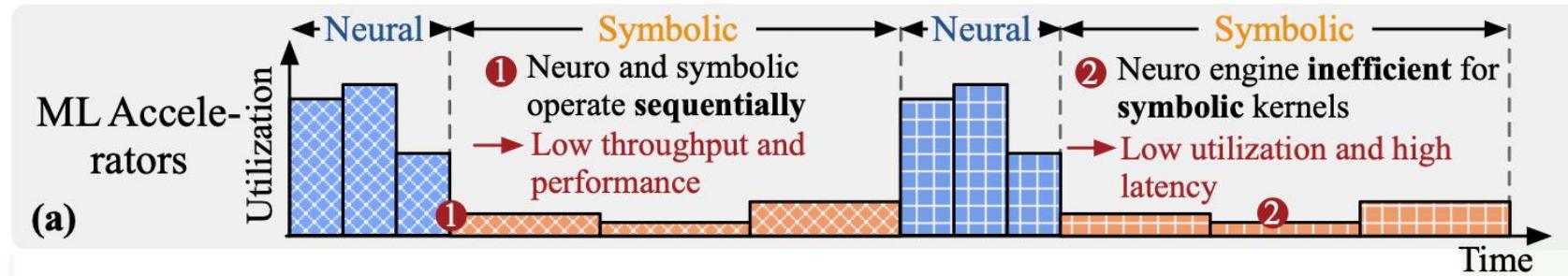
Our Methodology



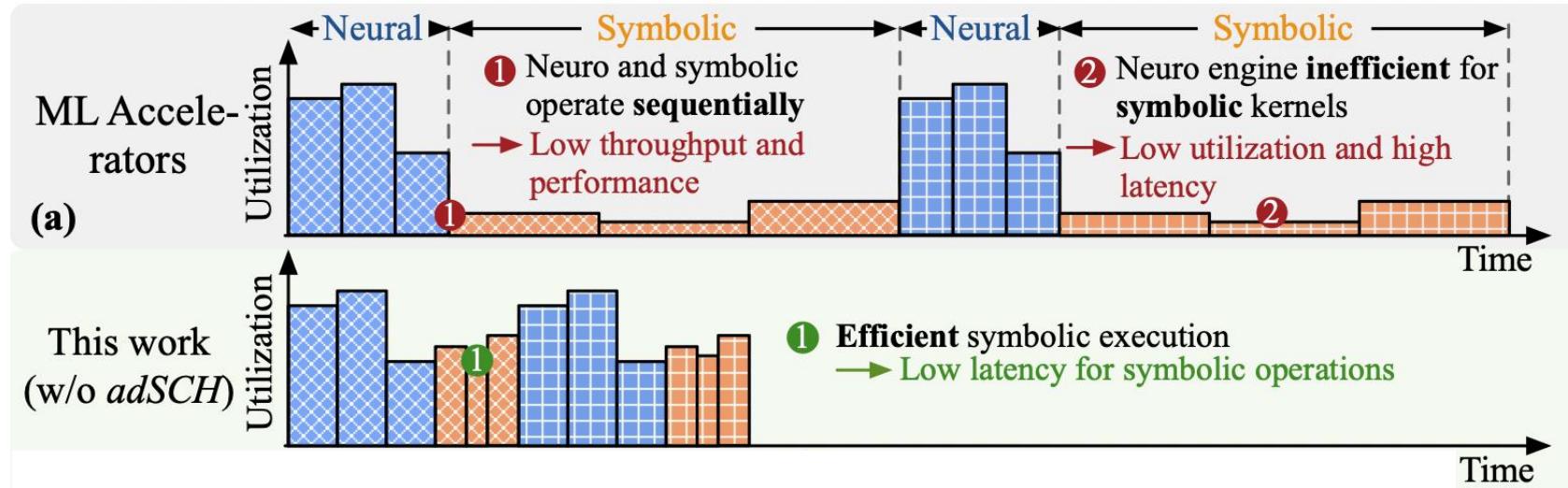
System Optimization - Adaptive Scheduling



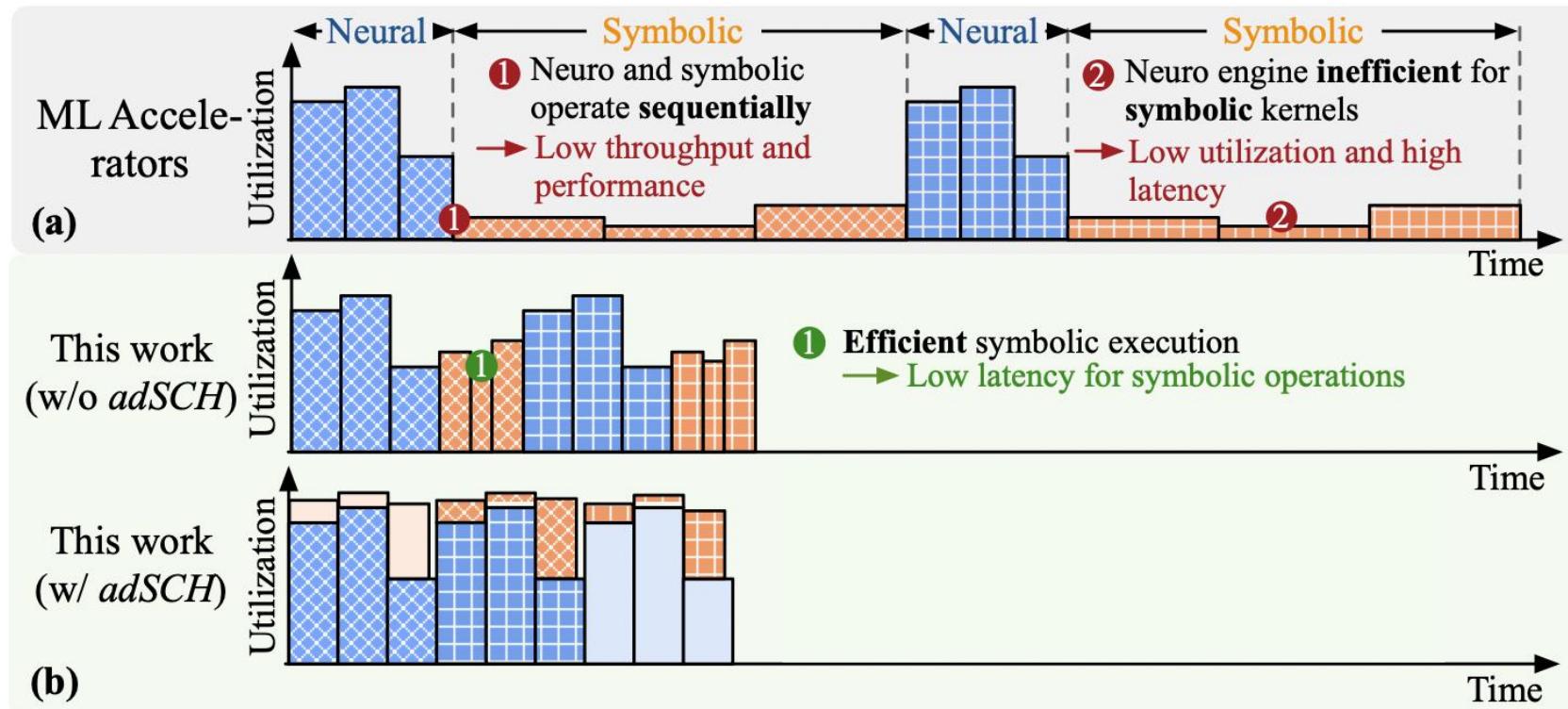
System Optimization - Adaptive Scheduling



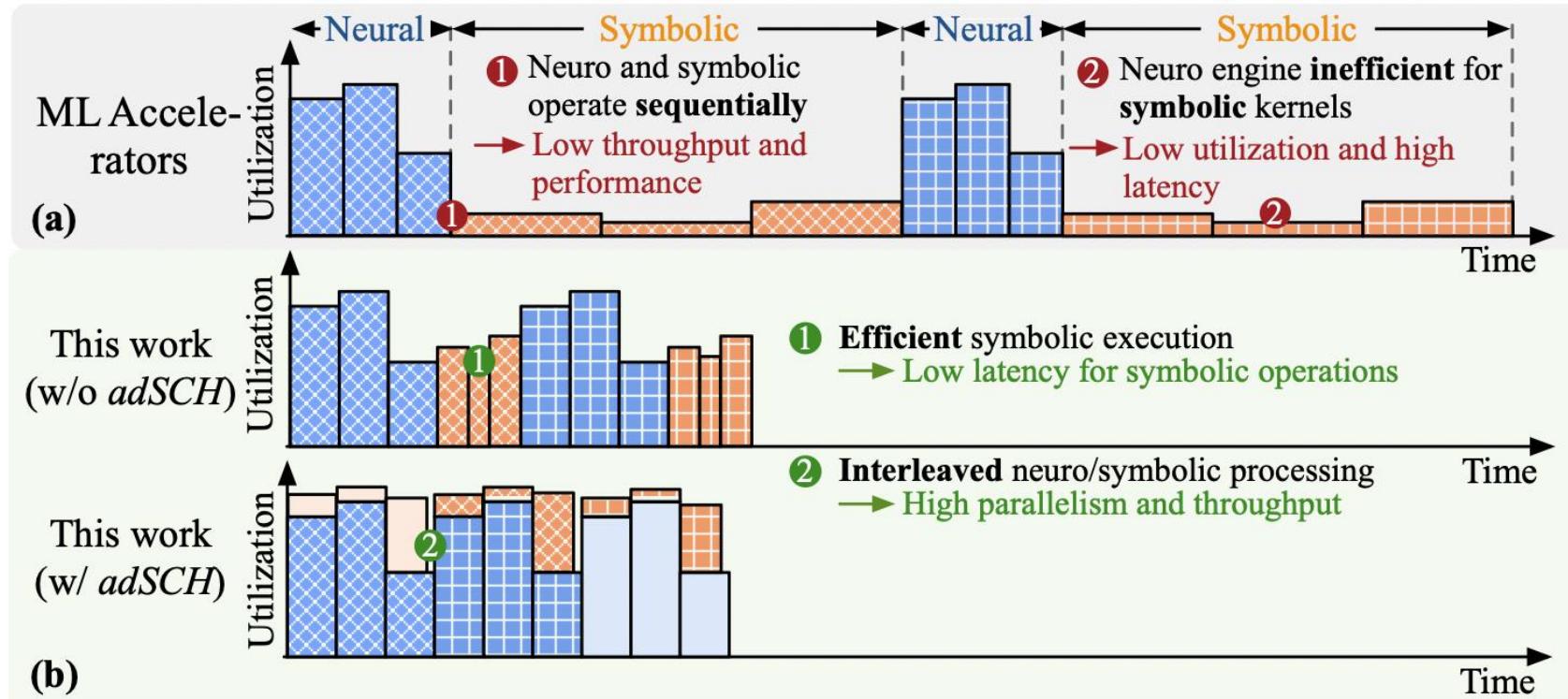
System Optimization - Adaptive Scheduling



System Optimization - Adaptive Scheduling

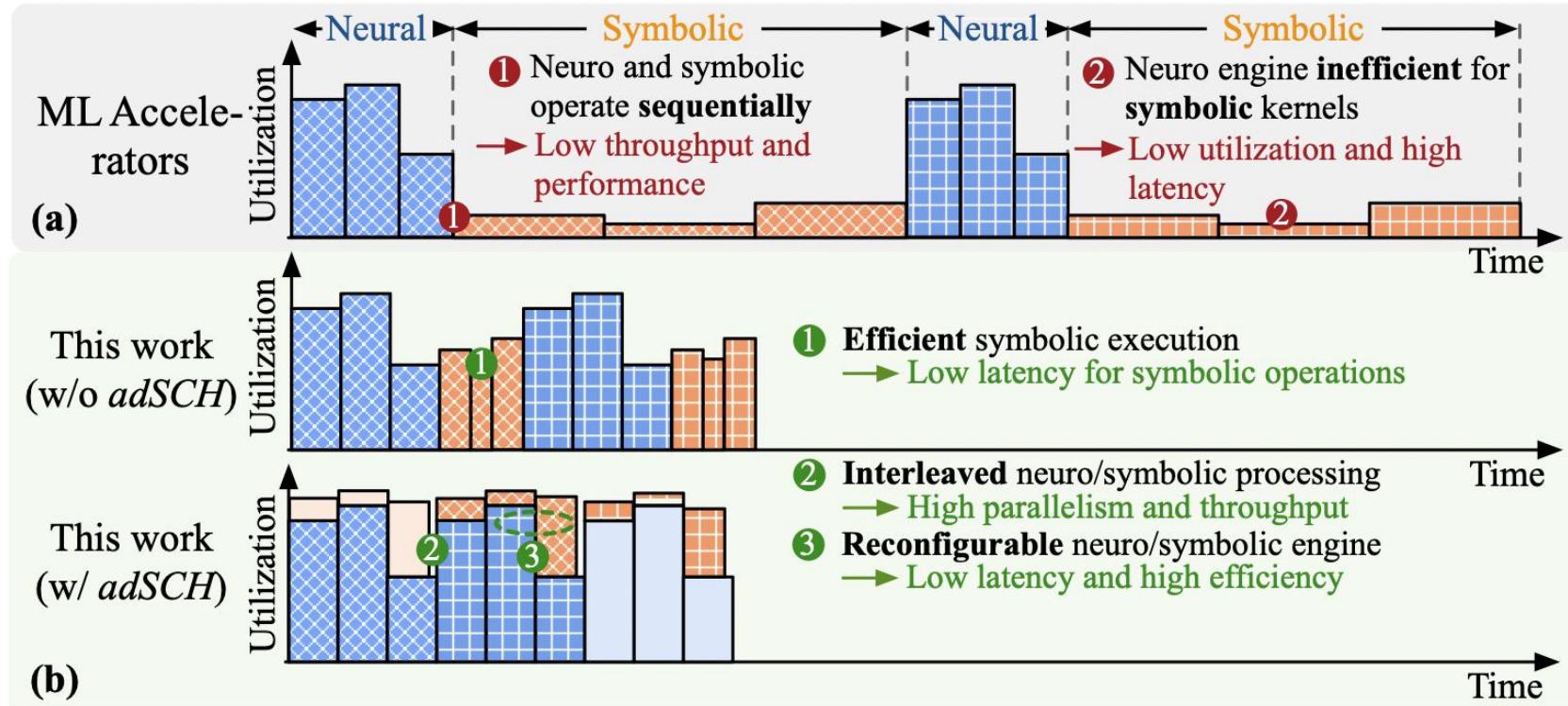


System Optimization - Adaptive Scheduling



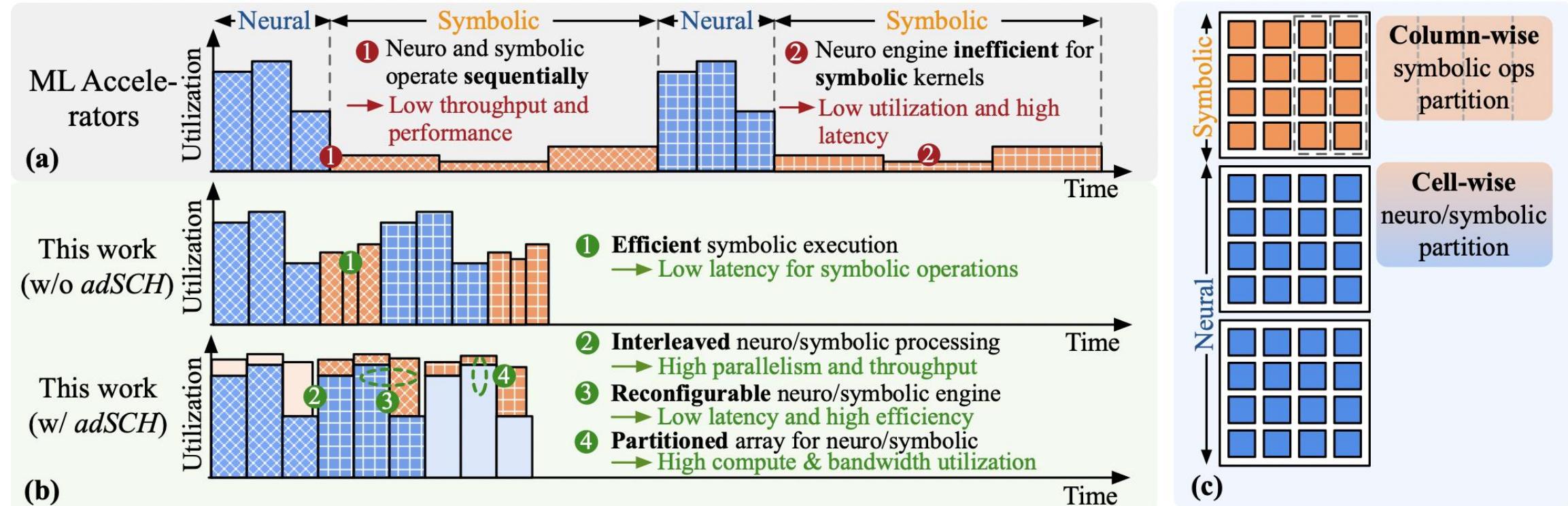
Adaptive scheduling enables **interleaved**

System Optimization - Adaptive Scheduling



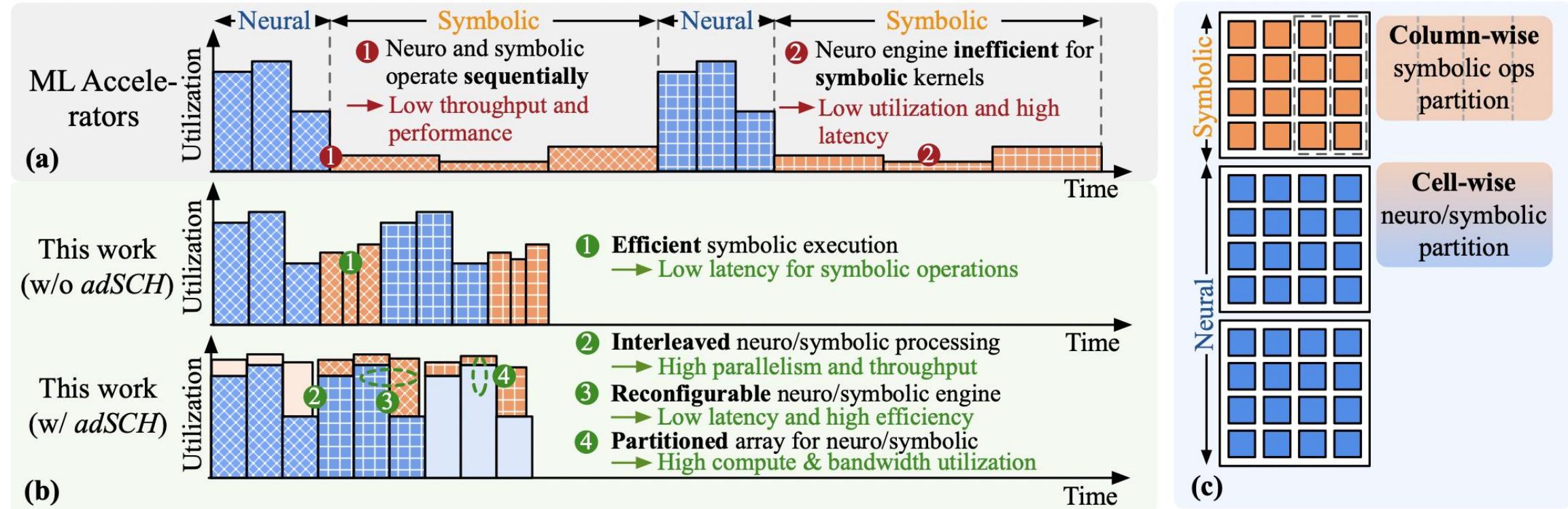
Adaptive scheduling enables **interleaved** and **reconfigurable** neuro/symbolic processing

System Optimization - Adaptive Scheduling



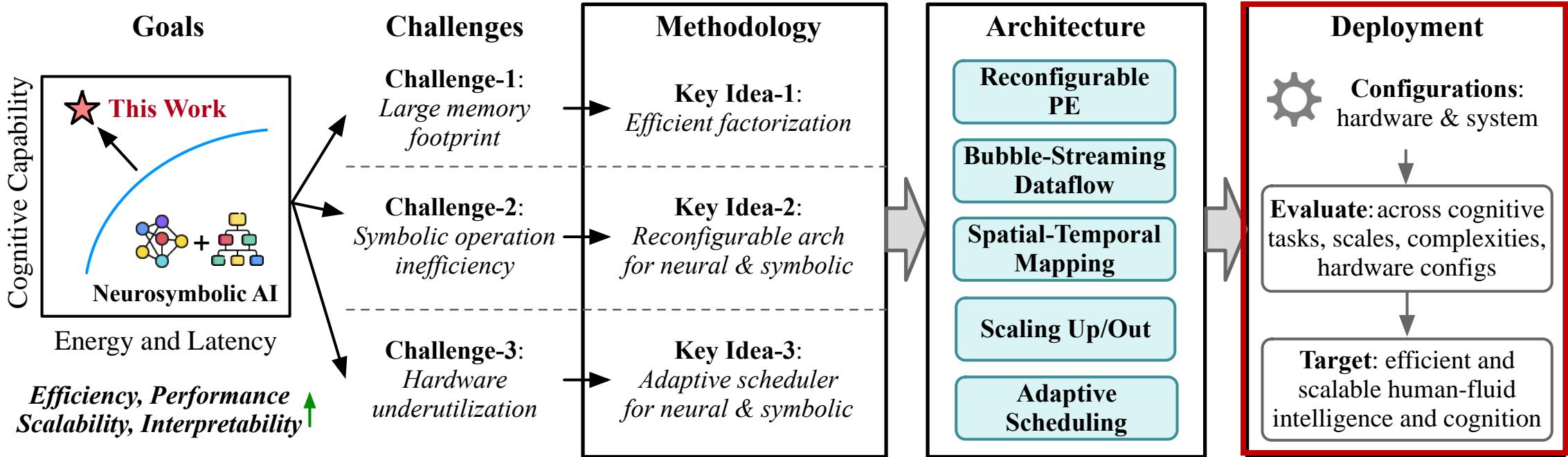
Adaptive scheduling enables **interleaved** and **reconfigurable** neuro/symbolic processing with **partitioned array**

System Optimization - Adaptive Scheduling



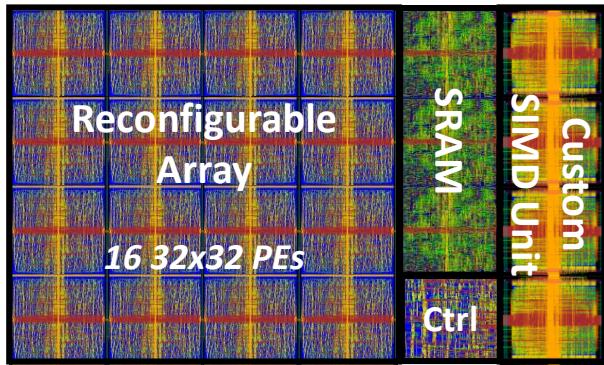
Adaptive scheduling enables **interleaved** and **reconfigurable** neuro/symbolic processing with **partitioned array**, improving parallelism, latency, efficiency, and utilization

Our Methodology



Evaluation – Setup and Accelerator Layout

Layout of Neuro-Symbolic Accelerator



Accelerator Specs

Technology	28 nm	Frequency	600 MHz
#Arrays	16	Voltage	1 V
Size of Each Array	32x32	Power	1.48 W
SRAM	4.5 MB	Area	4.9 mm ²

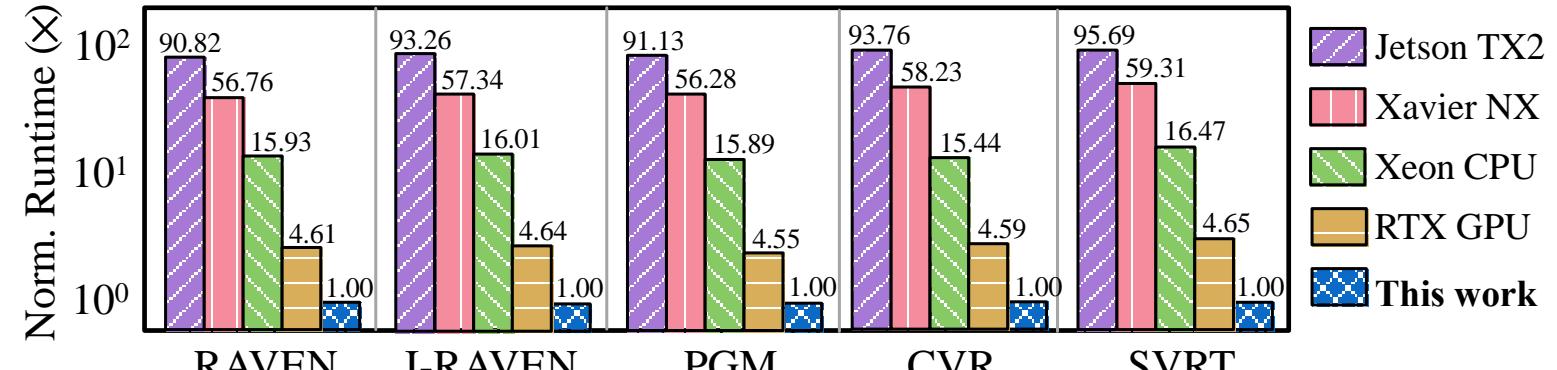
- **Task:** Cognitive reasoning tasks
- **Reasoning datasets:**
 - RAVEN, I-RAVEN, PGM, CVR, SVRT
- **Neuro-symbolic workloads:**
 - NVSA, MIMONet, LVRF
- **Hardware baseline:**
 - Jetson TX2, Xavier NX, RTX GPU, Xeon CPU
 - ML accelerators (TPU, MTIA, Gemmini)

Evaluation – Algorithm Performance

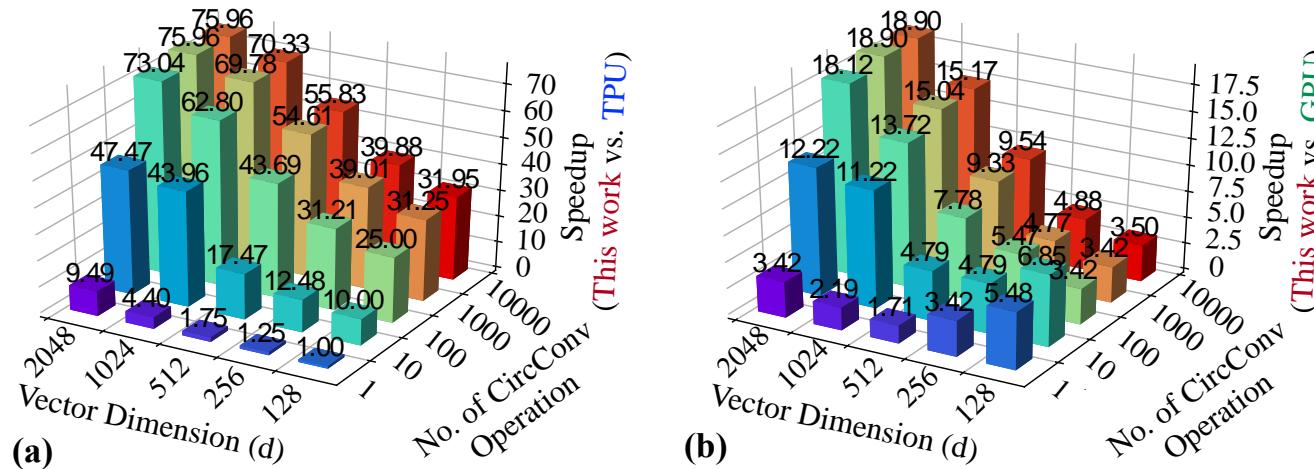
Dataset	Neurosymbolic Model			Non-neurosymbolic		Human
	NVSA	Our Design (+Algo Opt.)	Our Design (+Quant.)	ResNet18	GPT-4	
RAVEN	98.5%	98.9%	98.7%	53.4%	89.0%	84.4%
I-RAVEN	99.0%	99.0%	98.8%	40.3%	86.0%	78.6%
PGM	68.3%	68.7%	68.4%	36.8%	56.0%	N/A
#Parameters	38 MB	32 MB	8 MB	42 MB	1.7 TB	N/A

- **Better Reasoning Capability:** neurosymbolic methods achieve high accuracy across reasoning tasks than NNs and human.
- **Smaller Memory Footprint:** neurosymbolic methods consume much less #parameter than NNs (e.g., LLM).

Evaluation – Hardware Performance

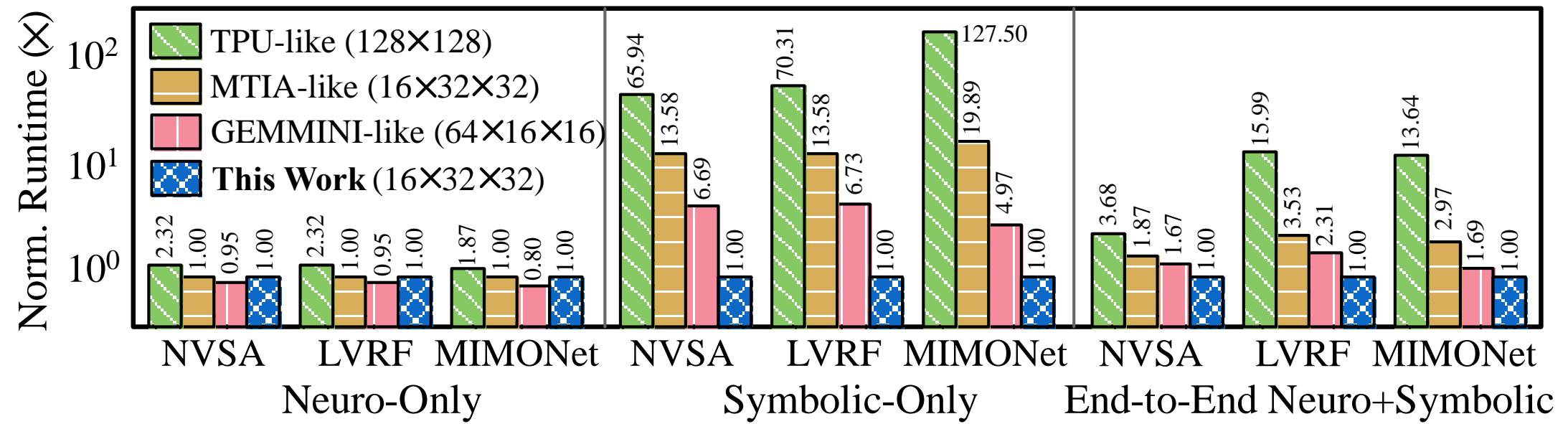


4x - 90x speedup
compared to CPU/GPU



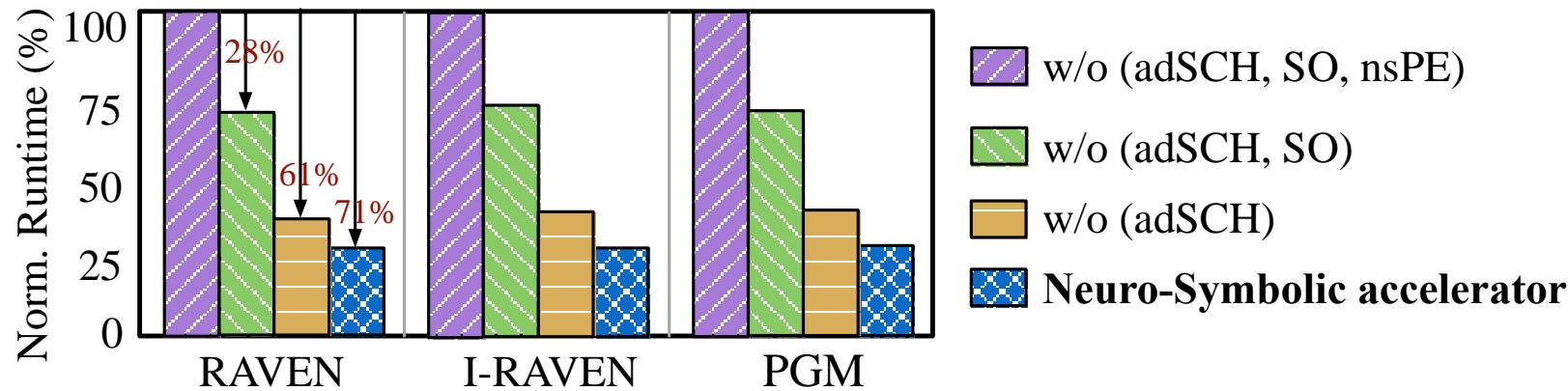
Symbolic operation:
75x speedup to TPU
18x speedup to GPU

Evaluation – Hardware Performance



Compared with ML accelerators: similar neuro latency, **7-120x symbolic speedup**,
2-16x end-to-end neuro-symbolic speedup

Evaluation – Ablation Study



Proposed **scheduling**,
reconfigurable **PE**,
bubble streaming
dataflow are effective

Neurosymbolic Cognitive Solution Algorithm @ Hardware	Normalized Runtime (%) on RAVEN I-RAVEN PGM CVR SVRT				
NVSA @ Xavier NX	100	100	100	100	100
Proposed Algorithm @ Xavier NX	89.5%	88.9%	90.7%	87.6%	88.4%
Proposed Algorithm @ Proposed Accelerator	1.76%	1.74%	1.78%	1.72%	1.69%

**Algorithm-system-
hardware co-design
is critical**



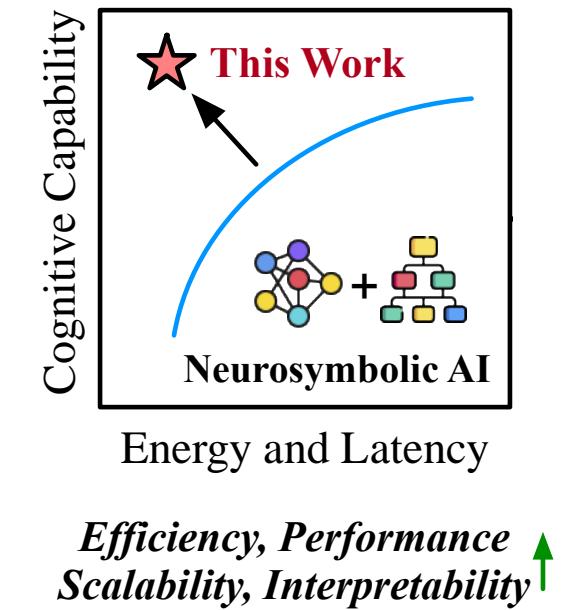
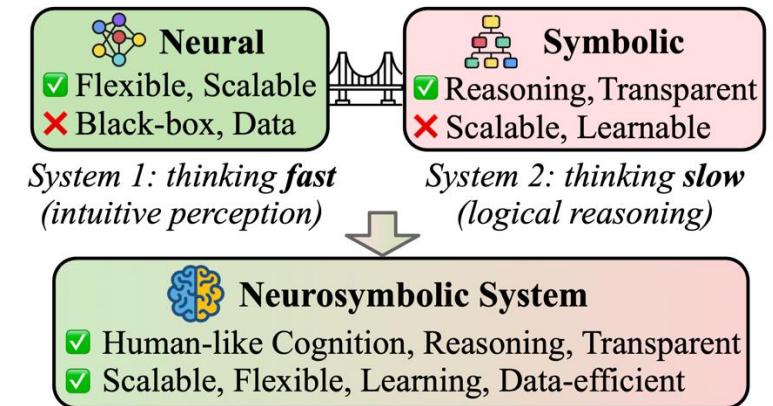
Key Observations:

Compared with systolic arrays that only support neural, our design provides **reconfigurable support for neural and symbolic** operations with **only 4.8% area overhead**.

Our design achieves **0.3s latency** per cognition task, with **1.18W power** consumption.

Summary

- **Neuro-symbolic AI** is a compositional method to improve reasoning and interpretability.
- In this work,
 - Characterize **system implications**
 - Propose **algorithm-system-hardware co-design**
 - **Algorithm**: efficient factorization
 - **System**: adaptive scheduling
 - **Hardware Architecture**: reconfigurable neuro/symbolic PE, dataflow, mapping, and scaling strategy
 - Achieve **efficient and scalable neuro-symbolic** execution across reasoning tasks

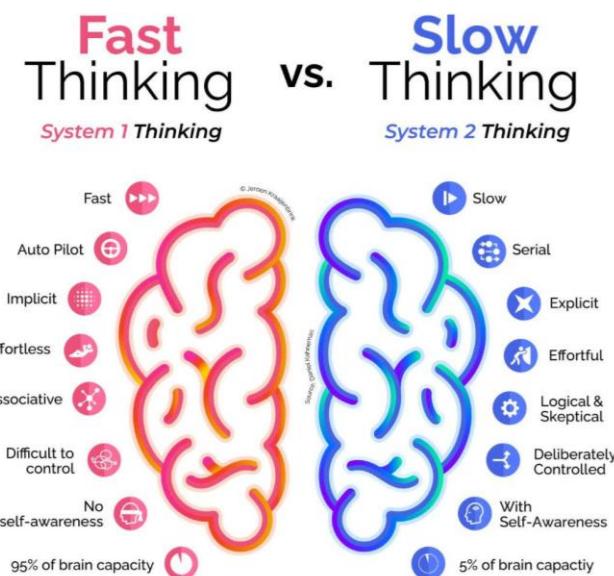
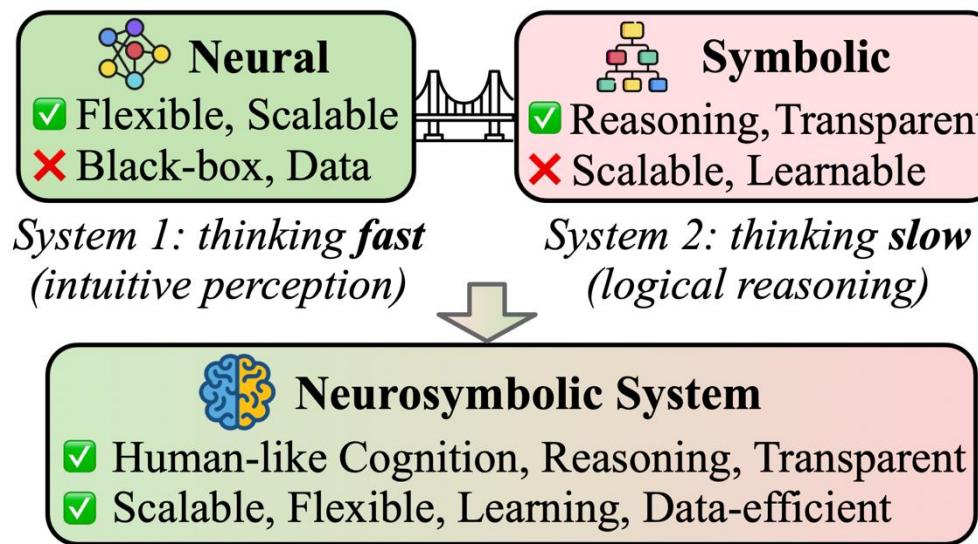


Outline

- Neuro-symbolic AI 101
- Neuro-symbolic AI workload characterization
- Neuro-symbolic AI hardware architecture
- **Final project: neuro-symbolic kernel optimization**
 - https://github.com/sharc-lab/FPGA_ECE8893/tree/main/2025_Spring/topic4

Project: Neuro-Symbolic Kernel Optimization

- Neural (neural networks): learning, flexibility, scalability
- Symbolic (reasoning-based AI): interpretability, data efficiency, reasoning
- **Neuro-Symbolic AI:** integrate **neural** and **symbolic** towards cognitive and trustworthy AI systems



Project: Neuro-Symbolic Kernel Optimization

≡ Google DeepMind

AlphaGeometry: An Olympiad-level AI system for geometry

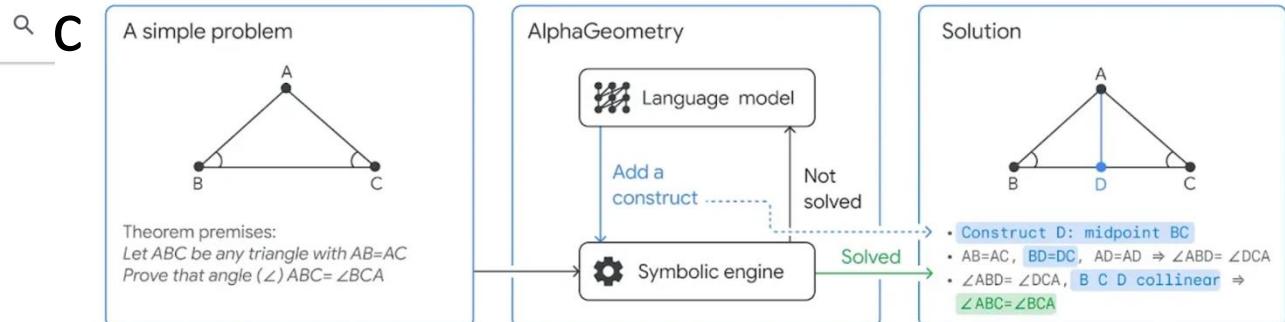
17 JANUARY 2024
Trieu Trinh and Thang Luong
Share



AlphaGeometry adopts a neuro-symbolic approach

AlphaGeometry is a neuro-symbolic system made up of a neural language model and a symbolic deduction engine, which work together to find proofs for complex geometry theorems. Akin to the idea of “thinking, fast and slow”, one system provides fast, “intuitive” ideas, and the other, more deliberate, rational decision-making.

“Solving Olympiad Geometry without Human Demonstrations”, Nature 2024



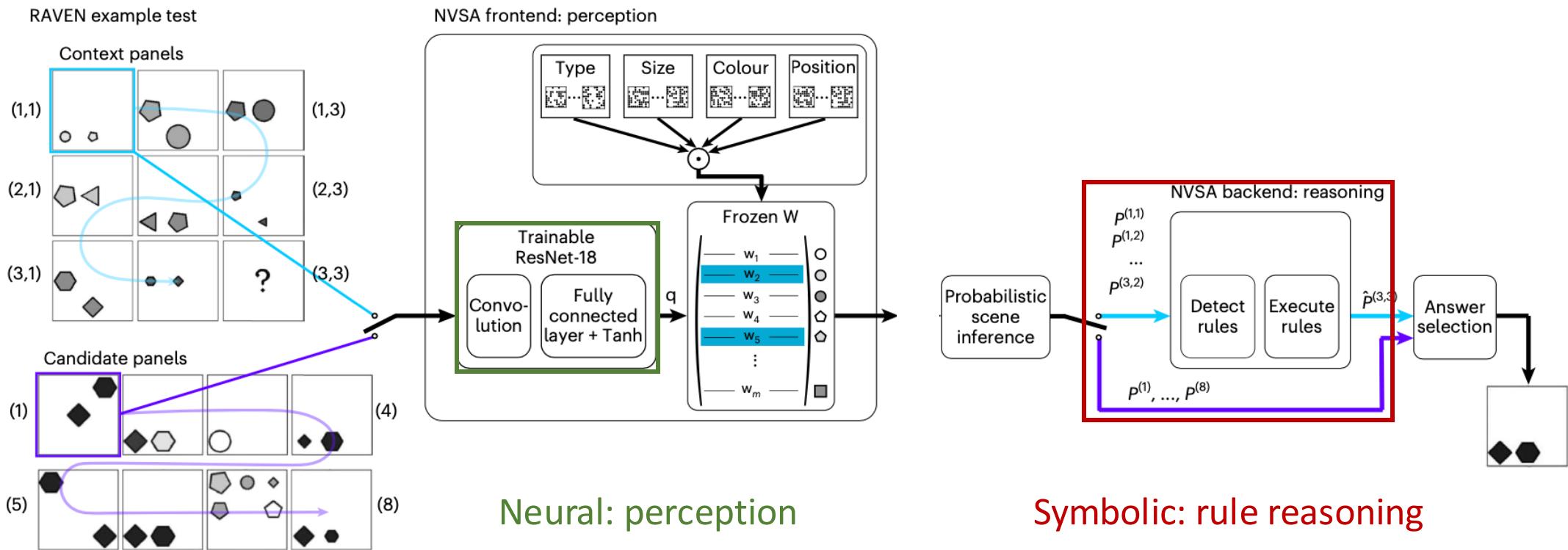
LLM: construct auxiliary points and lines
Symbolic: deductive reasoning

Eval on 30 Int. Math Olympics (IMO) problems:

- GPT-4: 0/30
- AlphaGeometry (Neuro-Symbolic): 25/30
- Human Gold Medalist: 26/30

Project: Neuro-Symbolic Kernel Optimization

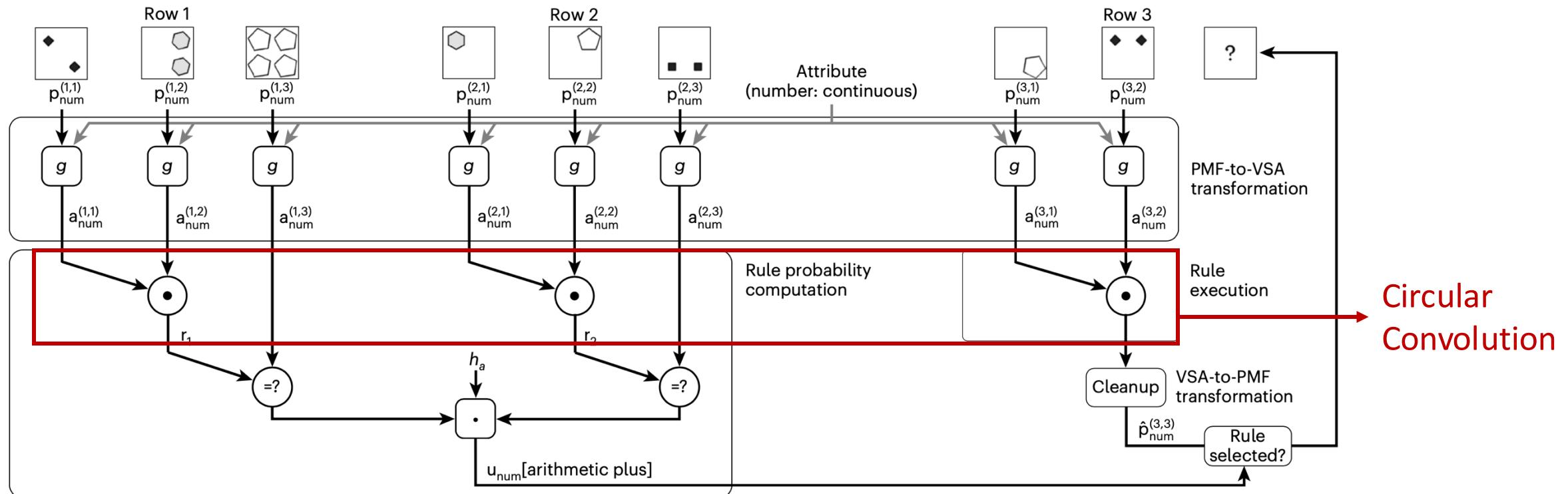
- Neuro-Vector-Symbolic Architecture



"A neuro-vector-symbolic architecture for solving Raven's progressive matrices.", Nature Machine Intelligence, 2023

Project: Neuro-Symbolic Kernel Optimization

Solving the arithmetic plus rule on number attribute with vector-symbolic reasoning



Hersche, et al, "A neuro-vector-symbolic architecture for solving Raven's progressive matrices.", Nature Machine Intelligence, 2023

Project: Neuro-Symbolic Kernel Optimization

- Part 1: Neural Kernel (One layer of ResNet18)
 - Reference code: <neural/neural_conv.py>
 - Input format: <neural/neural_input/input.npy>
 - Output data: <neural/neural_output/output.npy>
- Part 2: Symbolic Kernel (Circular Convolution)
 - Reference code: <symbolic/symbolic_circular_conv.py>
 - Input format: <symbolic /symbolic_input/input_A.npy, input_B.npy>
 - Output data: <symbolic/symbolic_output/output_C.npy>
- Bonus!
 - **Bonus 1:** Design a complete version of ResNet18 (Reference: [SkyNet](#))
 - **Bonus 2:** Design a kernel that is reconfigurable to support convolution (neural) and circular convolution (symbolic)

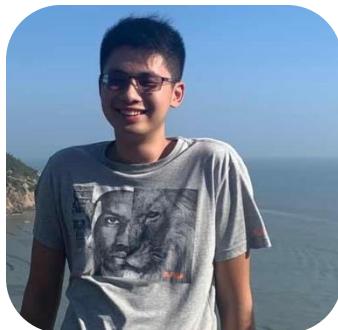
Acknowledgements



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Raychowdhury



Prof. Tushar
Krishna



Dr. Ananda
Samajdar





Towards Cognitive AI Systems: Workload Characterization and Hardware Architecture for Neuro-Symbolic AI

Zishen Wan

PhD Student @ School of ECE, Georgia Tech

Email: zishenwan@gatech.edu

Webpage: <https://zishenwan.github.io>

Guest Lecture @ ECE 8893, Parallel Programming for FPGAs
January 28, 2025

