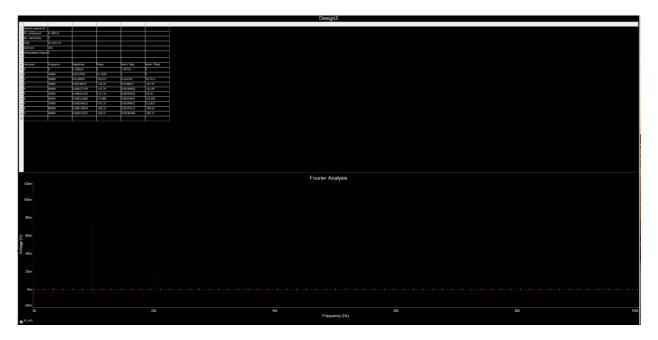
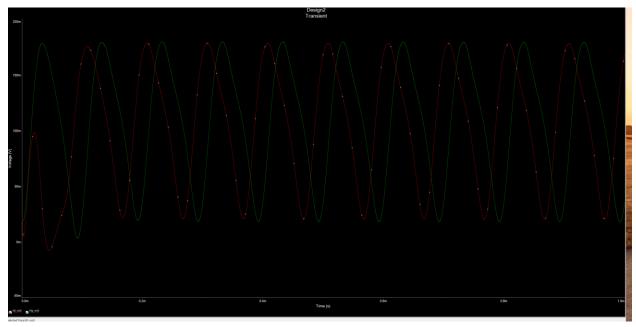


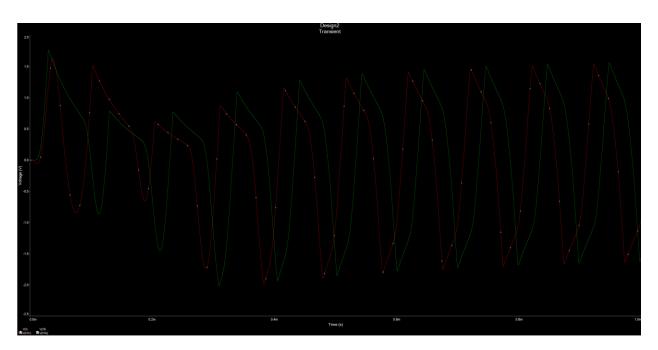
Mixer Q Fourier analysis



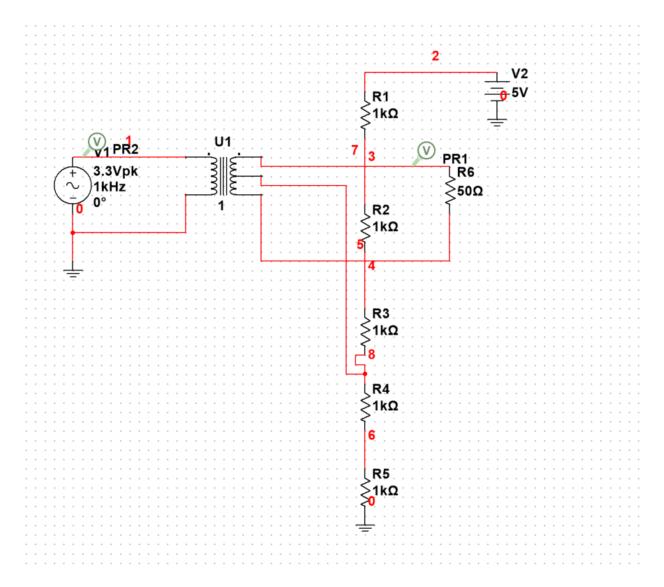
Mixer I Fourier analysis



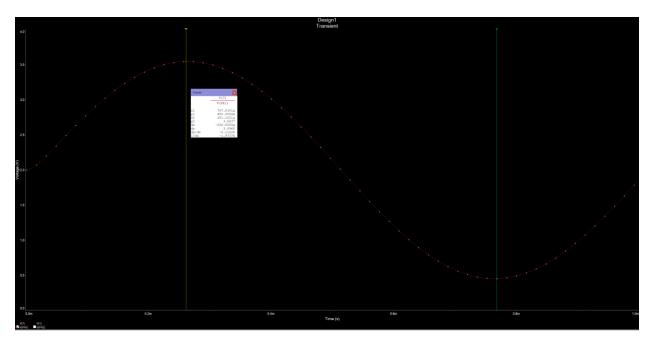
mixer IQ transient simulation



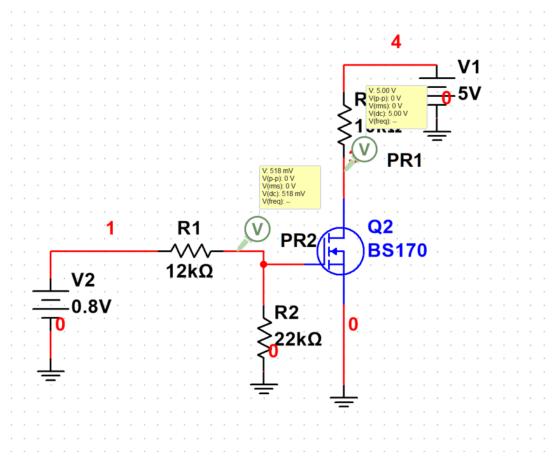
Subsystem B output



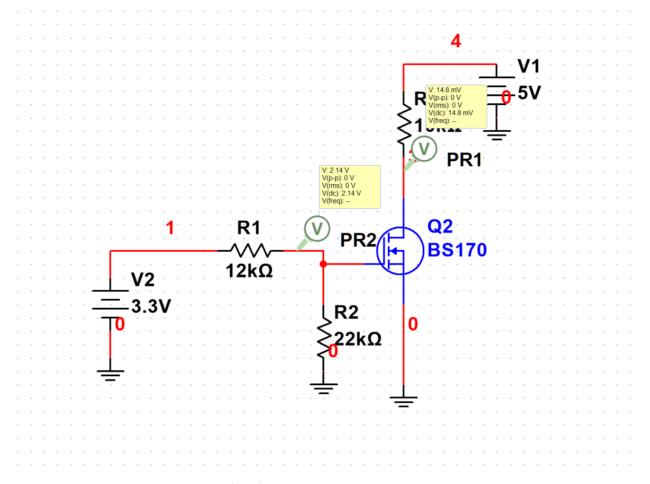
LO Bias circuit -bias to 2v (FST3253 minimum HIGH)



LO biased signal output (FST3253 max LOW =0.8v, max HIGH=5.5v)



TX Enable circuits when TXEN LOW (0.8v) with 5v output to disable demux



TX Enable circuits when TXEN HIGH (3.3v) with 14.8mV output to enable demux