A picture containing text

Description automatically generated

Mixer Q Fourier analysis

Text

Description automatically generated

Mixer I Fourier analysis

Background pattern

Description automatically generatedmixer IQ transient simulation

A screenshot of a computer

Description automatically generated with low confidence

Subsystem B output

Diagram, schematic

Description automatically generated

LO Bias circuit -bias to 2v (FST3253 minimum HIGH)

Graphical user interface

Description automatically generated

LO biased signal output (FST3253 max LOW =0.8v, max HIGH=5.5v)

Diagram, schematic

Description automatically generated

TX Enable circuits when TXEN LOW (0.8v) with 5v output to disable demux

Diagram, schematic

Description automatically generated

TX Enable circuits when TXEN HIGH (3.3v) with 14.8mV output to enable demux