

- Assumptions:**
- > 8-bit Multiplier can be built from full adder circuits
  - > Cheapest/fastest implementation of  $2^n$  is a lookup table
  - > X0, Y0, Z0 are loaded with initial values according to state diagram
  - > All 8-bit buses are fixed point
  - > if inverter en pin is disabled, then the inverter acts as a buffer



