

1. What's the advantage and disadvantage of the Miller Divider over CML Logic?

- ❑ With a shorter delay, Miller divider can provide a higher speed than CML logic
- ❑ However, that the divider loop requires some cycles to reach steady state, i.e., it does not divide correctly instantaneously

2. Analyze the circuit of Fig. 11.1 if S1 is turned on when CK is at high level and S2 is turned on when CK is at low level, Redraw the waveform at node D_{out} .

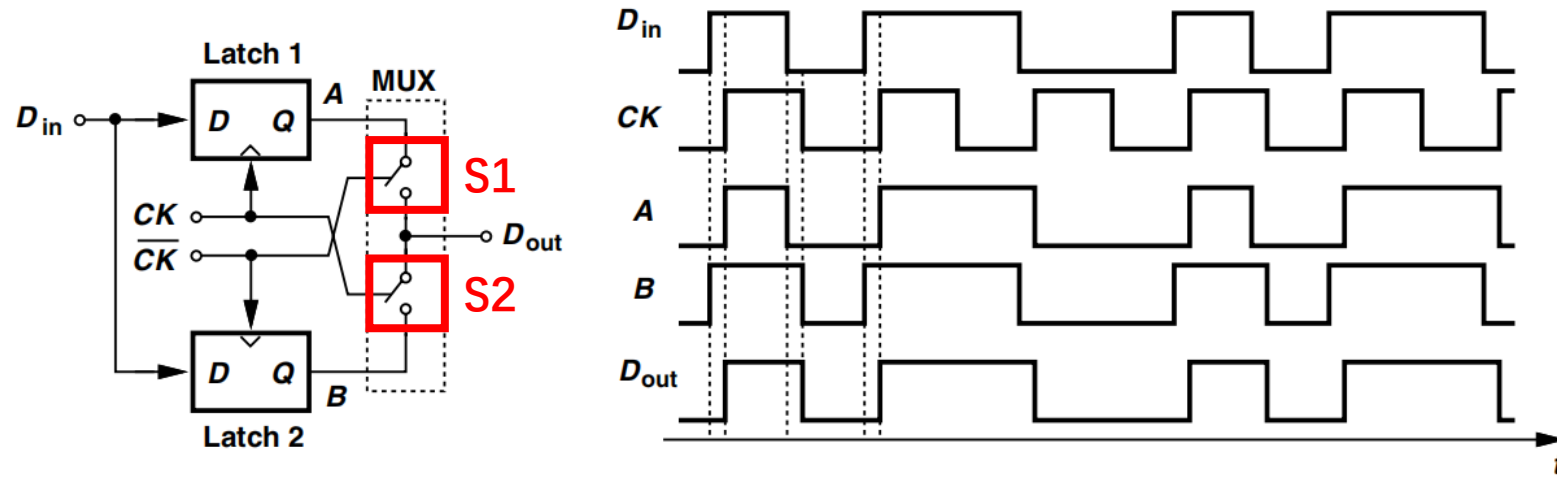


Fig. 11.1

Previously, when CK is high B is selected.
Now, when CK is high. A is selected.

