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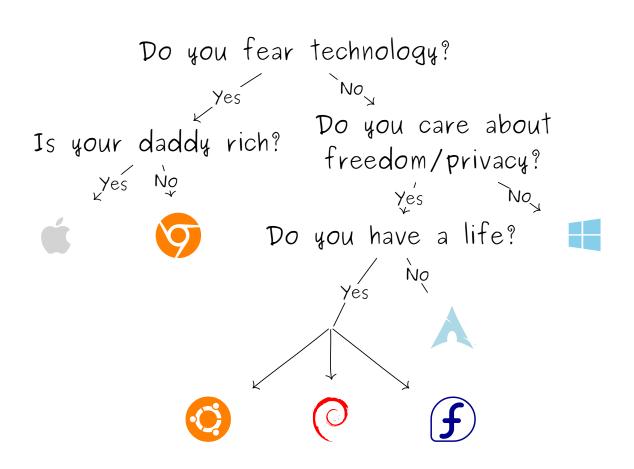


Fig. 1: Choosing an OS

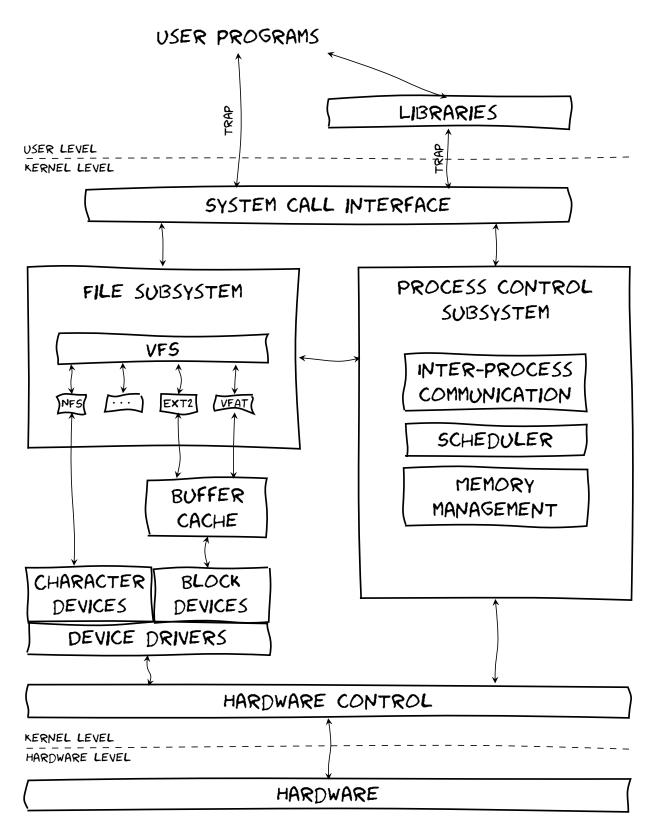


Fig. 2: OS overview

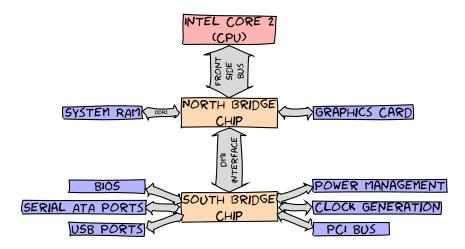


Fig. 3: Motherboard chipsets

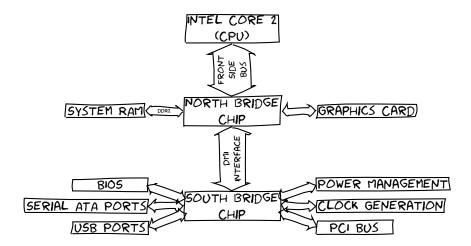


Fig. 4: Motherboard chipsets (bw version)



Fig. 5: CPU's working cycle

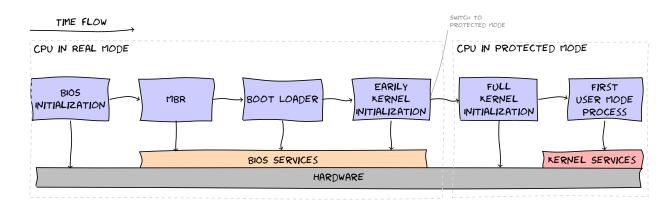


Fig. 6: Bootstrapping

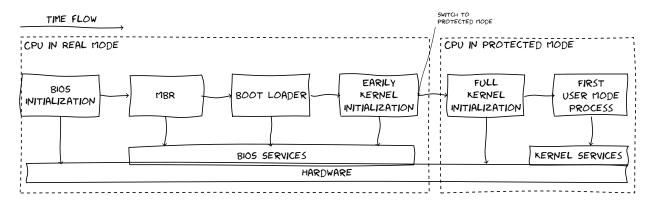


Fig. 7: Bootstrapping (bw version)

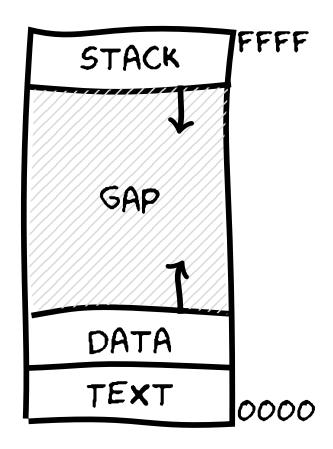


Fig. 8: Process' virtual address space

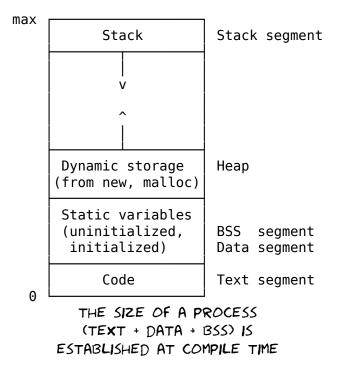


Fig. 9: UNIX view of a process

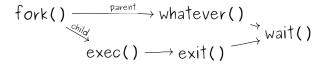


Fig. 10: Process creation

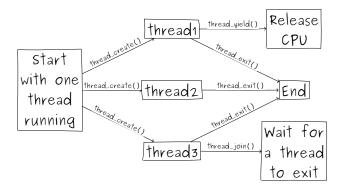


Fig. 11: Thread operations

```
typedef int semaphore;
 semaphore resource_1;
                                semaphore resource_1;
 semaphore resource_2;
                                semaphore resource_2;
 void process_A(void) {
                                void process_A(void) {
     down(&resource_1);
                                    down(&resource_1);
     down(&resource_2);
                                    down(&resource_2);
     use_both_resources( );
                                    use_both_resources( );
     up(&resource_2);
                                    up(&resource 2);
     up(&resource_1);
                                    up(&resource_1);
}
                                }
                                void process_B(void) {
 void process_B(void) {
     down(&resource_1);
                                    down(&resource_2);
     down(&resource_2);
                                    down(&resource_1);
     use_both_resources();
                                    use_both_resources();
     up(&resource_2);
                                    up(&resource_1);
     up(&resource_1);
                                    up(&resource_2);
}
                                }
         (a)
                                             (b)
```

Fig. 12: Deadlock — Resource issues

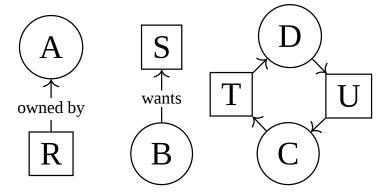


Fig. 13: Deadlock notions

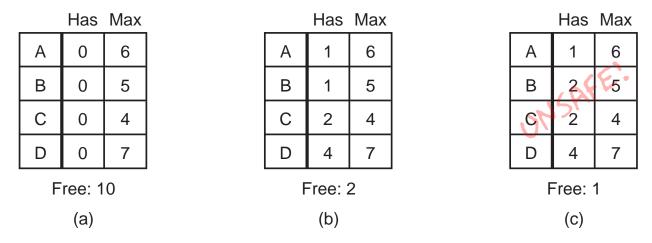


Fig. 14: Deadlock — Banker algorithm

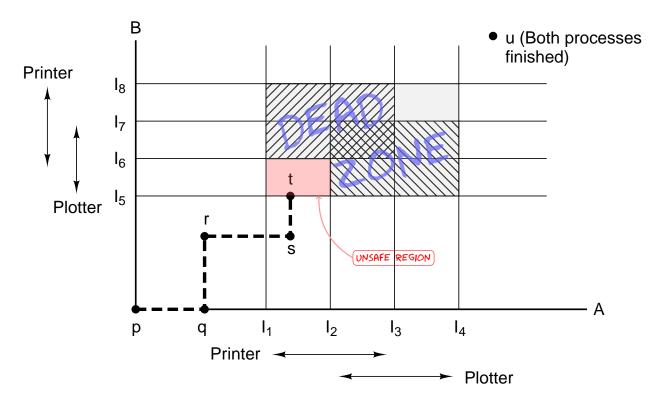


Fig. 15: Deadlock avoidance

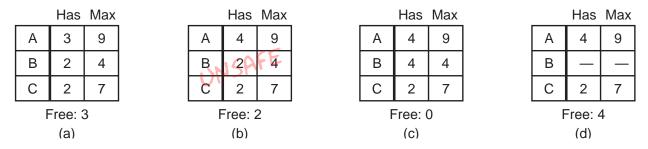


Fig. 16: Deadlock avoidance

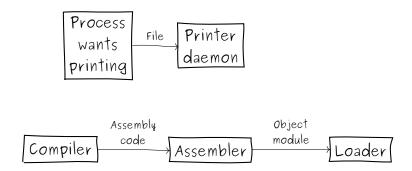


Fig. 17: Producers and consumers

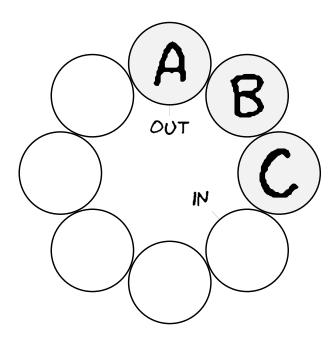


Fig. 18: A circular array

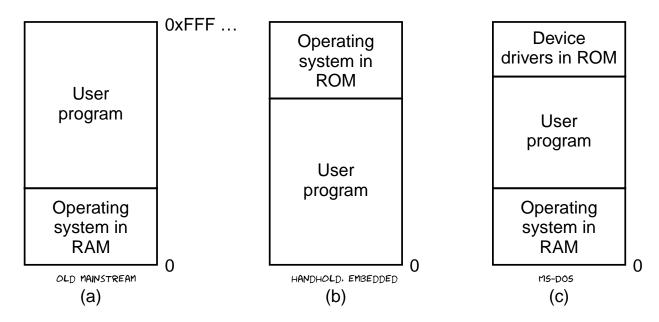


Fig. 19: Real mode memory layouts

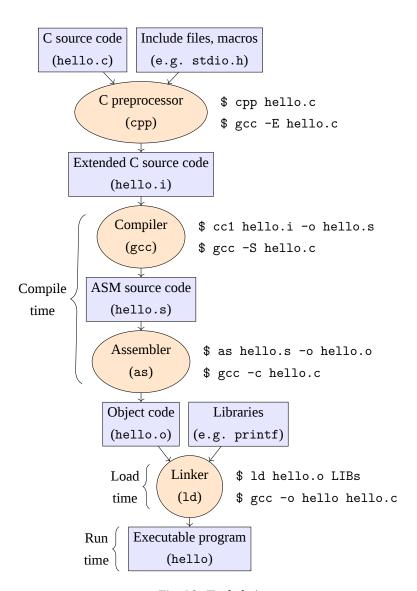


Fig. 20: Tool chain

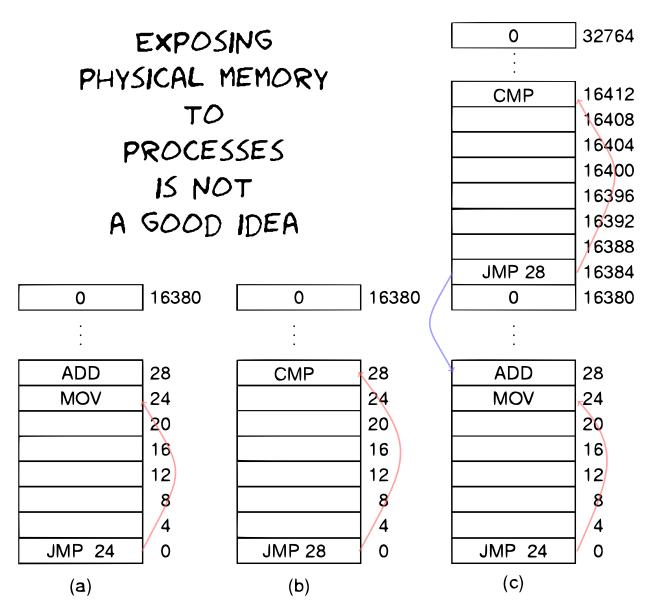


Fig. 21: Relocation

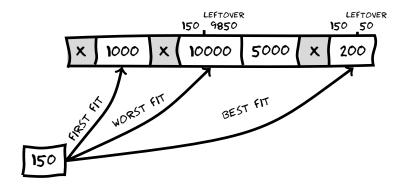


Fig. 22: First fit, best fit, worst fit

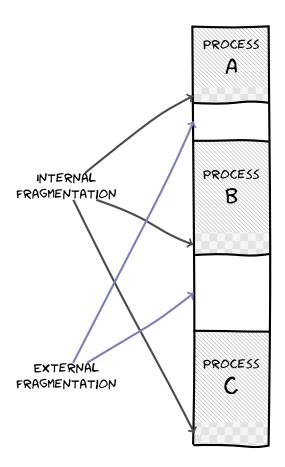


Fig. 23: Memory fragmentation

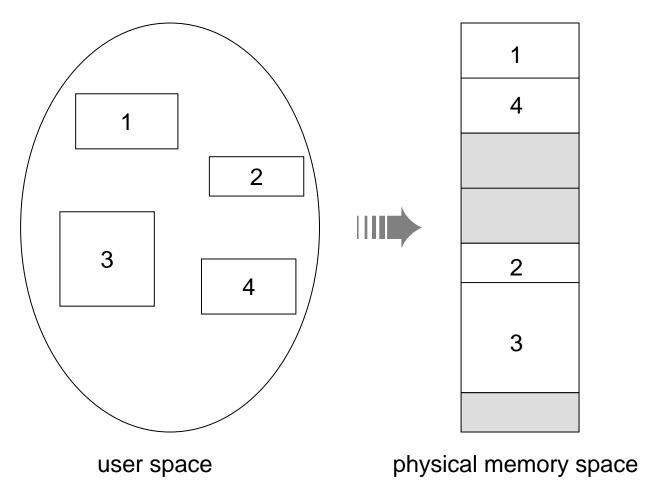


Fig. 24: Memory segmentation

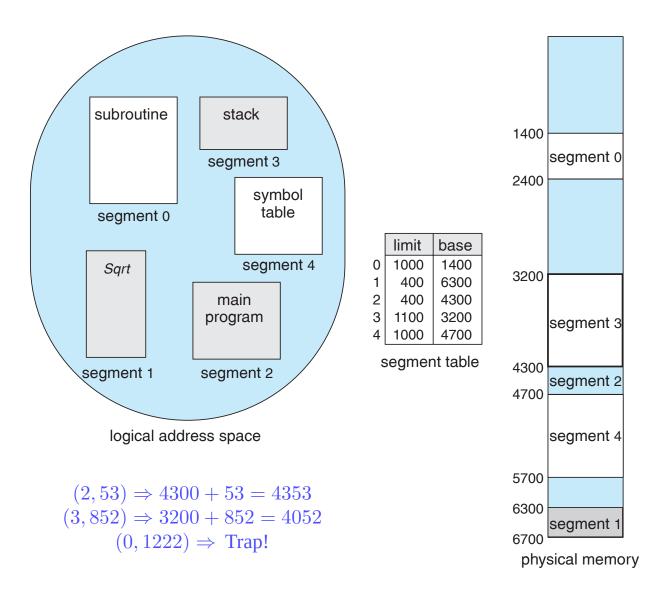


Fig. 25: Memory segmentation — Address translation

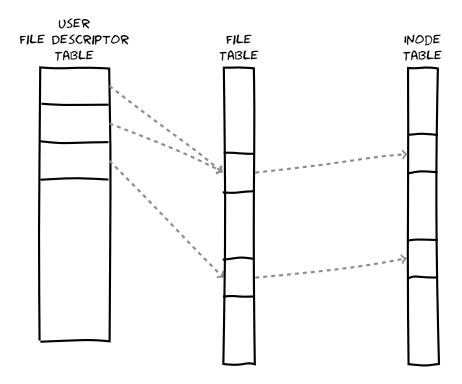


Fig. 26: File system tables

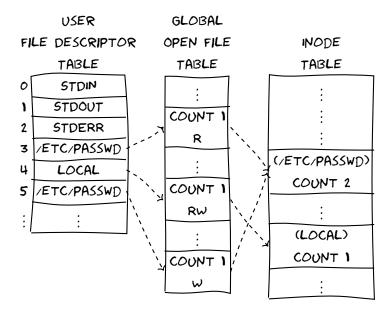


Fig. 27: File tables

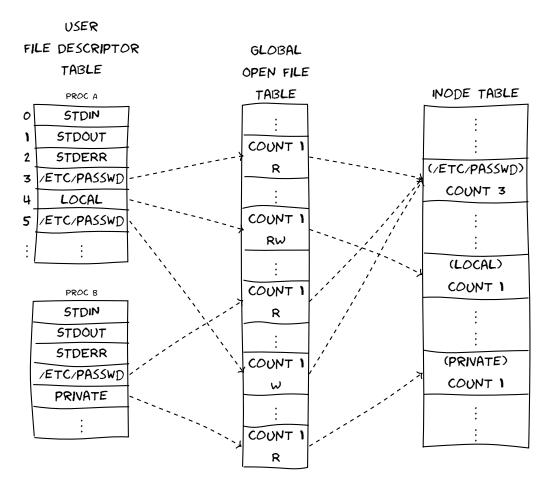


Fig. 28: File tables

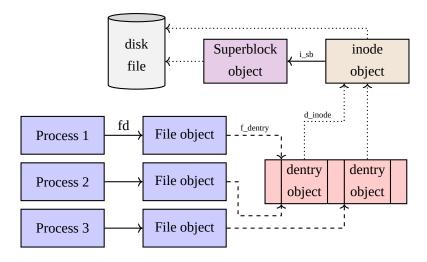


Fig. 29: VFS objects

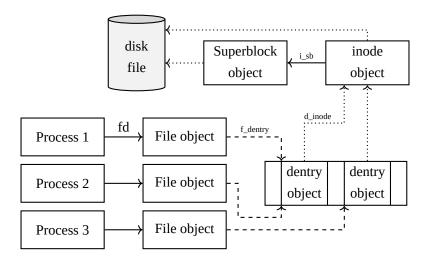


Fig. 30: VFS objects

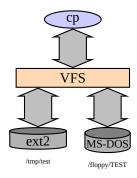


Fig. 31: VFS file copy

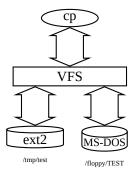


Fig. 32: VFS file copy (bw version)

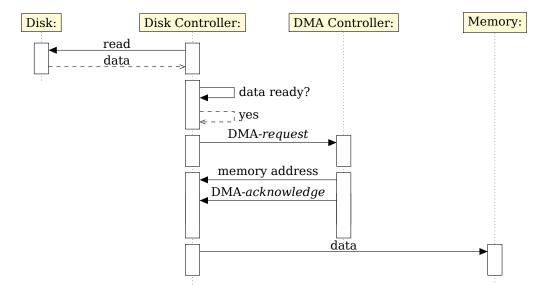


Fig. 33: DMA handshaking

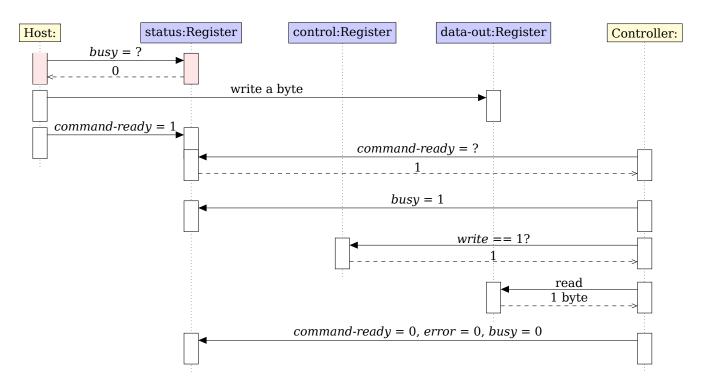


Fig. 34: Handshaking