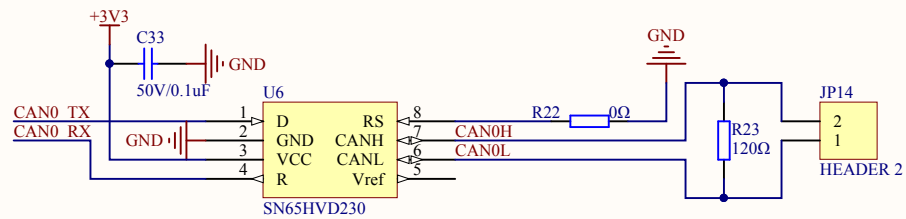
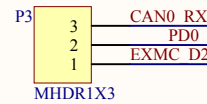
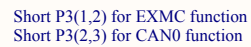
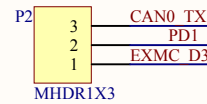
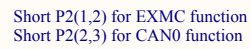


| | | |
|--------------------------|-----------------|---------------|
| Company Name: GigaDevice | | |
| File Name: AD_DA | | |
| Revision: 1.0 | Data: 2013-8-20 | Author: XuFei |

CAN



Company Name: GigaDevice

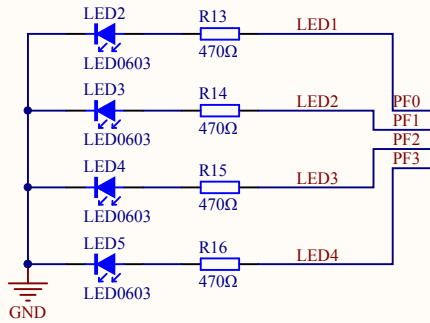
File Name: CAN

Revision: 1.0

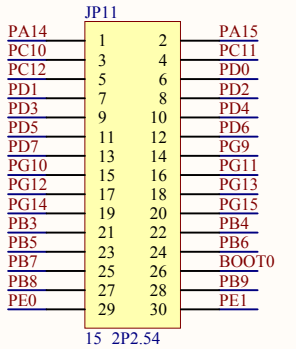
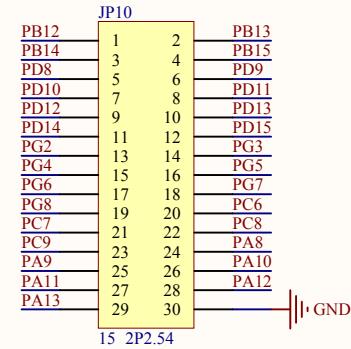
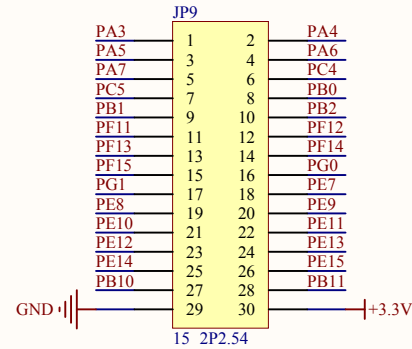
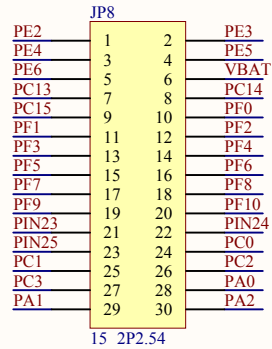
| |
|-----------------|
| Data: 2013-7-24 |
|-----------------|

Author: XuFei

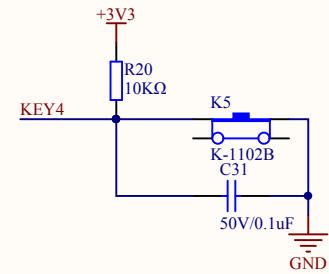
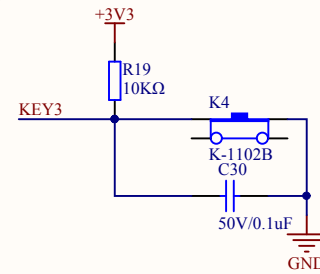
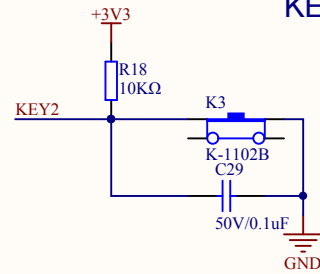
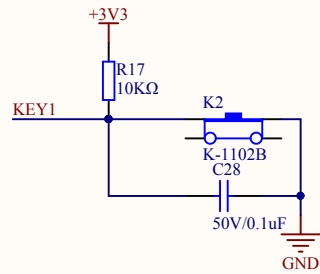
LED



Extension Pin



KEY



PA0 KEY1
PC13 KEY2
PF5 KEY3
PF4 KEY4

Company Name: GigaDevice

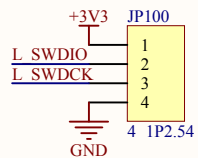
File Name: Extension

Revision: 1.0

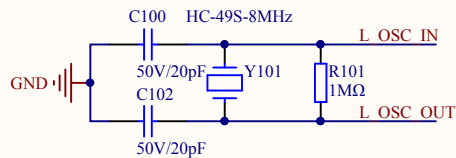
Data: 2013-8-20

Author: XuFei

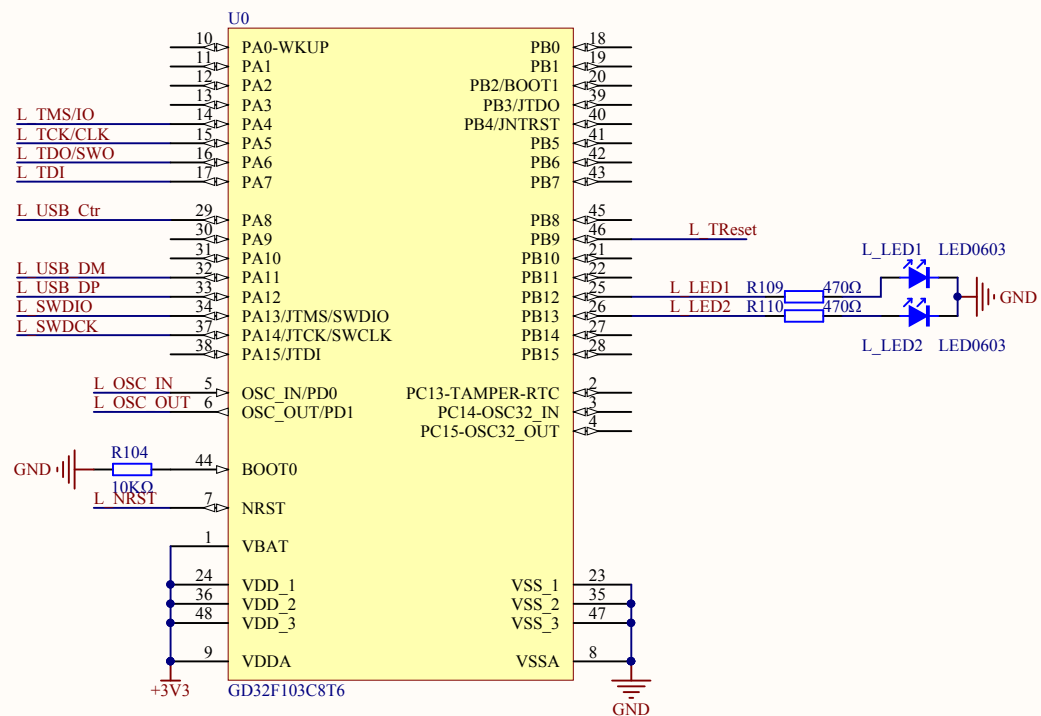
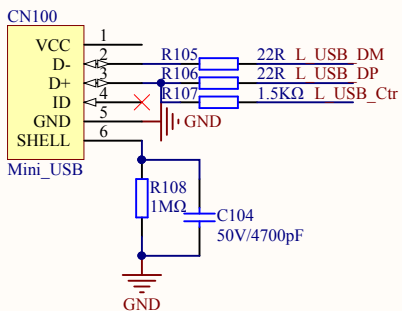
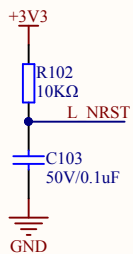
MCU SWD



HXTAL



Reset



Company Name: GigaDevice

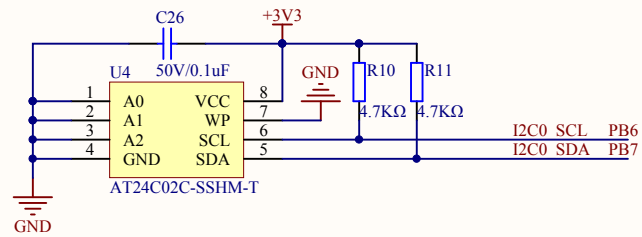
File Name: GDLink

Revision: 1.0

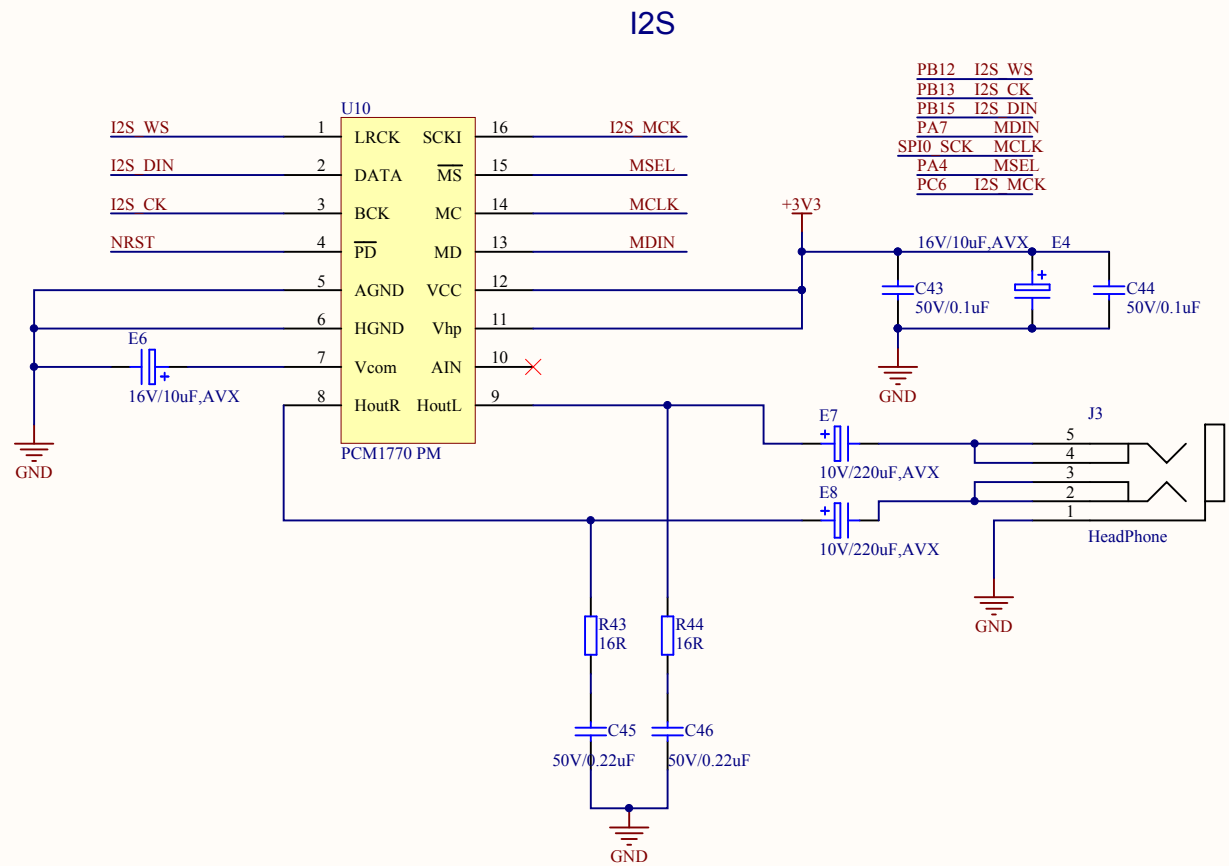
Data: 2015-01-09

Author: XuFei

I2C



| | | |
|--------------------------|-----------------|---------------|
| Company Name: GigaDevice | | |
| File Name: I2C_SMbus | | |
| Revision: 1.0 | Data: 2013-8-20 | Author: XuFei |



| | | |
|--------------------------|---------------|---------------|
| Company Name: GigaDevice | | |
| File Name: I2S | | |
| Revision: 1.1 | Data: 2015-08 | Author: Xufei |

A

B

C

D

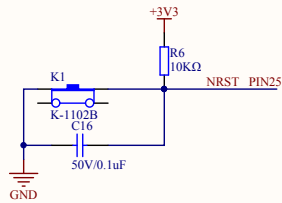
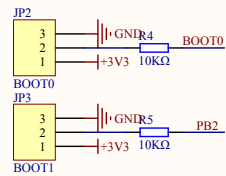
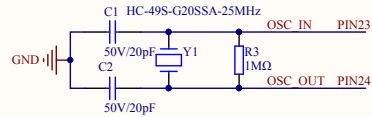
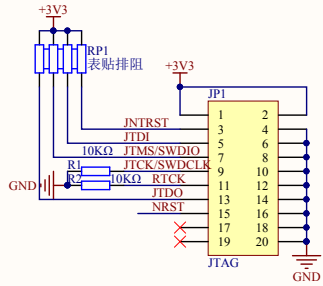
A

B

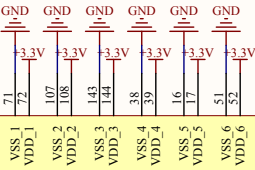
C

D

| | | |
|-----------|-------------|------|
| GDLink | JTAG | PB4 |
| L TDI | JTDI | PA15 |
| L TMS/IO | JTMS/SWDIO | PA13 |
| L TCK/CLK | JTCK/SWDCLK | PA14 |
| L TDO/SWO | JTDO | PB3 |
| L TRst | NRST | |

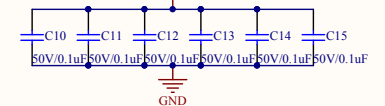
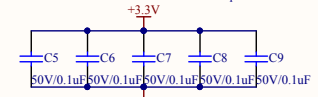
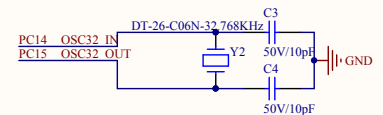
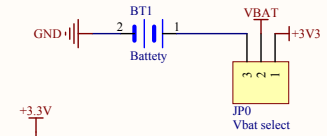


| | | |
|------|-----|------|
| PA0 | 34 | PA0 |
| PA1 | 35 | PA1 |
| PA2 | 36 | PA2 |
| PA3 | 37 | PA3 |
| PA4 | 40 | PA4 |
| PA5 | 41 | PA5 |
| PA6 | 42 | PA6 |
| PA7 | 43 | PA7 |
| PA8 | 100 | PA8 |
| PA9 | 101 | PA9 |
| PA10 | 102 | PA10 |
| PA11 | 103 | PA11 |
| PA12 | 104 | PA12 |
| PA13 | 105 | PA13 |
| PA14 | 109 | PA14 |
| PA15 | 110 | PA15 |
| PB0 | 46 | PB0 |
| PB1 | 47 | PB1 |
| PB2 | 48 | PB2 |
| PB3 | 133 | PB3 |
| PB4 | 134 | PB4 |
| PB5 | 135 | PB5 |
| PB6 | 136 | PB6 |
| PB7 | 137 | PB7 |
| PB8 | 139 | PB8 |
| PB9 | 140 | PB9 |
| PB10 | 69 | PB10 |
| PB11 | 70 | PB11 |
| PB12 | 73 | PB12 |
| PB13 | 74 | PB13 |
| PB14 | 75 | PB14 |
| PB15 | 76 | PB15 |
| PC0 | 26 | PC0 |
| PC1 | 27 | PC1 |
| PC2 | 28 | PC2 |
| PC3 | 29 | PC3 |
| PC4 | 44 | PC4 |
| PC5 | 45 | PC5 |
| PC6 | 96 | PC6 |
| PC7 | 97 | PC7 |
| PC8 | 98 | PC8 |
| PC9 | 99 | PC9 |
| PC10 | 111 | PC10 |
| PC11 | 112 | PC11 |
| PC12 | 113 | PC12 |
| PC13 | 7 | PC13 |
| PC14 | 8 | PC14 |
| PC15 | 9 | PC15 |
| PD0 | 114 | PD0 |
| PD1 | 115 | PD1 |
| PD2 | 116 | PD2 |
| PD3 | 117 | PD3 |
| PD4 | 118 | PD4 |
| PD5 | 119 | PD5 |
| PD6 | 122 | PD6 |
| PD7 | 123 | PD7 |
| PD8 | 77 | PD8 |
| PD9 | 78 | PD9 |
| PD10 | 79 | PD10 |
| PD11 | 80 | PD11 |
| PD12 | 81 | PD12 |
| PD13 | 82 | PD13 |
| PD14 | 85 | PD14 |
| PD15 | 86 | PD15 |



GD32F103ZET6

| | | |
|--------|-----|--------|
| PG0 | 56 | PG0 |
| PG1 | 57 | PG1 |
| PG2 | 87 | PG2 |
| PG3 | 88 | PG3 |
| PG4 | 89 | PG4 |
| PG5 | 90 | PG5 |
| PG6 | 91 | PG6 |
| PG7 | 92 | PG7 |
| PG8 | 93 | PG8 |
| PG9 | 124 | PG9 |
| PG10 | 125 | PG10 |
| PG11 | 126 | PG11 |
| PG12 | 127 | PG12 |
| PG13 | 128 | PG13 |
| PG14 | 129 | PG14 |
| PG15 | 132 | PG15 |
| PF0 | 10 | PF0 |
| PF1 | 11 | PF1 |
| PF2 | 12 | PF2 |
| PF3 | 13 | PF3 |
| PF4 | 14 | PF4 |
| PF5 | 15 | PF5 |
| PF6 | 18 | PF6 |
| PF7 | 19 | PF7 |
| PF8 | 20 | PF8 |
| PF9 | 21 | PF9 |
| PF10 | 22 | PF10 |
| PF11 | 49 | PF11 |
| PF12 | 50 | PF12 |
| PF13 | 53 | PF13 |
| PF14 | 54 | PF14 |
| PF15 | 55 | PF15 |
| PE0 | 141 | PE0 |
| PE1 | 142 | PE1 |
| PE2 | 1 | PE2 |
| PE3 | 2 | PE3 |
| PE4 | 3 | PE4 |
| PE5 | 4 | PE5 |
| PE6 | 5 | PE6 |
| PE7 | 58 | PE7 |
| PE8 | 59 | PE8 |
| PE9 | 60 | PE9 |
| PE10 | 63 | PE10 |
| PE11 | 64 | PE11 |
| PE12 | 65 | PE12 |
| PE13 | 66 | PE13 |
| PE14 | 67 | PE14 |
| PE15 | 68 | PE15 |
| VSS_7 | 61 | VSS_7 |
| VDD_7 | 62 | VDD_7 |
| VSS_8 | 83 | VSS_8 |
| VDD_8 | 84 | VDD_8 |
| VSS_9 | 94 | VSS_9 |
| VDD9 | 95 | VDD9 |
| VSS_10 | 120 | VSS_10 |
| VDD_10 | 121 | VDD_10 |
| VSS_11 | 130 | VSS_11 |
| VDD_11 | 131 | VDD_11 |
| VSSA | 30 | VSSA |
| VREF- | 31 | VREF- |
| VREF+ | 32 | VREF+ |
| VDDA | 33 | VDDA |



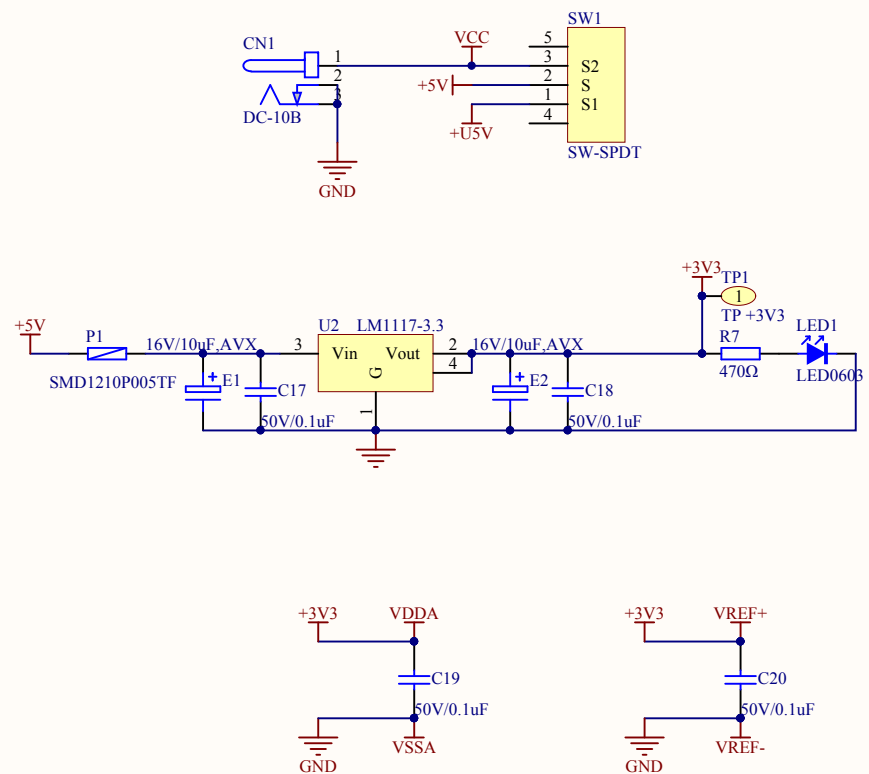
Company Name: GigaDevice

File Name: MCU

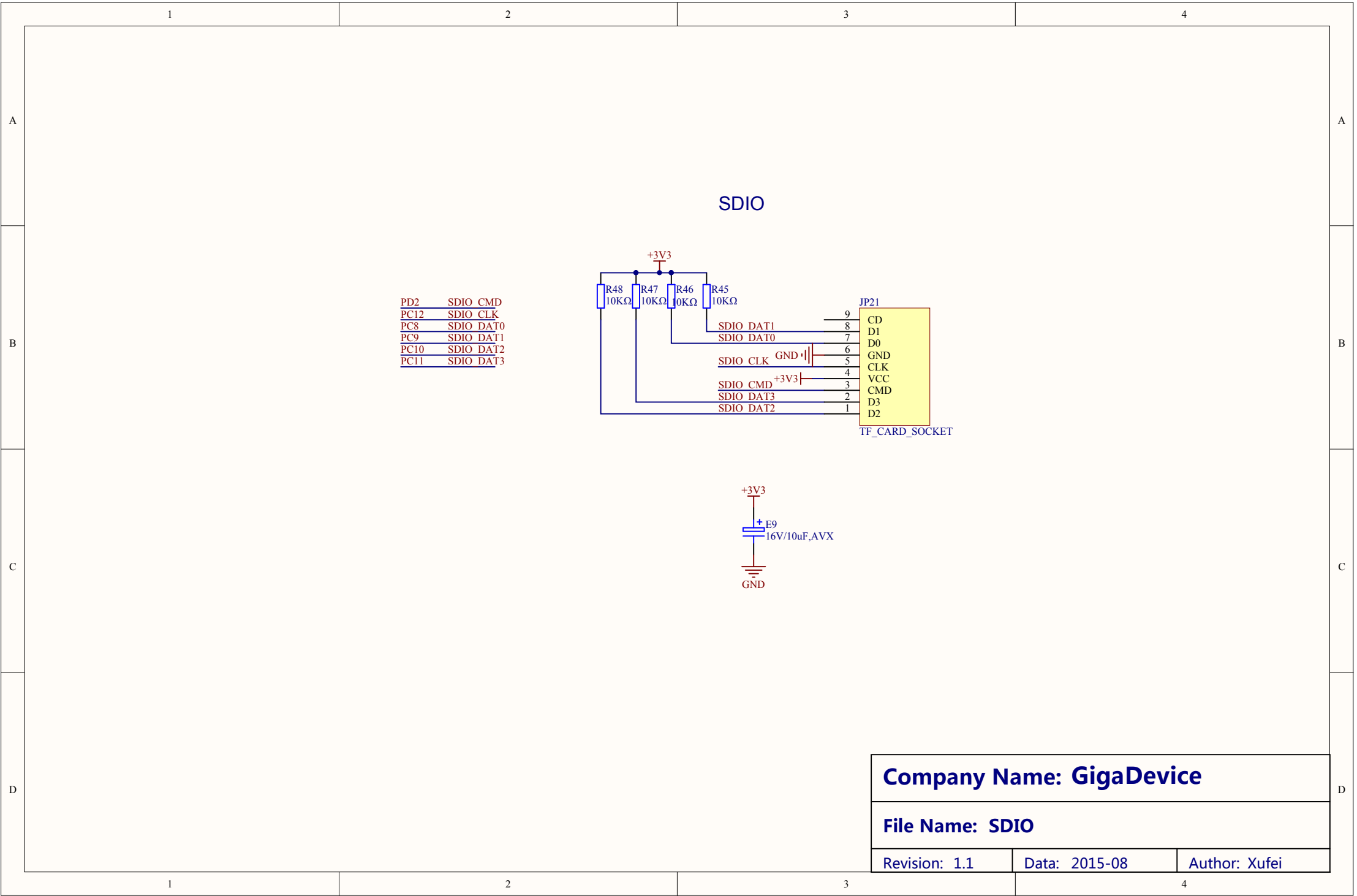
Revision: 1.1

Data: 2015-08

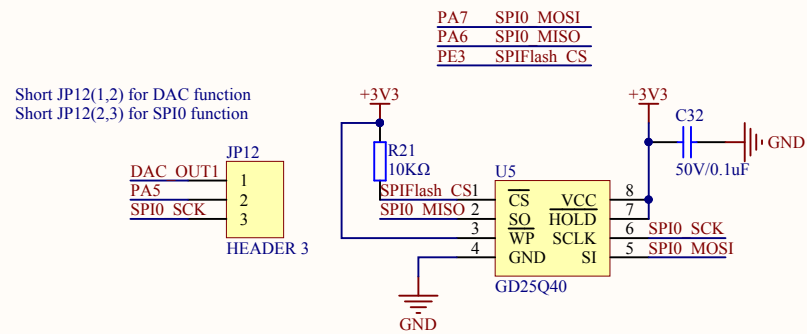
Author: XuFei



| | | |
|--------------------------|-----------------|---------------|
| Company Name: GigaDevice | | |
| File Name: Power | | |
| Revision: 1.0 | Data: 2013-8-20 | Author: XuFei |

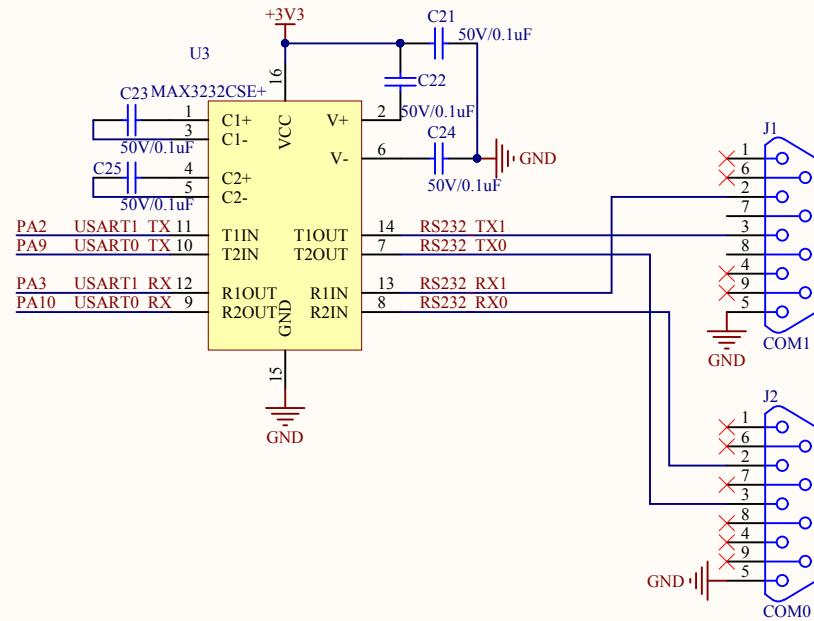


SPI Flash

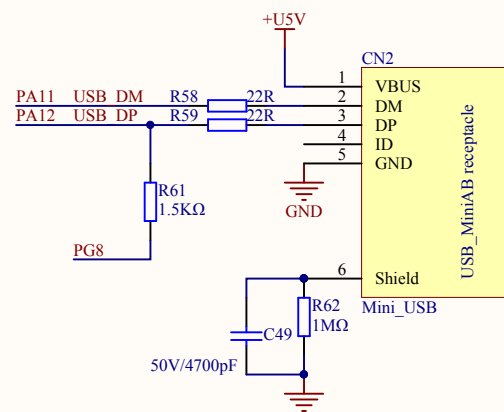


| | | |
|--------------------------|-----------------|---------------|
| Company Name: GigaDevice | | |
| File Name: SPI | | |
| Revision: 1.0 | Data: 2013-8-20 | Author: XuFei |

USART0/USART1



| | | |
|--------------------------|-----------------|---------------|
| Company Name: GigaDevice | | |
| File Name: USART | | |
| Revision: 1.0 | Data: 2013-8-20 | Author: XuFei |



| | | |
|--------------------------|-----------------|---------------|
| Company Name: GigaDevice | | |
| File Name: USB | | |
| Revision: 1.1 | Data: 2013.9.10 | Author: Xufei |

