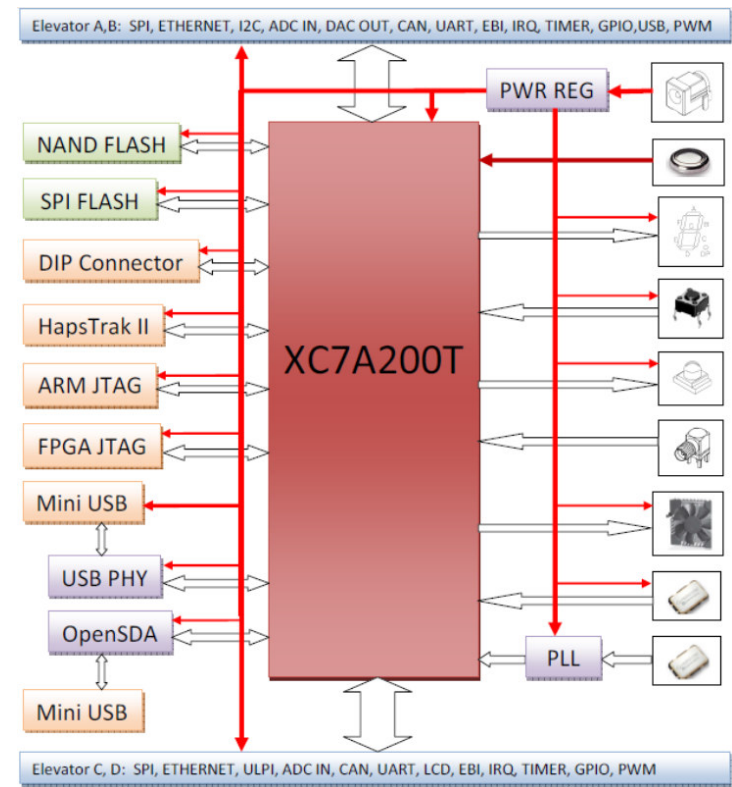
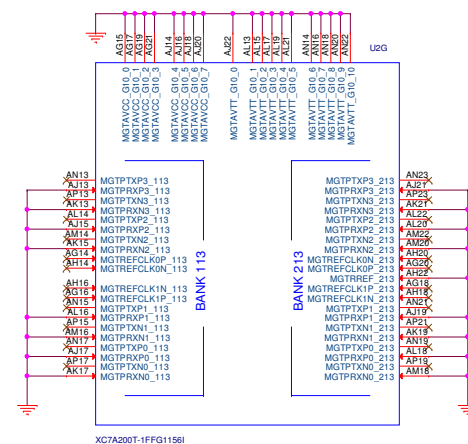
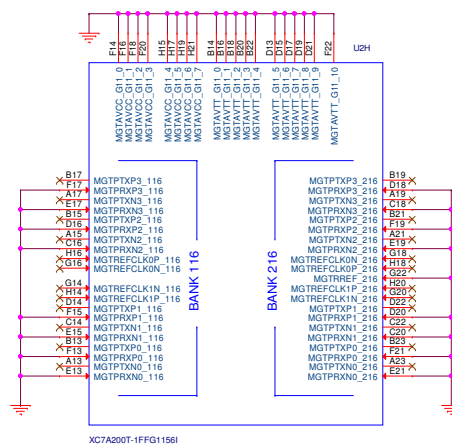
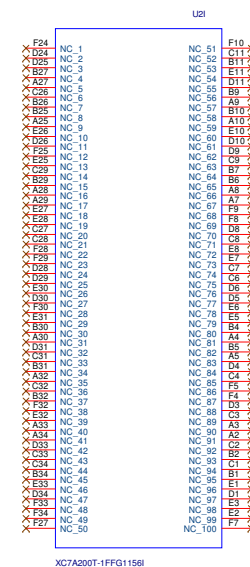


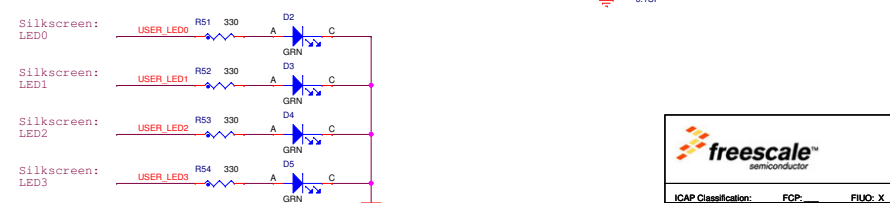
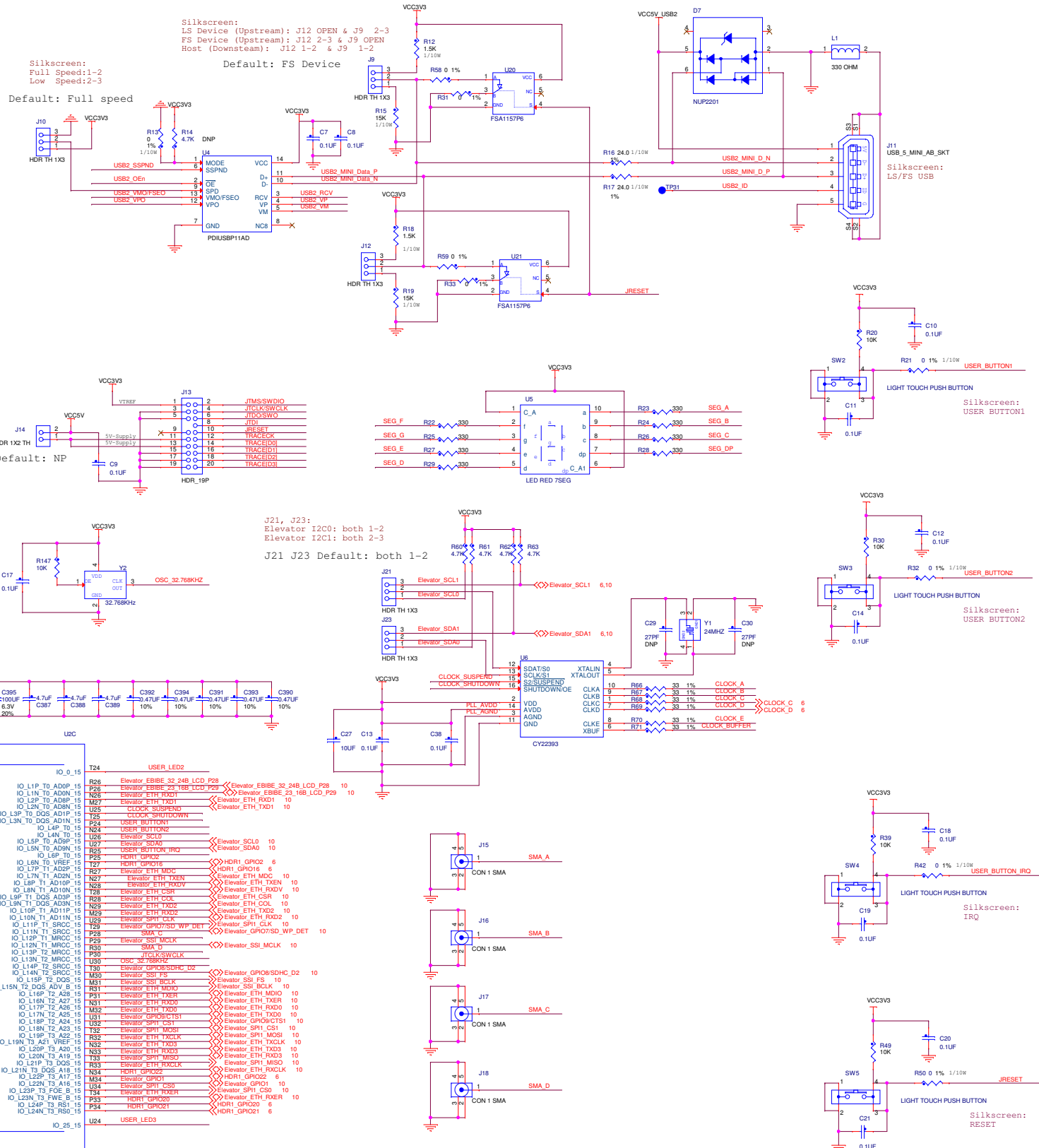
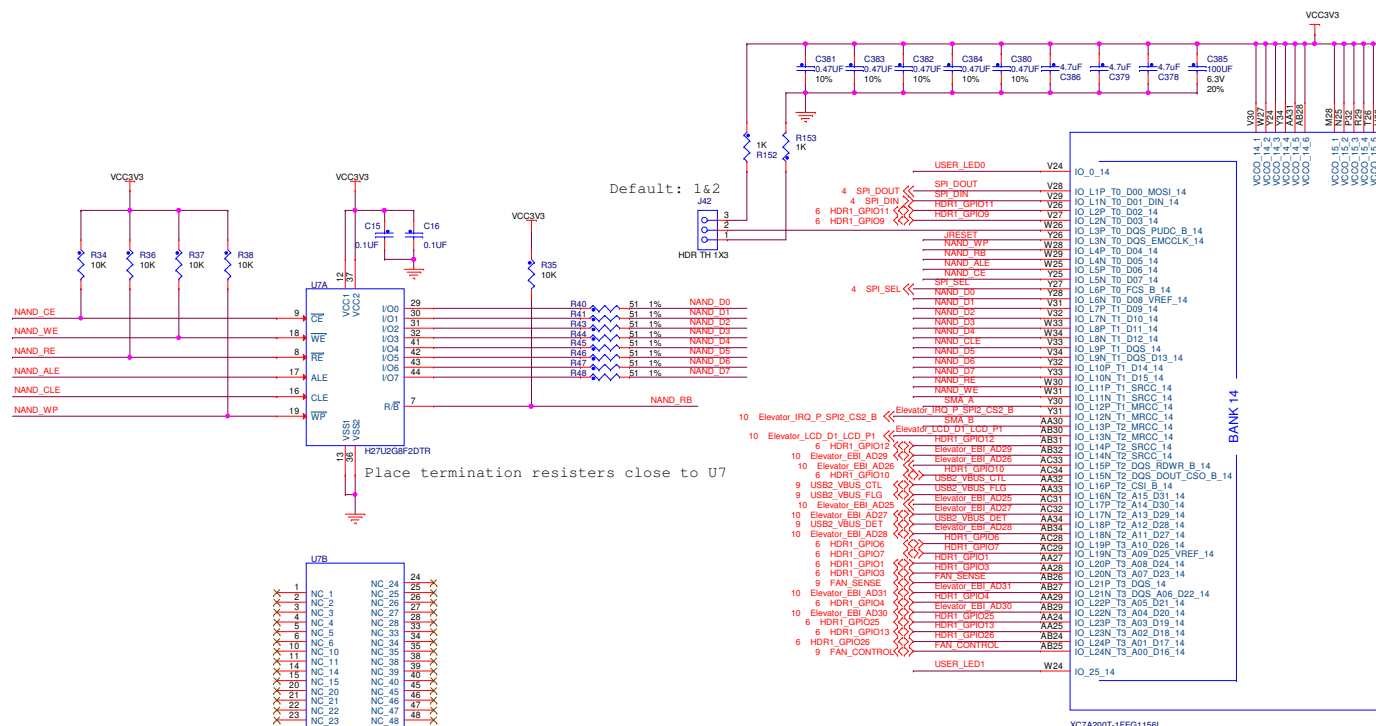
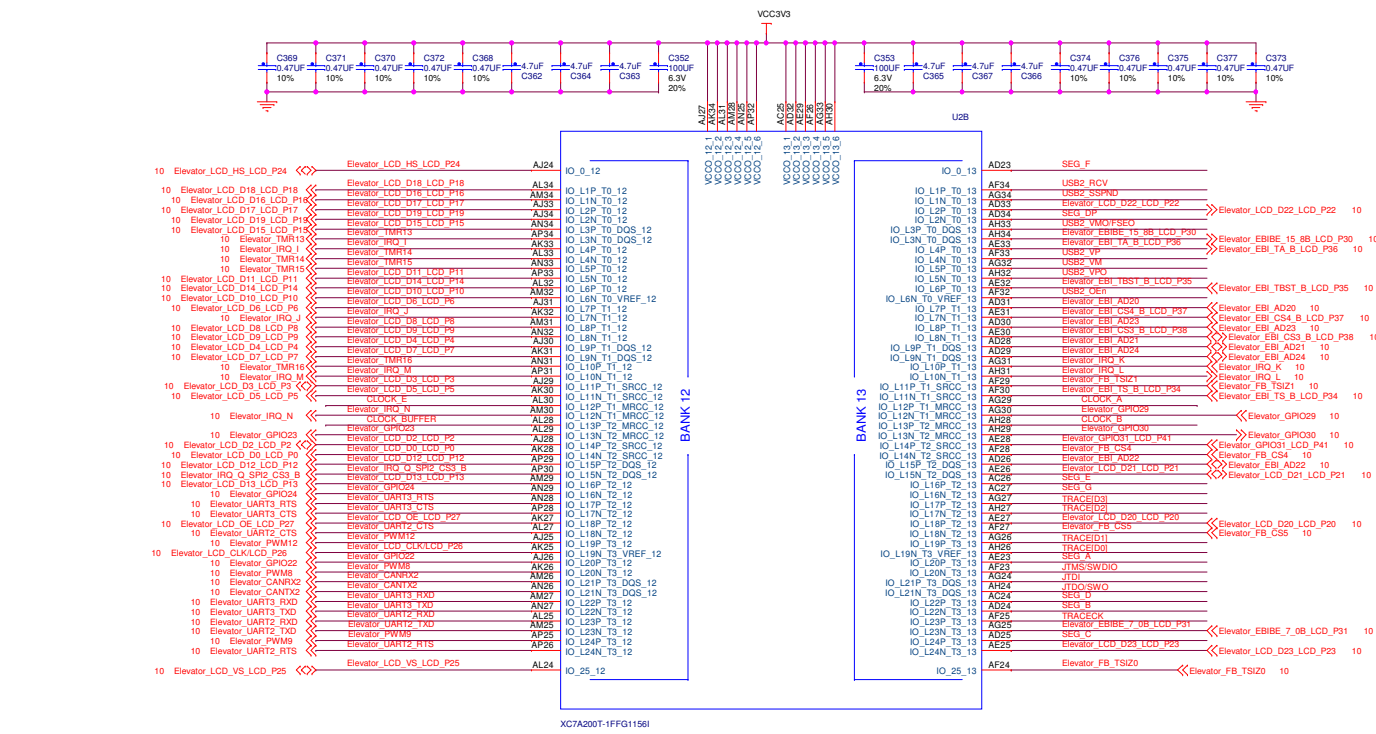
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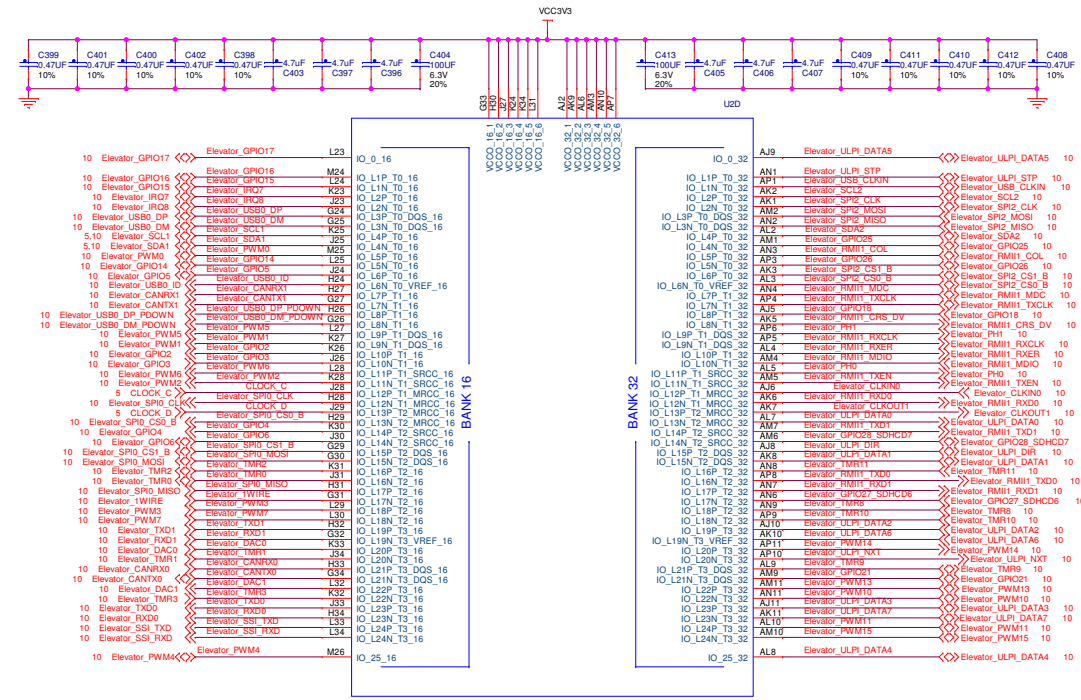
Revisions			
Rev	Description	Date	Approved
X1	Original Release	5-17-2013	
X2	Review Update	5-23-2013	
X3	Change Power System	6-4-2013	
X4	Change HAPSTRAK II	6-13-2013	
A	Release	7-3-2013	Eric Zhang
A1	Bug Fix	12-3-2013	
A2	Update USB	3-6-2014	
A3	Update CLOCK	3-31-2014	
A4	Update	4-10-2014	
B	Release	4-25-2014	

X-TWR-FPGA-A7









XC7A200T-1FFG1158I

