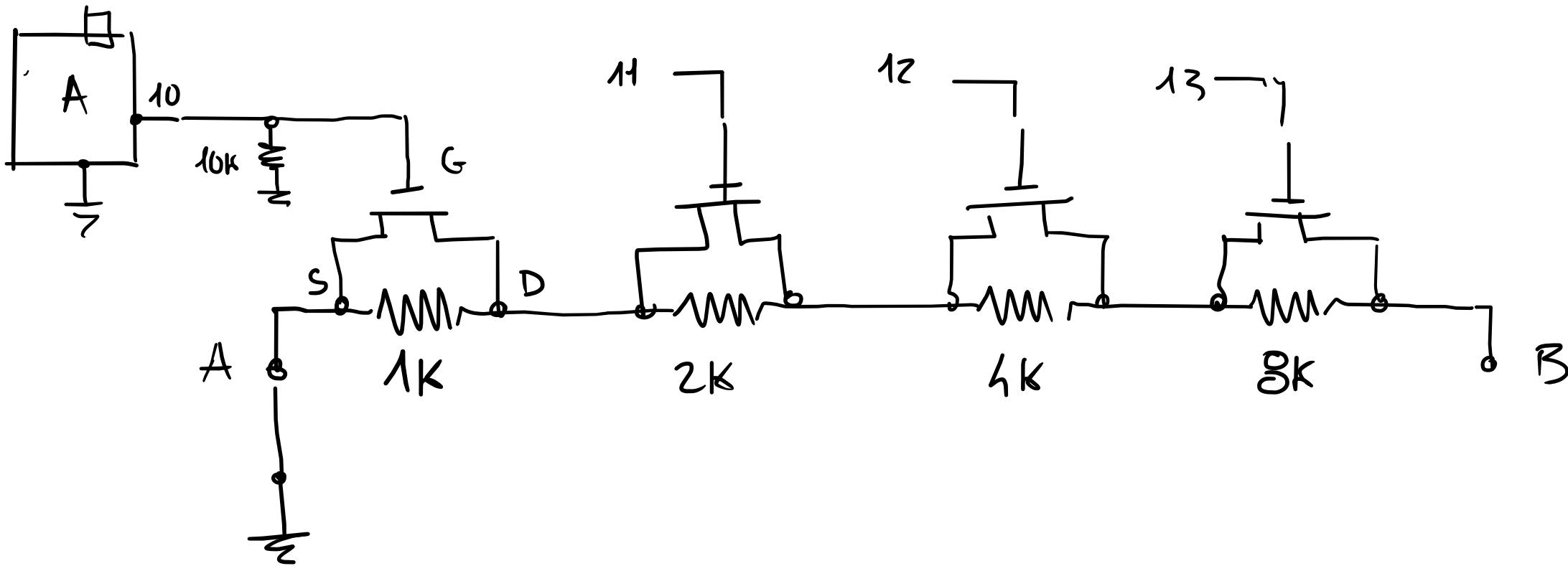
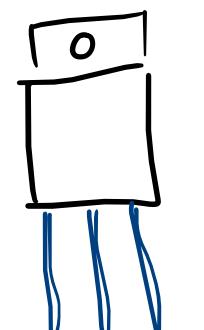


RÉS. VAR a MOSFET



	0000	0
	0001	1
	0010	2
	0011	3
	0100	4
	0101	5
	0110	6
	0111	7
1	0	
1	1	
		:

IRLZ34



G D S

i = 0 0000 0000
1 0001
2 0010
3 0011
4 100
.
.
.

15

3D → 0011
 0010] &
—
 0010

i & (1 <<)
1 <<

0000 0001 4 MASCHERA

1 << 1

0000 0010

bool stato =