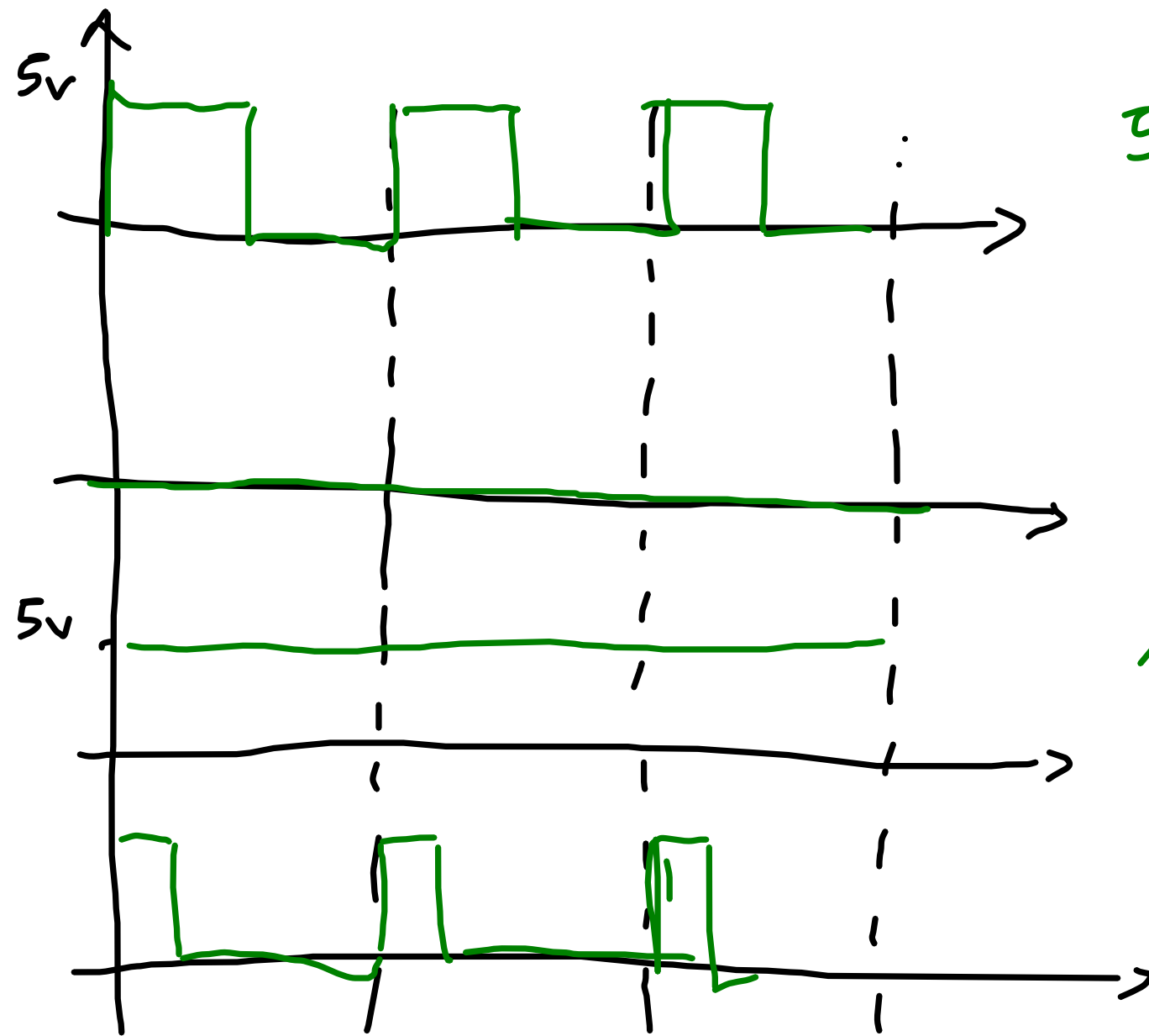


PULSE WIDTH MODULATION - PWM



50%

0%

100%

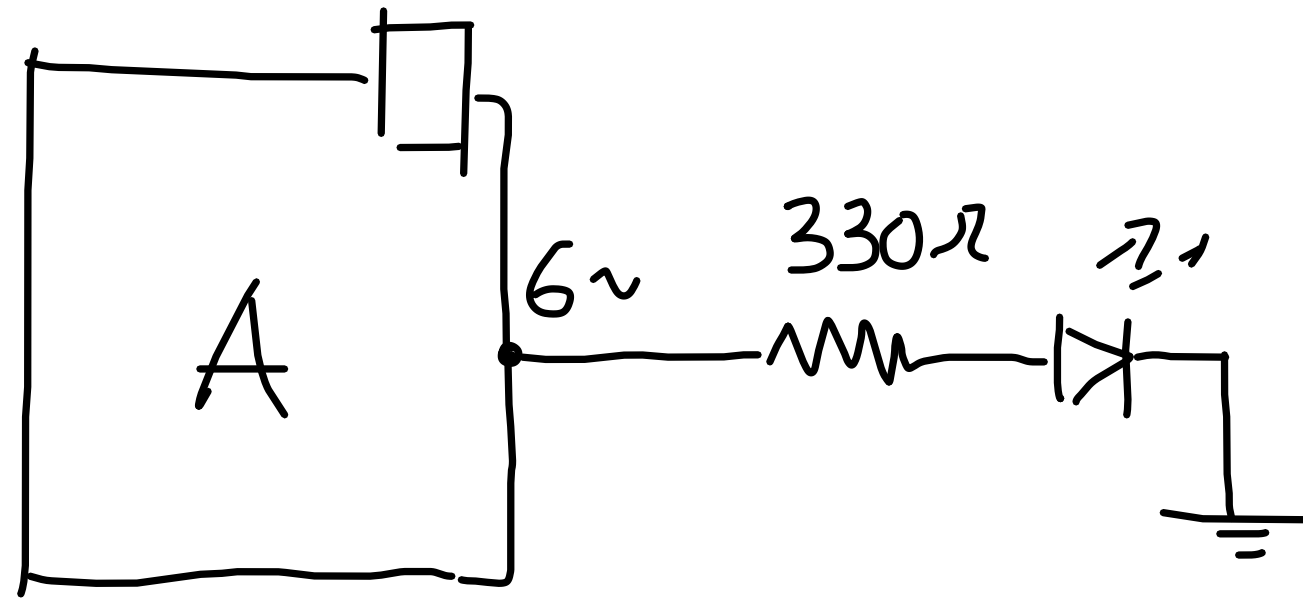
25%

`analogWrite(,);`

PIN

%

$0 \div 255$



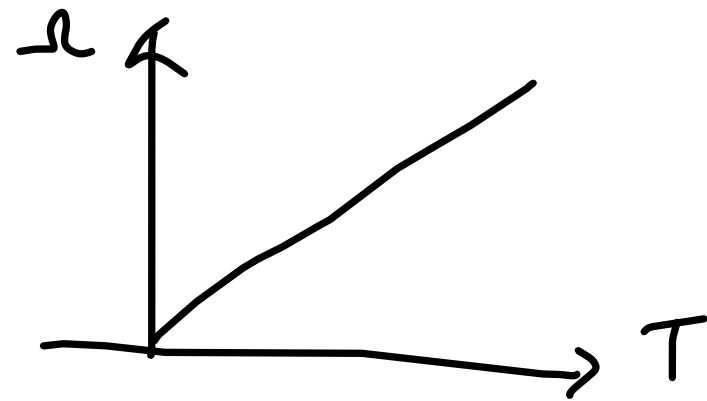
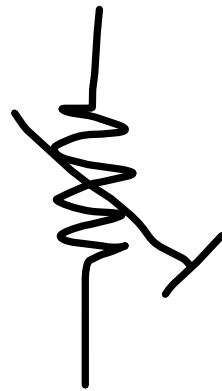
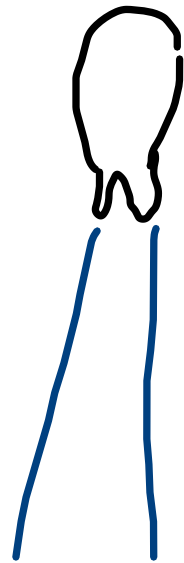
for (\textcircled{A} ; \textcircled{B} ; \textcircled{C}) {

\equiv CORPO

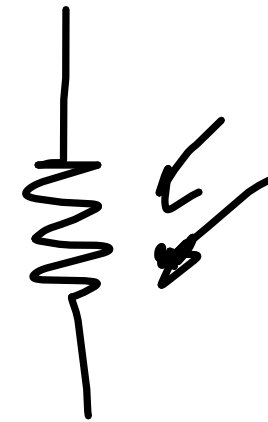
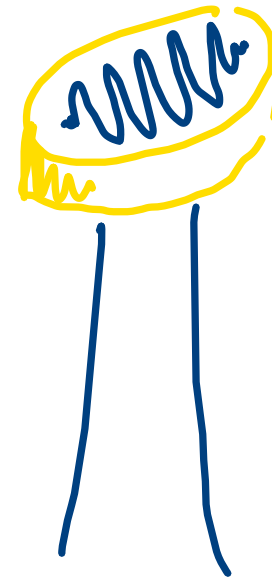
}

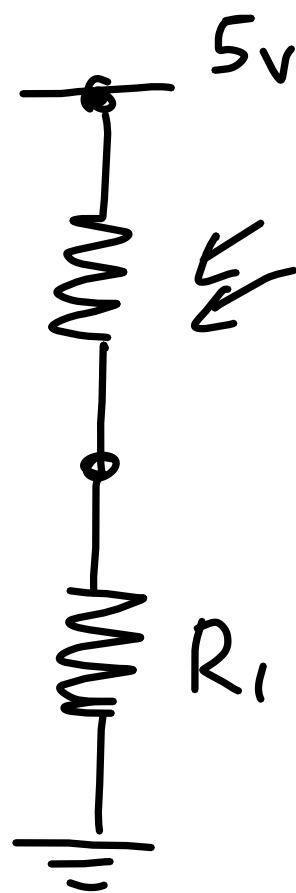
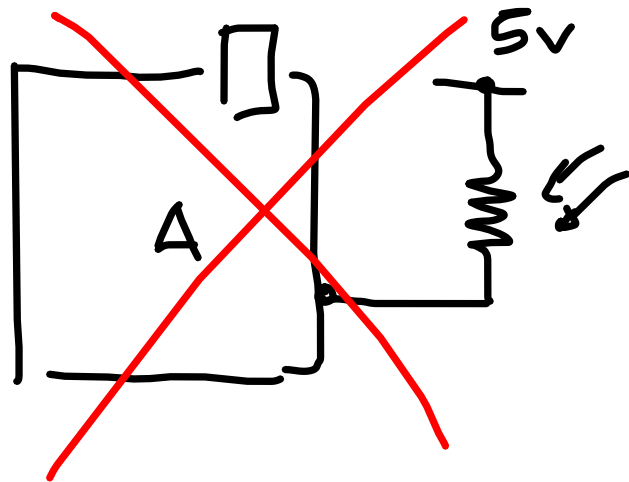
Sensori Analogici

TERMISTORE

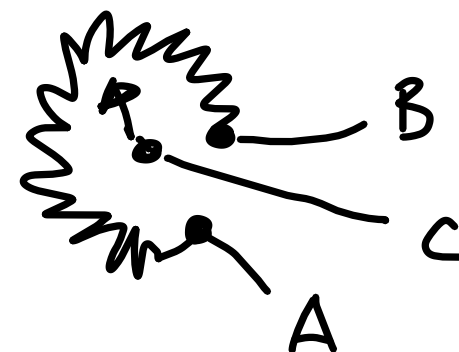


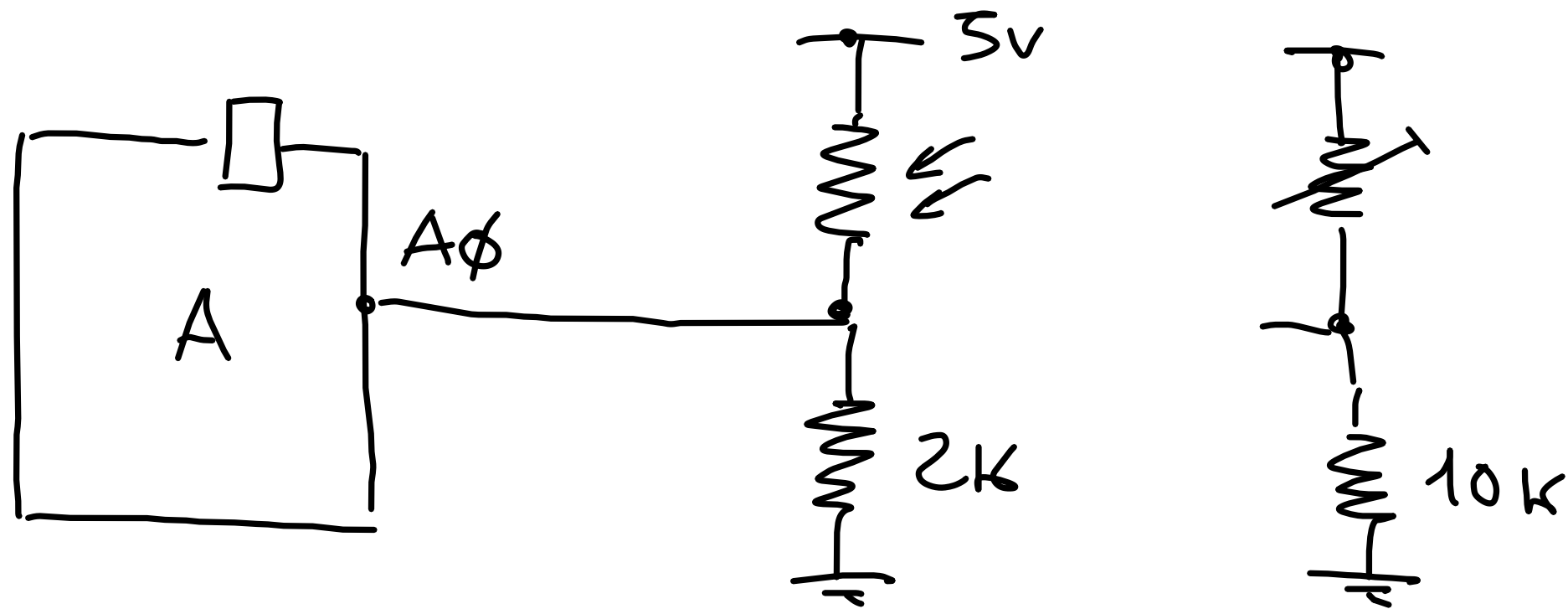
FOTORES.




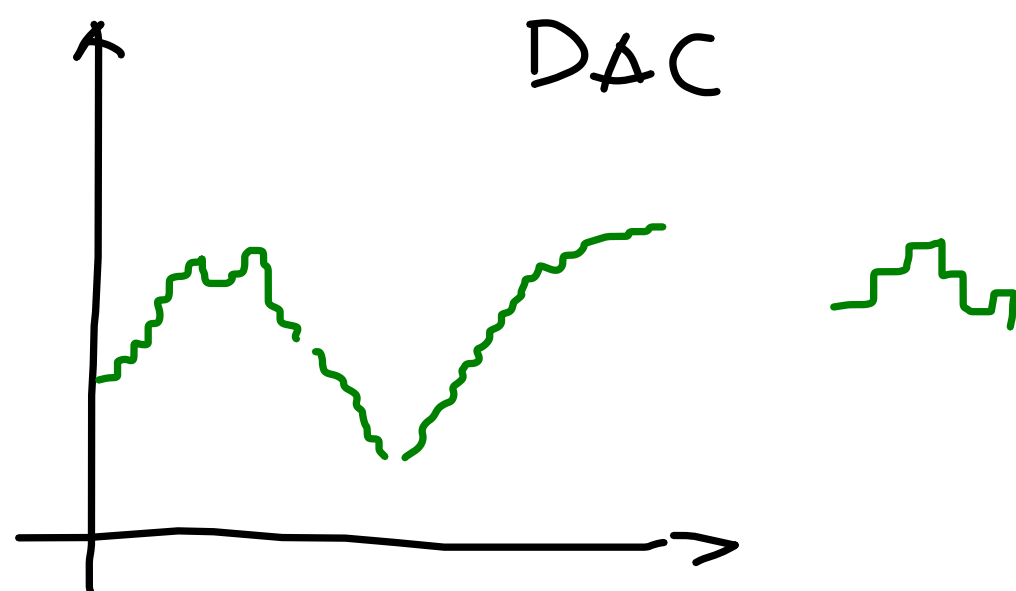
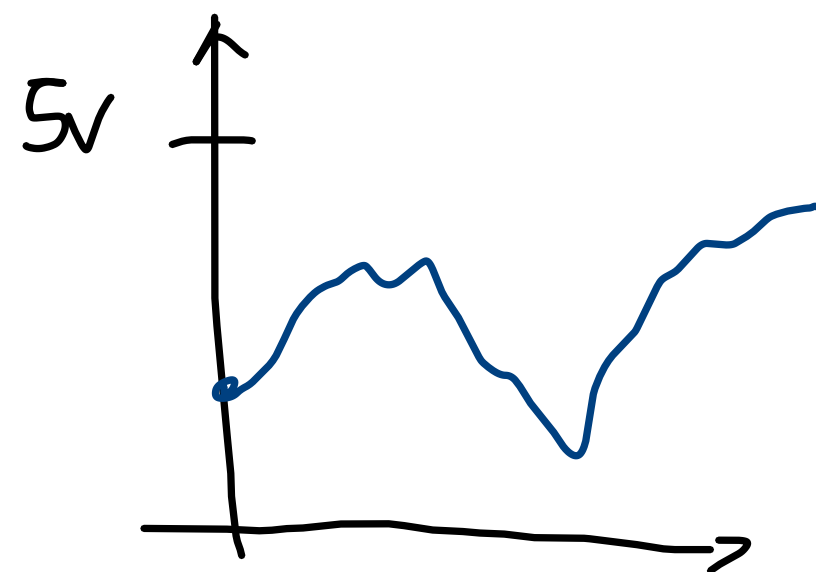


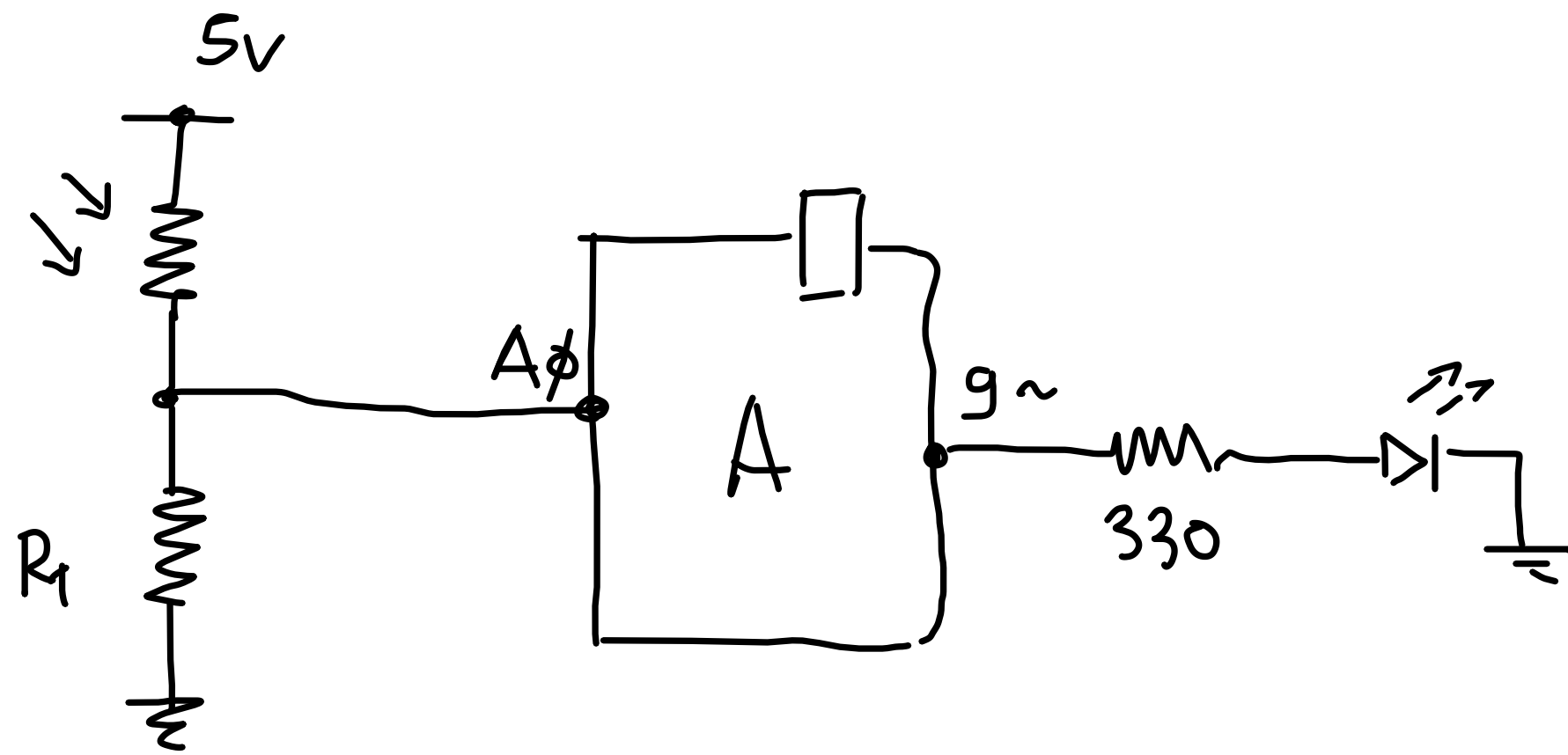
PARTITORE
RESISTIVO



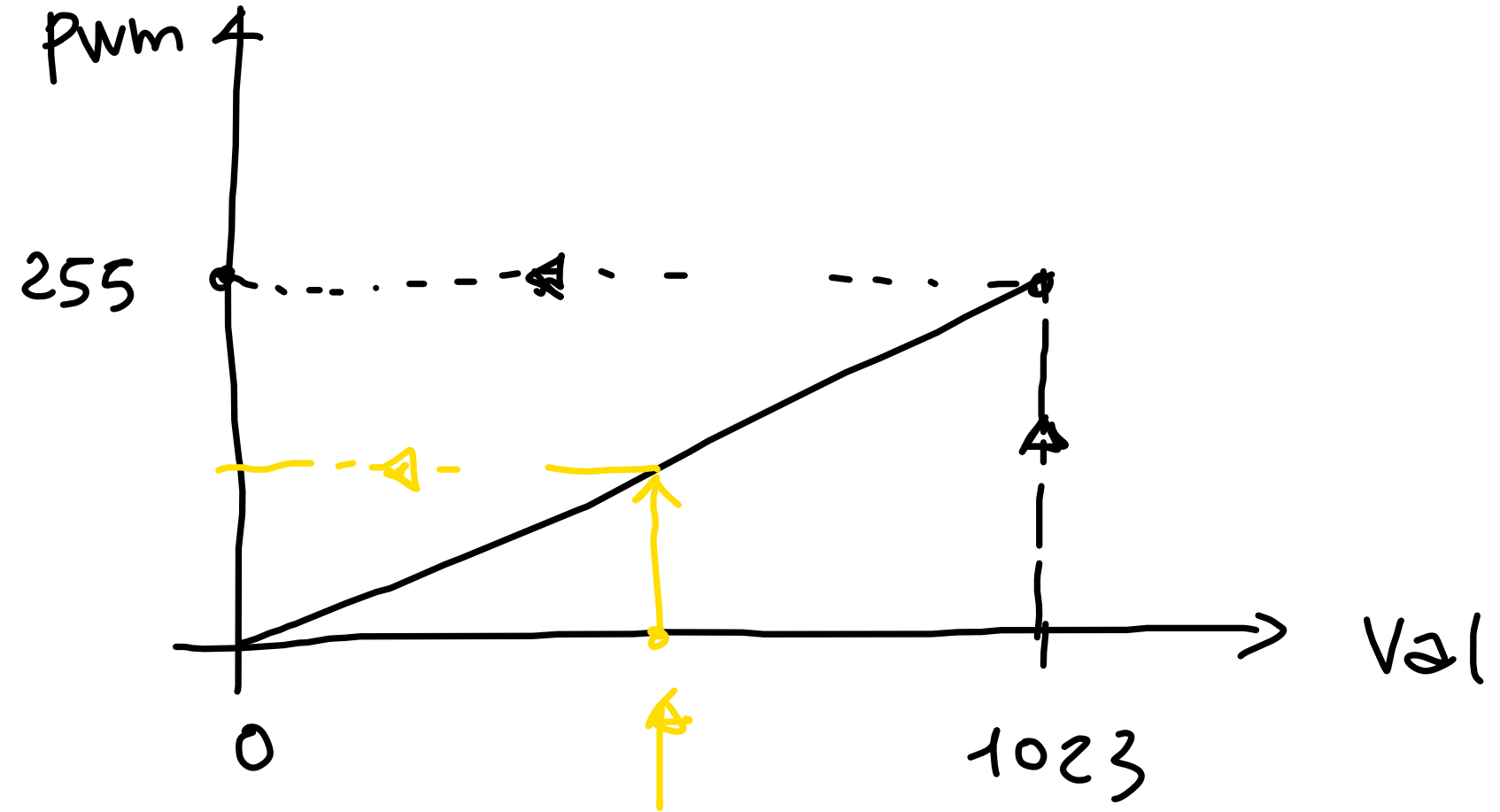


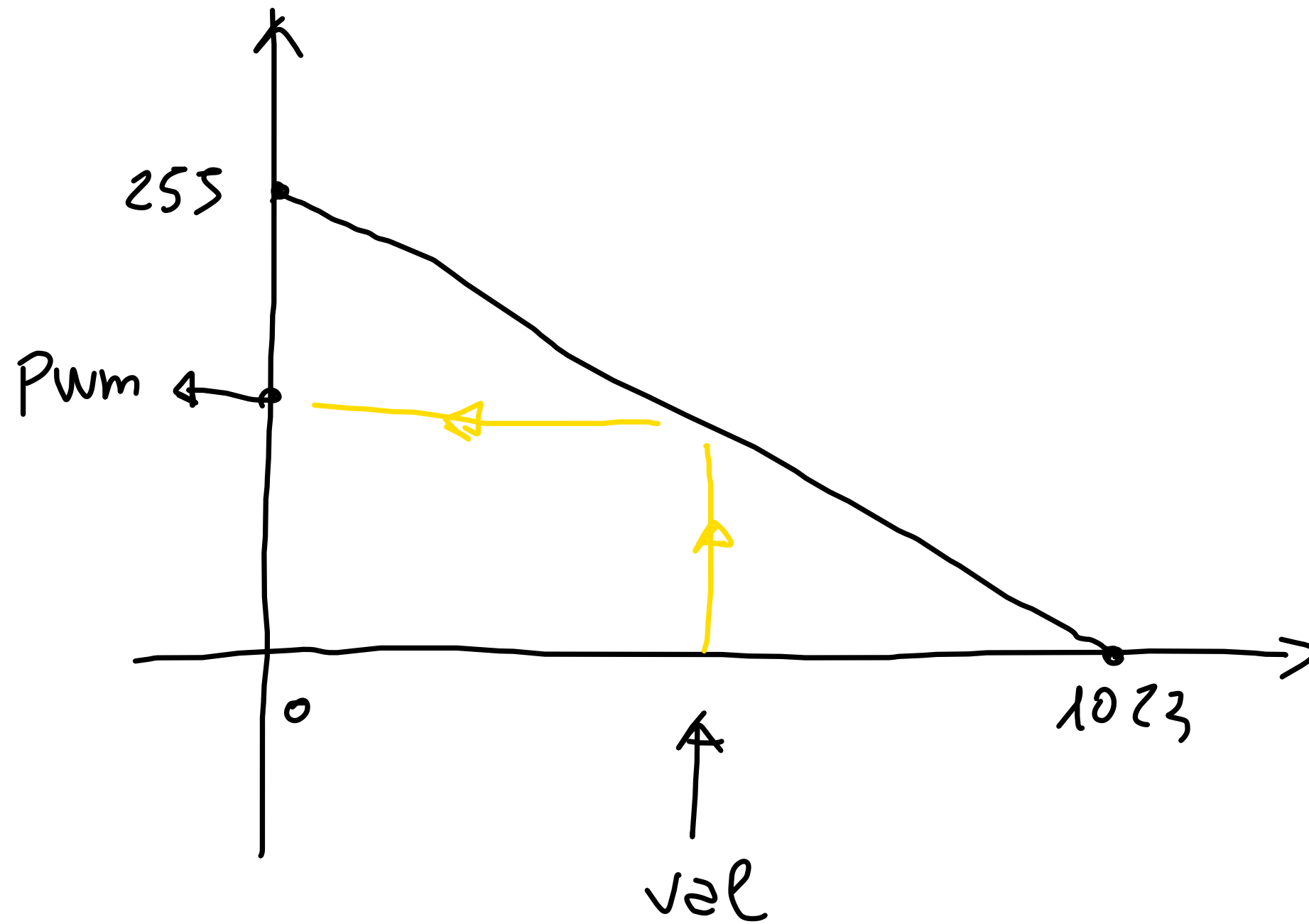
`analogRead(`

`);`
 $\rightarrow \phi \div 1023$
 \uparrow
 P_{in}



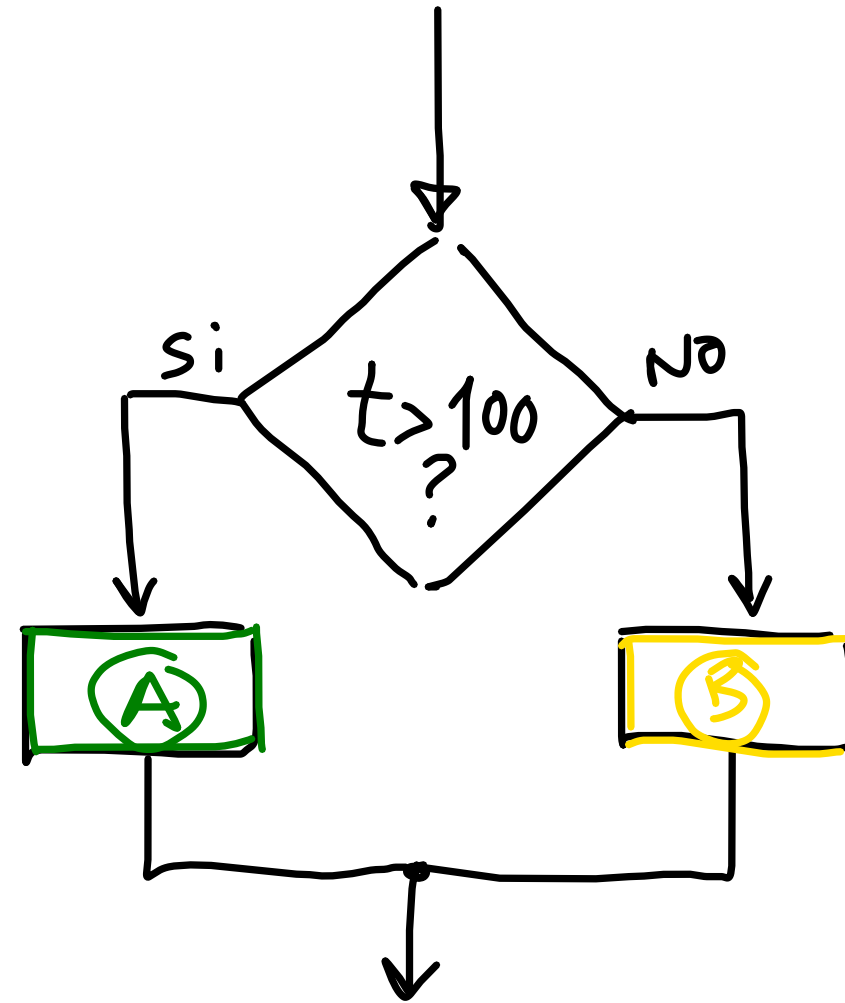


map





if



```
if ( expr ) {  
    (A)  
} else {  
    (B)  
}
```