# Zaid O. Narmouq

Ellicott City, MD

443-453-8569 | znarmouq@terpmail.umd.edu | linkedin.com/in/znarmouq |

## EDUCATION

**University of Maryland, College Park MD**                    Expected May 2023
Bachelors, Computer Engineering                                          **CGPA: 3.3**
Cybersecurity Specialization
  o   Yurie/Jeong H. Kim Scholarship

**Howard Community College, MD**                              Graduated May 2021
Associates, Computer Engineering                                         **CGPA: 3.7**

## SKILLS

**Languages:** C, C++, HTML5, Java, OCaml, Python, Ruby, Verilog
**Software:** Eclipse, Visual Studio Code, Git, Ghidra, MS Office 365, Unix, Windows, MacOS,
            Xilinx Vivado
**Hardware**: Soldering, Oscilloscope, Artix-7(FPGA)

## PROJECTS

**Shell Senior**                                                        Spring 2022
*Introduction to computer systems – C*
  o   Implemented the body of a shell that supports pipes, input and output redirection, Boolean
      expressions, and subshells.
  o   Assembled a Makefile that is used to build the shell, and also utilized the Makefile to build
      custom test executables.
  o   Developed secure methods as the shell will handle invalid commands without terminating

**Orders Processor**                                                    Spring 2022
*Object Oriented Programming II – Java*
  o   Optimized a program that demonstrates the effectiveness of multithreading by reading orders
      from files and outputting a receipt to a file.
  o   Benchmarks are output to the console.

**Online Test**                                                         Spring 2022
*Object Oriented Programming II – Java*
  o   Implemented a data manager of an online test that is capable of processing true/false, multiple
      choice, and fill-in-the-blank-questions.
  o   The system will grade submitted exams and generate statistical data.

**Digital Circuits and Systems Lab**                                    Spring 2022
*Xilinx Vivado*
  o   Designed, implemented, and simulated 3 types of 4-bit multipliers (combinational, array, booth).
  o   Engineered a display module that would allow me to program my circuit onto a Basys3 FPGA
      board.
  o   Utilized pmod ports to connect the FPGA to a digital logic analyzer to investigate the propagation
      delay between inputs and outputs in each multiplier.

## EXPERIENCE

**Clark's Ace Hardware,**                              August 2021 – September 2022
  o   Mastered different divisions priorities and stocking formats to ensure customer ease.
  o   Conducting technical diagnostic assessments on hardware tools and equipment.