Zaid O. Narmouq

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EDUCATION

University of Maryland, College Park MD Expected May 2023
Bachelors, Computer Engineering **GPA: 3.3**
Cybersecurity Specialization

- Yurie/Jeong H. Kim Scholarship
- District 9 Senatorial Scholarship

Howard Community College, Columbia MD Graduated May 2021
Associates, Computer Engineering **GPA: 3.7**

SKILLS

Languages: C, C++, HTML5, Java, OCaml, Python, Ruby, Verilog
Software: Eclipse, Visual Studio Code, Git, Ghidra, MS Office 365, Unix, Windows, MacOS, Xilinx Vivado
Hardware: Soldering, Oscilloscope, Artix-7(FPGA)

PROJECTS

Shell Senior - *Introduction to computer systems – C* Spring 2022

- Implemented the body of a shell that supports pipes, input and output redirection, boolean expressions, and subshells.
- Assembled a Makefile that is used to build the shell, and custom test executables.
- Developed secure methods as the shell handles invalid commands without terminating

Orders Processor - *Object Oriented Programming II – Java* Spring 2022

- Optimized a program that demonstrates the effectiveness of multithreading by reading orders from files and outputting a receipt to a file.
- Benchmarks are output to the console.

Online Test - *Object Oriented Programming II – Java* Spring 2022

- Implemented a data manager of an online test that is capable of processing true/false, multiple choice, and fill-in-the-blank-questions.
- The system will grade submitted exams and generate statistical data.

Interest Table - Object Oriented Programing – Java Spring 2022

- Project was designed and implemented by using the Model-View-Control paradigm.
- Utilized Java Fx and GUI to create a user-friendly interest calculator that can calculate compound and simple interests.
- Inner classes and lambda expressions were associated with the GUI to handle computations.

Digital Circuits and Systems Lab - *Xilinx Vivado* Spring 2022

- Designed, implemented, and simulated 3 types of 4-bit multipliers (combinational, array, booth).
- Engineered a display module that would allow programming A circuit onto a Basys3 FPGA board.
- Utilized PMOD ports to connect the FPGA to a digital logic analyzer to investigate the propagation delay between inputs and outputs in each multiplier.

EXPERIENCE

Clark's Ace Hardware, Service August 2021 – September 2022

- Mastered different divisions priorities and stocking formats to ensure customer ease.
- Conducting technical diagnostic assessments on hardware tools and equipment.