

Lab Report - 03

Course No: 206

Course Title: Digital Logic Design

Submitted To:

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Dept: CSE

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Section: 07

Dept: CSE

Name	of	experiment	+	Implementation	ω	Boolean	
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7	1	1.	1	1	0	

Implementation the Boolean Sunction

F = ABCD + ABCD + ABCD + ABCD + ABCD + ABCD + ABCD

=> Ācō(B+B) + ABCŌ + ABCŌ + ABCŌ + ABCO
[Distributive Law]

=> ACD.1 + ABCD + ABD (C+E) + ABD (C+C) [complement Law]

=> ACD.1 + ABCD + ABD.1 + ABD.1 [Dentity Law]

=> ACD + ABCO + ABO + ABO

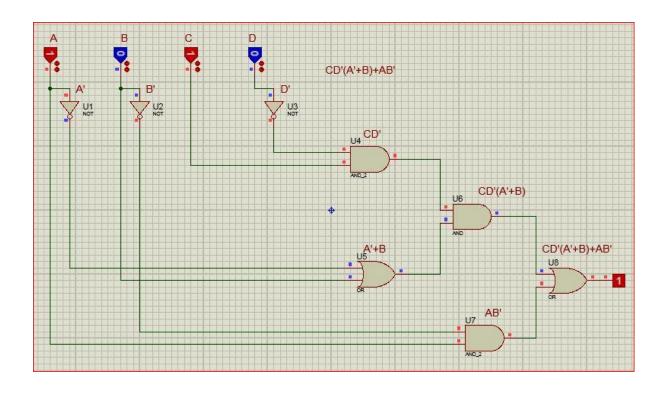
=> CO (A+AB) + AB (O+D) [Distributive Law]

=> CO (A+A) (A+B) + AB. 1 [Abgomption Law]

>> CO (1. A+B)+AB

\$ >> CD (A+B) + AB

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Conclussiont

- O Boolean function can be implemention wing the basic gates.
 - De Logic gates are very small cincuit that implement Boolean operation.
 - 3 Boolean function are easy to implements by truth table & Logic godes
 - 9 we design. the cincul to proteus software.

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