

Summary

Mic500 200k Termination	
Signal Path Setup	🍼 PASSED
Stepped Frequency Sweep MIC 500	▲ FAILED
Mic 2k 200k termination	
Signal Path Setup	PASSED
Stepped Frequency Sweep MIC 2K	🛕 FAILED
Mic 2k 15dB PAD 200k termination	
Signal Path Setup	PASSED
Stepped Frequency Sweep 15dB PAD	▲ FAILED
Line Gain -10 200kTermination	
Signal Path Setup	PASSED
Stepped Frequency Sweep -10	▲ FAILED
Line Gain -10 600 Termination	
Signal Path Setup	PASSED
Level and Gain -10	▲ FAILED
Line Gain +5 200kTermination	
Signal Path Setup	PASSED
Stepped Frequency Sweep +5	▼ PASSED
Line Gain +5 600 Termination	
Signal Path Setup	PASSED
Level and Gain +5	PASSED
Line Gain -5 600 Termination	
Signal Path Setup	PASSED
Level and Gain -5	▼ PASSED
Line Gain 0 600 Termination	
Signal Path Setup	PASSED
Level and Gain 0	▼ PASSED
Line Gain +10 600 Termination	
Signal Path Setup	PASSED
Level and Gain +10	♥ PASSED
Line Gain +10 200k Termination Level Hi	
Signal Path Setup	PASSED
Noise Recorder (RMS) CW	▲ FAILED
Line Gain +10 200k Termination Level Low	
Signal Path Setup	PASSED
Noise Recorder (RMS) CCW	▲ FAILED
Hi Z Gain -10 2.2M 200k Termination	

Signal Path Setup	PASSED
Level and Gain 2.2M	🍼 PASSED
Hi Z Gain -10 47k 200k Termination	
Signal Path Setup	PASSED
Level and Gain 47K	🛕 FAILED
Dummy Signal Path For Report	
Signal Path Setup	PASSED
Sequence Result:	
Sequence Result:	

5/8/2023 9:23 AM Page 2 of 45



Mic500 200k Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms 10.00 mVrms dBSPL2: dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

DCX is not detected.

W(watts) (Input Power):

5/8/2023 9:23 AM Page 3 of 45



Mic500 200k Termination : Verify Connections

Waveform: Sine

Generator Level: -42.300 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/8/2023 9:21:16.844 AM)

Ch1 71.92 mVrms

Gain (5/8/2023 9:21:16.844 AM)

Ch1 21.659 dB

THD+N Ratio (5/8/2023 9:21:16.844 AM)

Ch1 ---- %

Frequency (5/8/2023 9:21:16.844 AM)

Ch1 ---- Hz

5/8/2023 9:23 AM Page 4 of 45



Mic500 200k Termination: Stepped Frequency Sweep MIC 500

Generator Level: -42.300 dBu
DC Offset: 0.000 V
EQ: None

Start Frequency: 20.0000 kHz
Stop Frequency: 20.0000 Hz
Step Type: Logarithmic

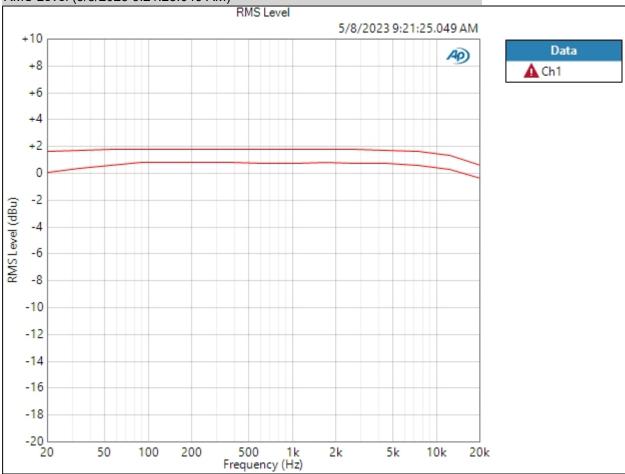
Number of Points: 10

Weighting Filter: Signal Path High-pass Filter: 20 Hz

Phase Ref Channel: Ch1

Measured 1 5/8/2023 9:21:25 AM

RMS Level (5/8/2023 9:21:25.049 AM)

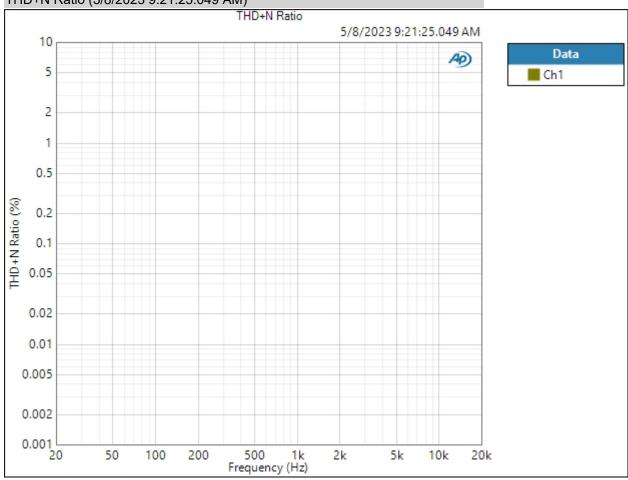


5/8/2023 9:23 AM Page 5 of 45



Result: A FAILED

THD+N Ratio (5/8/2023 9:21:25.049 AM)



Result: V PASSED

THD Ratio (5/8/2023 9:21:25.049 AM)

5/8/2023 9:23 AM Page 6 of 45



Result: A FAILED

5/8/2023 9:23 AM Page 7 of 45



Mic 2k 200k termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms 10.00 mVrms dBSPL2: dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL

• DCX

DCX is not detected.

W(watts) (Input Power):

dBm (Input Power):

5/8/2023 9:23 AM Page 8 of 45

600.0 ohm



Mic 2k 200k termination : Verify Connections

Waveform: Sine

Generator Level: -42.300 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/8/2023 9:21:30.513 AM)

Ch1 72.01 mVrms

Gain (5/8/2023 9:21:30.513 AM)

Ch1 21.668 dB

THD+N Ratio (5/8/2023 9:21:30.513 AM)

Ch1 ---- %

Frequency (5/8/2023 9:21:30.513 AM)

Ch1 ---- Hz

5/8/2023 9:23 AM Page 9 of 45



Mic 2k 200k termination : Stepped Frequency Sweep MIC 2K

Generator Level: -42.300 dBu
DC Offset: 0.000 V
EQ: None

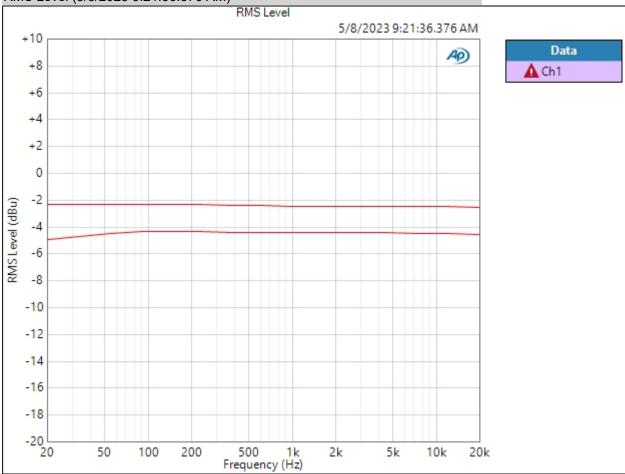
Start Frequency: 20.0000 kHz
Stop Frequency: 20.0000 Hz
Step Type: Logarithmic

Number of Points: 10

Weighting Filter: Signal Path
High-pass Filter: 20 Hz
Phase Ref Channel: Ch1

Measured 1 5/8/2023 9:21:36 AM

RMS Level (5/8/2023 9:21:36.376 AM)

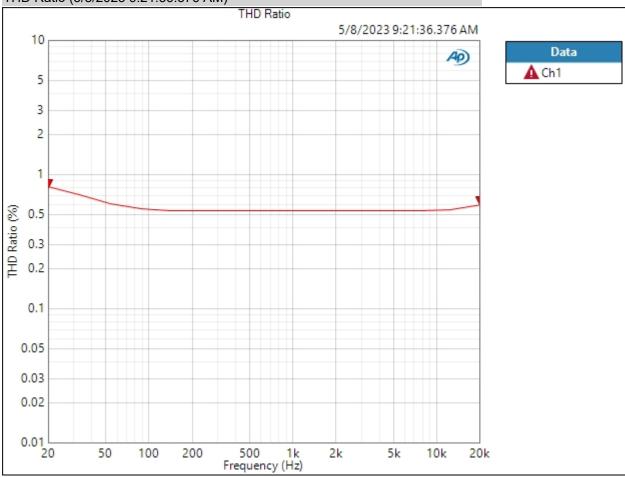


5/8/2023 9:23 AM Page 10 of 45



Result: A FAILED

THD Ratio (5/8/2023 9:21:36.376 AM)



Ch1 A Failed Upper Limit

Result: A FAILED

5/8/2023 9:23 AM Page 11 of 45



Mic 2k 15dB PAD 200k termination : Signal Path Setup
Output Connector:

Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1
Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm W(watts) (Input Power): 8.000 ohm

• DCX

DCX is not detected.

5/8/2023 9:23 AM Page 12 of 45



Mic 2k 15dB PAD 200k termination : Verify Connections

Waveform: Sine

Generator Level: -42.300 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/8/2023 9:21:41.693 AM)

Ch1 72.00 mVrms

Gain (5/8/2023 9:21:41.693 AM)

Ch1 21.670 dB

THD+N Ratio (5/8/2023 9:21:41.693 AM)

Ch1 ---- %

Frequency (5/8/2023 9:21:41.693 AM)

Ch1 ---- Hz

5/8/2023 9:23 AM Page 13 of 45



Mic 2k 15dB PAD 200k termination : Stepped Frequency Sweep 15dB PAD

Generator Level: -42.000 dBu
DC Offset: 0.000 V
EQ: None

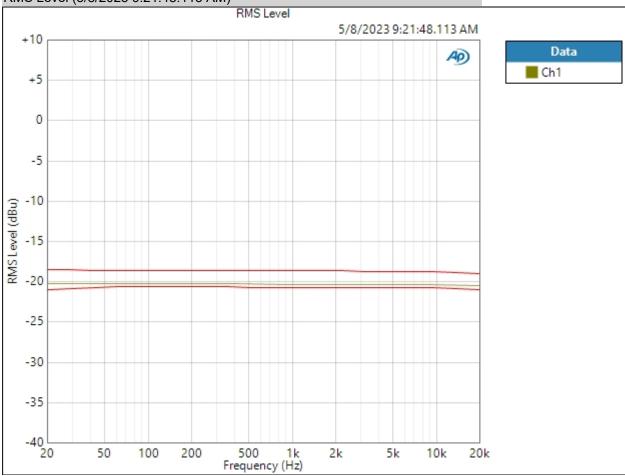
Start Frequency: 20.0000 kHz
Stop Frequency: 20.0000 Hz
Step Type: Logarithmic

Number of Points: 10

Weighting Filter: Signal Path
High-pass Filter: 20 Hz
Phase Ref Channel: Ch1

Measured 1 5/8/2023 9:21:48 AM

RMS Level (5/8/2023 9:21:48.113 AM)



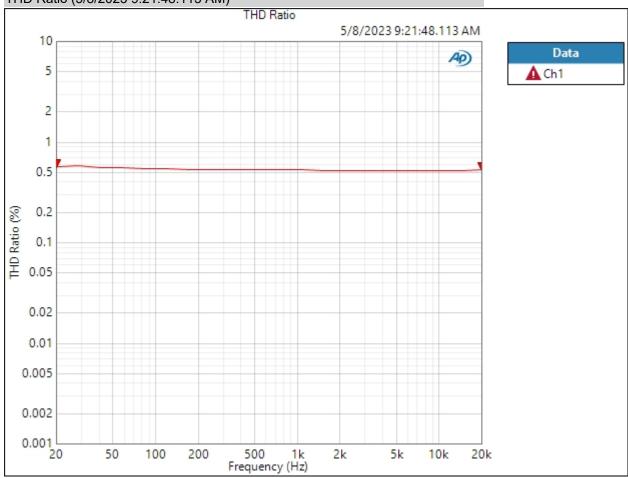
Ch1 🔮 PASSED

5/8/2023 9:23 AM Page 14 of 45



Result: V PASSED

THD Ratio (5/8/2023 9:21:48.113 AM)



Ch1 A Failed Upper Limit

Result: A FAILED

5/8/2023 9:23 AM Page 15 of 45



Line Gain -10 200kTermination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

DCX is not detected.

W(watts) (Input Power):

5/8/2023 9:23 AM Page 16 of 45



Line Gain -10 200kTermination : Verify Connections

Waveform: Sine

Generator Level: 0.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/8/2023 9:21:53.840 AM)

Ch1 8.395 Vrms

Gain (5/8/2023 9:21:53.840 AM)

Ch1 20.719 dB

THD+N Ratio (5/8/2023 9:21:53.840 AM)

Ch1 6.011291 %

Frequency (5/8/2023 9:21:53.840 AM)

Ch1 1.00000 kHz

5/8/2023 9:23 AM Page 17 of 45



Line Gain -10 200kTermination : Stepped Frequency Sweep -10

Generator Level: 0.000 dBu
DC Offset: 0.000 V
EQ: None

Start Frequency: 20.0000 kHz
Stop Frequency: 20.0000 Hz
Step Type: Logarithmic

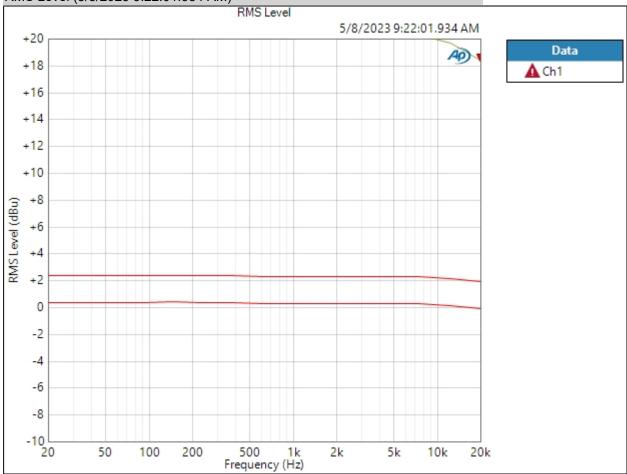
Number of Points: 15

Weighting Filter: Signal Path

High-pass Filter: 20 Hz Phase Ref Channel: Ch1

Measured 1 5/8/2023 9:22:01 AM

RMS Level (5/8/2023 9:22:01.934 AM)



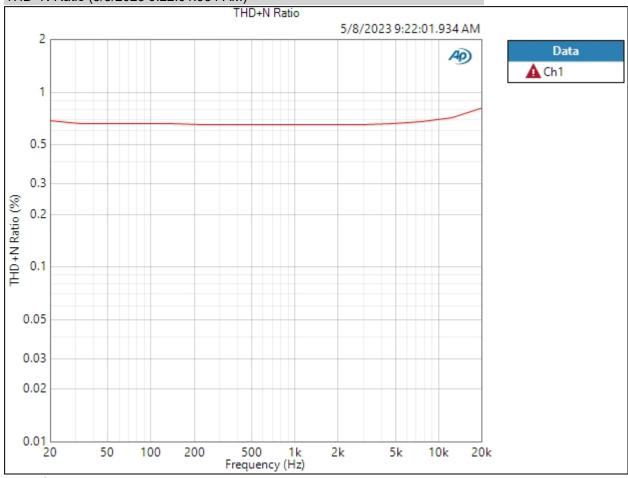
Ch1 A Failed Upper Limit

5/8/2023 9:23 AM Page 18 of 45



Result: A FAILED

THD+N Ratio (5/8/2023 9:22:01.934 AM)



Ch1 A Failed Upper Limit

Result: A FAILED

5/8/2023 9:23 AM Page 19 of 45



Line Gain -10 600 Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 600 ohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms 10.00 mVrms dBSPL2: dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

DCX is not detected.

W(watts) (Input Power):

5/8/2023 9:23 AM Page 20 of 45



Line Gain -10 600 Termination: Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/8/2023 9:22:07.849 AM)

Ch1 2.471 Vrms

Gain (5/8/2023 9:22:07.849 AM)

Ch1 20.079 dB

THD+N Ratio (5/8/2023 9:22:07.849 AM)

Ch1 0.733723 %

Frequency (5/8/2023 9:22:07.849 AM)

Ch1 1.00000 kHz

Line Gain -10 600 Termination: Level and Gain -10

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/8/2023 9:22:10.798 AM)

Channel Lower Limit Value Upper Limit
Ch1 -11.500 dBu +10.077 dBu -8.500 dBu ▲

Result: A FAILED

5/8/2023 9:23 AM Page 21 of 45



Line Gain +5 200kTermination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

DCX is not detected.

W(watts) (Input Power):

5/8/2023 9:23 AM Page 22 of 45



Line Gain +5 200kTermination : Verify Connections

Waveform: Sine

Generator Level: 0.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/8/2023 9:22:16.568 AM)

Ch1 5.019 Vrms

Gain (5/8/2023 9:22:16.568 AM)

Ch1 16.233 dB

THD+N Ratio (5/8/2023 9:22:16.568 AM)

Ch1 0.270513 %

Frequency (5/8/2023 9:22:16.568 AM)

Ch1 1.00000 kHz

5/8/2023 9:23 AM Page 23 of 45



Line Gain +5 200kTermination : Stepped Frequency Sweep +5

Generator Level: 0.000 dBu
DC Offset: 0.000 V
EQ: None

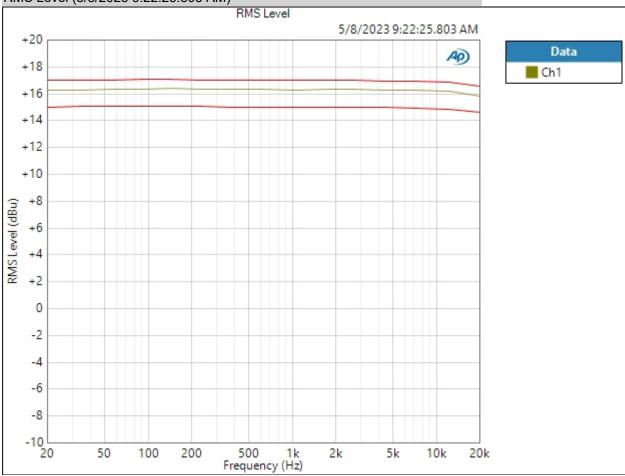
Start Frequency: 20.0000 kHz
Stop Frequency: 20.0000 Hz
Step Type: Logarithmic

Number of Points: 15

Weighting Filter: Signal Path
High-pass Filter: 20 Hz
Phase Ref Channel: Ch1

Measured 1 5/8/2023 9:22:25 AM

RMS Level (5/8/2023 9:22:25.803 AM)



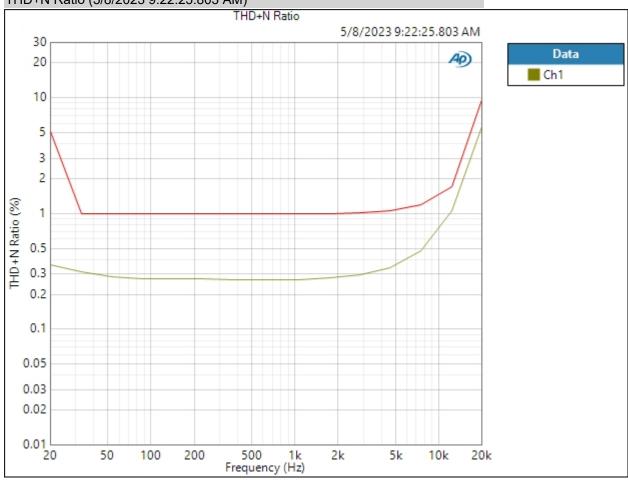
Ch1 🔮 PASSED

5/8/2023 9:23 AM Page 24 of 45



Result: V PASSED

THD+N Ratio (5/8/2023 9:22:25.803 AM)



Ch1 🔮 PASSED

Result: V PASSED

5/8/2023 9:23 AM Page 25 of 45



Line Gain +5 600 Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 600 ohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms 10.00 mVrms dBSPL2: dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

DCX is not detected.

W(watts) (Input Power):

5/8/2023 9:23 AM Page 26 of 45



Line Gain +5 600 Termination : Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu DC Offset: 0.000 V 1.00000 kHz

Frequency:

RMS Level (5/8/2023 9:22:31.741 AM)

Ch1 1.399 Vrms

Gain (5/8/2023 9:22:31.741 AM)

Ch1 15.136 dB

THD+N Ratio (5/8/2023 9:22:31.741 AM)

Ch1 0.294616 %

Frequency (5/8/2023 9:22:31.741 AM)

Ch1 1.00000 kHz

Line Gain +5 600 Termination: Level and Gain +5

Waveform: Sine

Generator Level: -10.000 dBu DC Offset: 0.000 V Frequency: 1.00000 kHz

RMS Level (5/8/2023 9:22:34.939 AM)

Channel Lower Limit Value **Upper Limit** +6.500 dBu Ch1 +3.500 dBu +5.135 dBu **3**

Result: V PASSED

5/8/2023 9:23 AM Page 27 of 45



Line Gain -5 600 Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 600 ohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms 10.00 mVrms dBSPL2: dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

DCX is not detected.

W(watts) (Input Power):

5/8/2023 9:23 AM Page 28 of 45



Line Gain -5 600 Termination: Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/8/2023 9:22:40.879 AM)

Ch1 433.3 mVrms

Gain (5/8/2023 9:22:40.879 AM)

Ch1 4.955 dB

THD+N Ratio (5/8/2023 9:22:40.879 AM)

Ch1 0.194819 %

Frequency (5/8/2023 9:22:40.879 AM)

Ch1 1.00000 kHz

Line Gain -5 600 Termination: Level and Gain -5

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/8/2023 9:22:44.118 AM)

Result: V PASSED

5/8/2023 9:23 AM Page 29 of 45



Line Gain 0 600 Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 600 ohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms 10.00 mVrms dBSPL2: dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL

• DCX

DCX is not detected.

dBm (Input Power):

W(watts) (Input Power):

5/8/2023 9:23 AM Page 30 of 45

600.0 ohm



Line Gain 0 600 Termination: Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/8/2023 9:22:49.974 AM)

Ch1 755.9 mVrms

Gain (5/8/2023 9:22:49.974 AM)

Ch1 9.788 dB

THD+N Ratio (5/8/2023 9:22:49.974 AM)

Ch1 0.174775 %

Frequency (5/8/2023 9:22:49.974 AM)

Ch1 1.00000 kHz

Line Gain 0 600 Termination: Level and Gain 0

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/8/2023 9:22:53.180 AM)

Result: V PASSED

5/8/2023 9:23 AM Page 31 of 45



Line Gain +10 600 Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 600 ohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

DCX is not detected.

W(watts) (Input Power):

5/8/2023 9:23 AM Page 32 of 45



Line Gain +10 600 Termination: Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/8/2023 9:22:59.184 AM)

Ch1 2.472 Vrms

Gain (5/8/2023 9:22:59.184 AM)

Ch1 20.080 dB

THD+N Ratio (5/8/2023 9:22:59.184 AM)

Ch1 0.722154 %

Frequency (5/8/2023 9:22:59.184 AM)

Ch1 1.00000 kHz

Line Gain +10 600 Termination: Level and Gain +10

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/8/2023 9:23:02.475 AM)

Result: V PASSED

5/8/2023 9:23 AM Page 33 of 45



Line Gain +10 200k Termination Level Hi : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms 10.00 mVrms dBSPL2: dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL

• DCX

DCX is not detected.

W(watts) (Input Power):

dBm (Input Power):

5/8/2023 9:23 AM Page 34 of 45

600.0 ohm



Line Gain +10 200k Termination Level Hi: Verify Connections

Waveform: Sine

Generator Level: -20.000 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/8/2023 9:23:08.479 AM)

Ch1 889.8 mVrms

Gain (5/8/2023 9:23:08.479 AM)

Ch1 21.204 dB

THD+N Ratio (5/8/2023 9:23:08.479 AM)

Ch1 2.608313 %

Frequency (5/8/2023 9:23:08.479 AM)

Ch1 1.00000 kHz

5/8/2023 9:23 AM Page 35 of 45



Line Gain +10 200k Termination Level Hi: Noise Recorder (RMS) CW

Waveform: None
Low-pass Filter: 20 kHz
Weighting Filter: Signal Path

High-pass Filter: 20 Hz

Sweep Time: 0.00:00:03.000

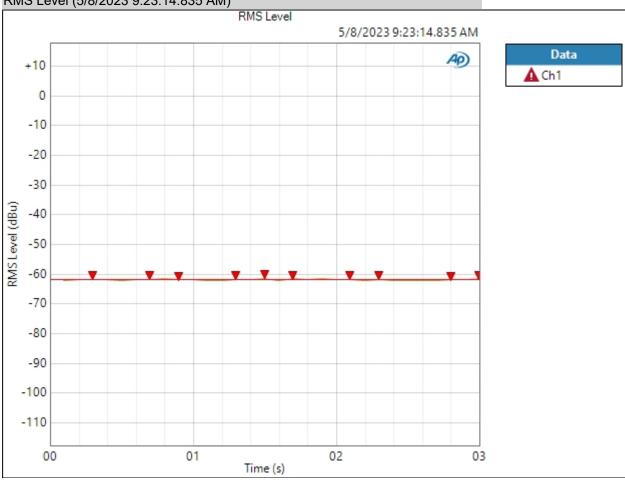
Reading Rate: 10/sec

Input Bandwidth: Use Signal Path

Record Acquisition: False

Measured 1 5/8/2023 9:23:14 AM

RMS Level (5/8/2023 9:23:14.835 AM)



Result: A FAILED

5/8/2023 9:23 AM Page 36 of 45



Line Gain +10 200k Termination Level Low: Signal Path Setup

Output Connector: Analog Balanced

Channels:

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm W(watts) (Input Power): 8.000 ohm

• DCX

DCX is not detected.

5/8/2023 9:23 AM Page 37 of 45



Line Gain +10 200k Termination Level Low: Verify Connections

Waveform: Sine

Generator Level: -20.000 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/8/2023 9:23:21.523 AM)

Ch1 889.7 mVrms

Gain (5/8/2023 9:23:21.523 AM)

Ch1 21.204 dB

THD+N Ratio (5/8/2023 9:23:21.523 AM)

Ch1 2.629235 %

Frequency (5/8/2023 9:23:21.523 AM)

Ch1 1.00000 kHz

5/8/2023 9:23 AM Page 38 of 45



Line Gain +10 200k Termination Level Low: Noise Recorder (RMS) CCW

Waveform: None
Low-pass Filter: 20 kHz
Weighting Filter: Signal Path

High-pass Filter: 20 Hz

Sweep Time: 0.00:00:03.000

Reading Rate: 10/sec

Input Bandwidth: Use Signal Path

Record Acquisition: False

Measured 1 5/8/2023 9:23:27 AM

RMS Level (5/8/2023 9:23:27.794 AM)



Result: A FAILED

5/8/2023 9:23 AM Page 39 of 45



Hi Z Gain -10 2.2M 200k Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 2

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

DCX is not detected.

W(watts) (Input Power):

5/8/2023 9:23 AM Page 40 of 45



Hi Z Gain -10 2.2M 200k Termination: Verify Connections

Waveform: Sine

Generator Level: -22.300 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/8/2023 9:23:34.232 AM)

Ch1 23.33 mVrms

Gain (5/8/2023 9:23:34.232 AM)

Ch1 -8.121 dB

THD+N Ratio (5/8/2023 9:23:34.232 AM)

Ch1 ---- %

Frequency (5/8/2023 9:23:34.232 AM)

Ch1 ---- Hz

Hi Z Gain -10 2.2M 200k Termination: Level and Gain 2.2M

Waveform: Sine

Generator Level: -22.300 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/8/2023 9:23:38.205 AM)

Channel Lower Limit Value Upper Limit
Ch1 -2.000 dBu -1.095 dBu +2.000 dBu ❖

Result: V PASSED

5/8/2023 9:23 AM Page 41 of 45



Hi Z Gain -10 47k 200k Termination : Signal Path Setup
Output Connector:
Analog Unbalanced

Channels: 2

Source Impedance: 50 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL

W(watts) (Input Power):

• DCX

DCX is not detected.

dBm (Input Power):

5/8/2023 9:23 AM Page 42 of 45

600.0 ohm



Hi Z Gain -10 47k 200k Termination : Verify Connections

Waveform: Sine

Generator Level: -22.300 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/8/2023 9:23:44.358 AM)

Ch1 23.20 mVrms

Gain (5/8/2023 9:23:44.358 AM)

Ch1 -8.166 dB

THD+N Ratio (5/8/2023 9:23:44.358 AM)

Ch1 ---- %

Frequency (5/8/2023 9:23:44.358 AM)

Ch1 ---- Hz

Hi Z Gain -10 47k 200k Termination: Level and Gain 47K

Waveform: Sine

Generator Level: -22.300 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/8/2023 9:23:47.894 AM)

Channel Lower Limit Value Upper Limit
Ch1 -8.000 dBu -30.468 dBu -4.000 dBu ▲

Result: A FAILED

5/8/2023 9:23 AM Page 43 of 45



Dummy Signal Path For Report : Signal Path Setup

Output Connector: Analog Unbalanced

Channels: 2

Source Impedance: 50 ohm
Output EQ: None

Input Connector: Analog Unbalanced

Channels: 2

Termination: 100 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms 94.000 dBSPL dBSPL1 Calibrator Level: dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

DCX is not detected.

W(watts) (Input Power):

5/8/2023 9:23 AM Page 44 of 45



Dummy Signal Path For Report : Verify Connections

Waveform: Sine

Generator Level: 100.0 mVrms

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/8/2023 9:23:52.361 AM)

Ch1 15.85 uVrms Ch2 7.300 uVrms

5/8/2023 9:23 AM Page 45 of 45