

## Sequence Report



### Summary

#### Mic500 200k Termination

Signal Path Setup PASSED

Stepped Frequency Sweep MIC 500 FAILED

#### Mic 2k 200k termination

Signal Path Setup PASSED

Stepped Frequency Sweep MIC 2K FAILED

#### Mic 2k 15dB PAD 200k termination

Signal Path Setup PASSED

Stepped Frequency Sweep 15dB PAD FAILED

#### Line Gain -10 200kTermination

Signal Path Setup PASSED

Stepped Frequency Sweep -10 FAILED

#### Line Gain -10 600 Termination

Signal Path Setup PASSED

Level and Gain -10 PASSED

#### Line Gain +5 200kTermination

Signal Path Setup PASSED

Stepped Frequency Sweep +5 FAILED

#### Line Gain +5 600 Termination

Signal Path Setup PASSED

Level and Gain +5 FAILED

#### Line Gain -5 600 Termination

Signal Path Setup PASSED

Level and Gain -5 FAILED

#### Line Gain 0 600 Termination

Signal Path Setup PASSED

Level and Gain 0 FAILED

#### Line Gain +10 600 Termination

Signal Path Setup PASSED

Level and Gain +10 FAILED

#### Line Gain +10 200k Termination Level Hi

Signal Path Setup PASSED

Noise Recorder (RMS) CW FAILED

#### Line Gain +10 200k Termination Level Low

Signal Path Setup PASSED

Noise Recorder (RMS) CCW FAILED

#### Hi Z Gain -10 2.2M 200k Termination

Signal Path Setup	✓ PASSED
Level and Gain 2.2M	⚠ FAILED
Hi Z Gain -10 47k 200k Termination	
Signal Path Setup	✓ PASSED
Level and Gain 47K	⚠ FAILED
Dummy Signal Path For Report	
Signal Path Setup	✓ PASSED
Sequence Result:	
Sequence Result:	⚠ FAILED

## Sequence Report



### Mic500 200k Termination : Signal Path Setup

Output Connector:	Analog Balanced
Channels:	1
Source Impedance:	100 ohm
Output EQ:	None
Input Connector:	Analog Balanced
Channels:	1
Channel:	Ch1
Termination:	200 kohm
Input Bandwidth:	AC (<10 Hz) - 90k (192 kHz SR)
Device Delay:	0.000 s
Input EQ:	None

#### • References

dBr G:	100.0 mVrms
dBm (Output Power):	600.0 ohm
W(watts) (Output Power):	8.000 ohm
Shared Frequency Reference:	1.00000 kHz
dBrA:	1.000 Vrms
dBrB:	1.000 Vrms
dBrA Offset:	0.000 dB
dBrB Offset:	0.000 dB
dB SPL1:	10.00 mVrms
dB SPL2:	10.00 mVrms
dB SPL1 Calibrator Level:	94.000 dB SPL
dB SPL2 Calibrator Level:	94.000 dB SPL
dBm (Input Power):	600.0 ohm
W(watts) (Input Power):	8.000 ohm

#### • DCX

DC Output 1:	0.000 V
DC Output 1:	Off
DC Output 2:	0.000 V
DC Output 2:	Off
Port A (hex):	00
Port B (hex):	00
Port C (hex):	00

5/2/2023 1:48 PM

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Port D (hex): 00

### Mic500 200k Termination : Verify Connections

Waveform: Sine

Generator Level: -42.300 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 1:45:27.575 PM)

Ch1 297.3 mVrms

Gain (5/2/2023 1:45:27.575 PM)

Ch1 33.983 dB

THD+N Ratio (5/2/2023 1:45:27.575 PM)

Ch1 ---- %

Frequency (5/2/2023 1:45:27.575 PM)

Ch1 ---- Hz

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Mic500 200k Termination : Stepped Frequency Sweep MIC 500

Generator Level: -42.300 dBu

DC Offset: 0.000 V

EQ: None

Start Frequency: 20.0000 kHz

Stop Frequency: 20.0000 Hz

Step Type: Logarithmic

Number of Points: 10

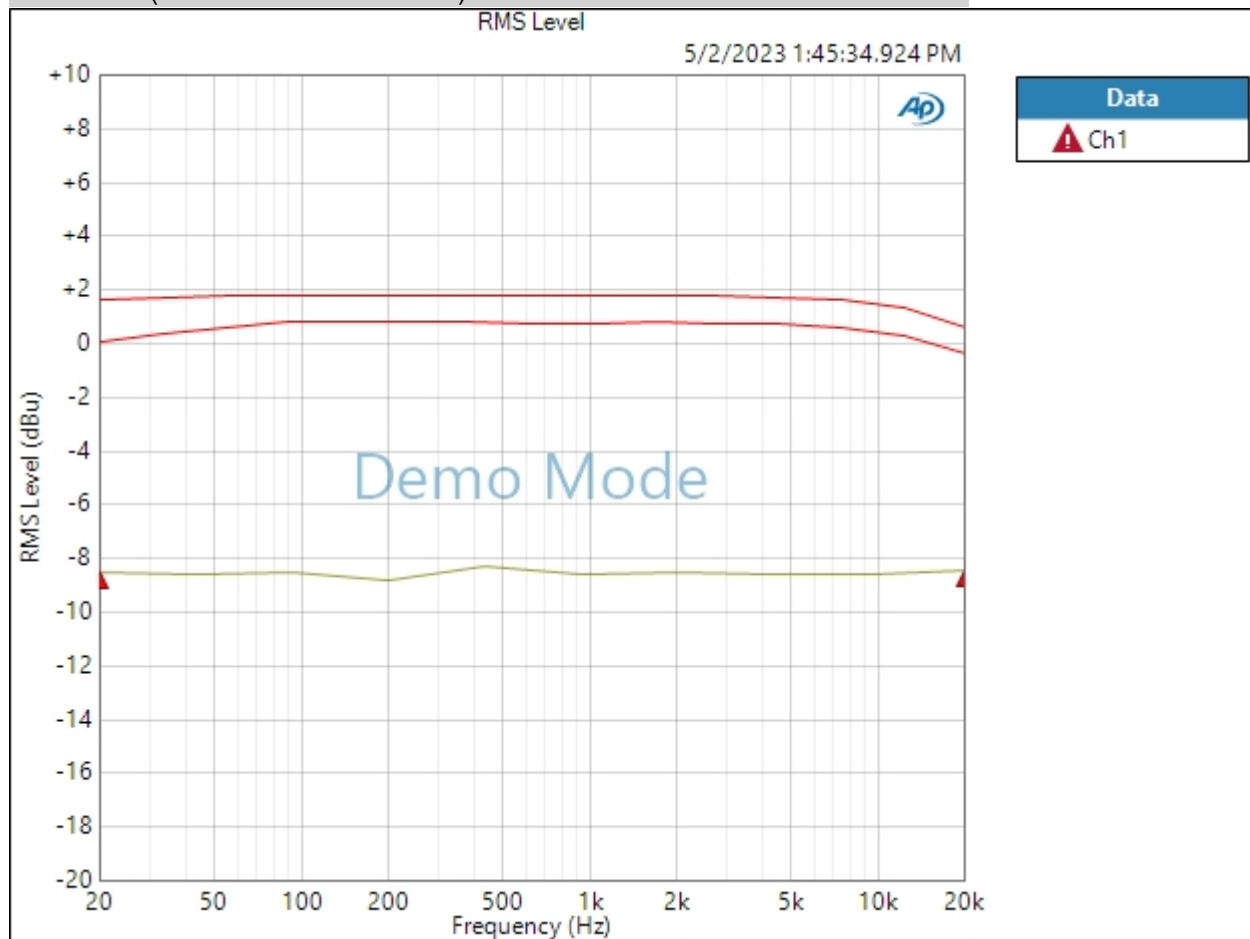
Weighting Filter: Signal Path

High-pass Filter: 20 Hz

Phase Ref Channel: Ch1

Measured 1 5/2/2023 1:45:34 PM

RMS Level (5/2/2023 1:45:34.924 PM)



Ch1 Failed Lower Limit

5/2/2023 1:48 PM

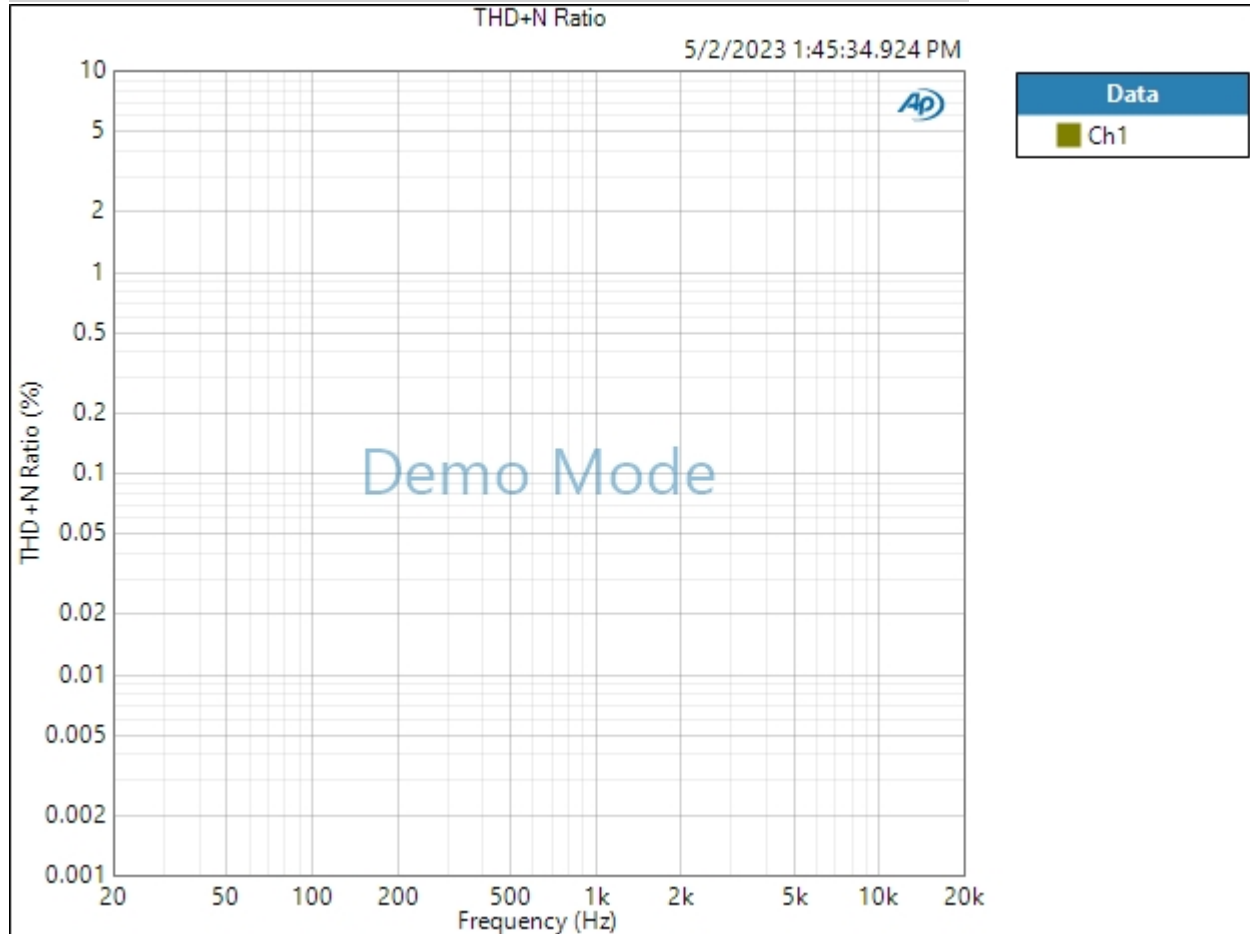
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Result: ▲ FAILED

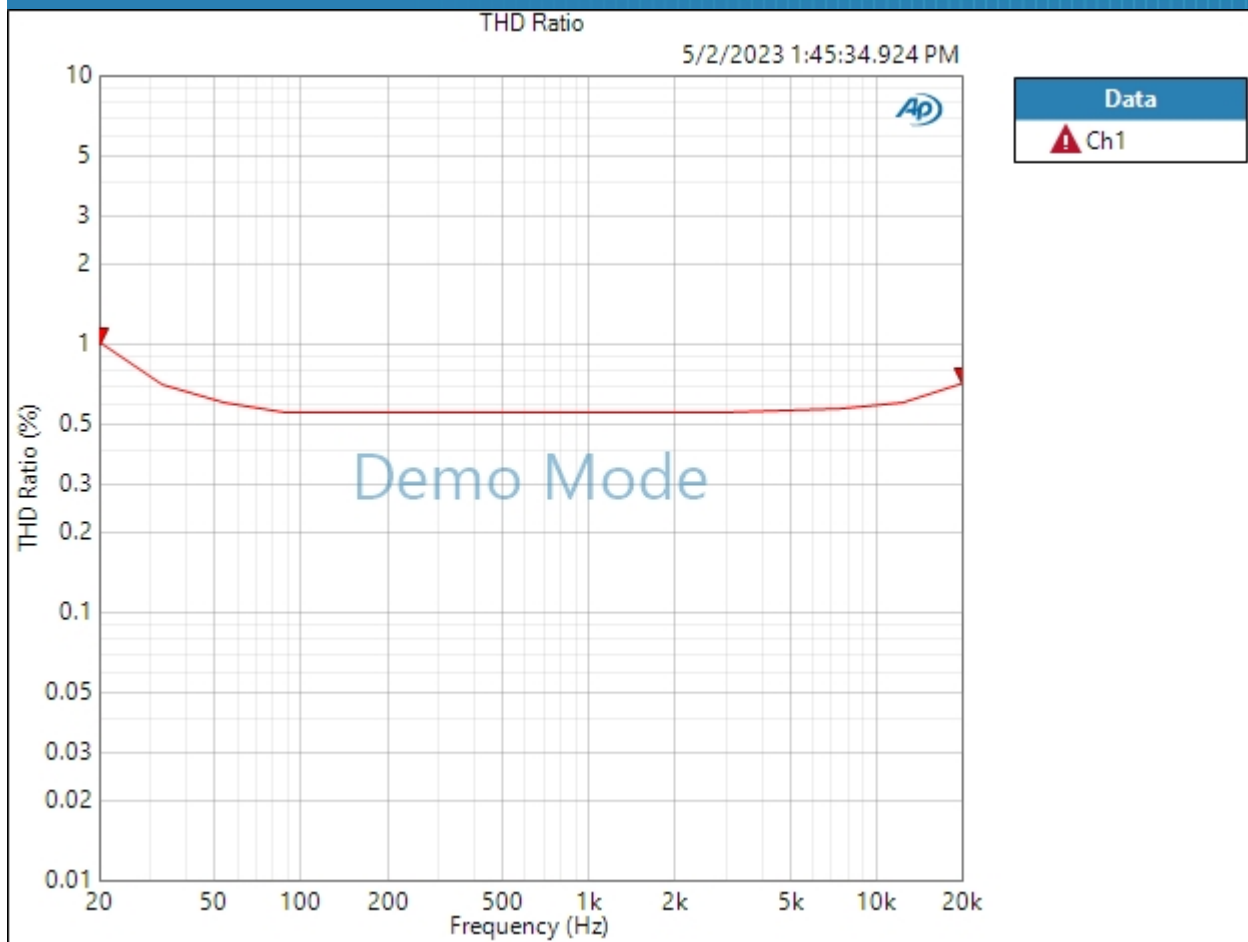
THD+N Ratio (5/2/2023 1:45:34.924 PM)



Result: ✔ PASSED

THD Ratio (5/2/2023 1:45:34.924 PM)

## Sequence Report



Ch1 Failed Upper Limit

Result: FAILED

## Sequence Report



### Mic 2k 200k termination : Signal Path Setup

Output Connector:	Analog Balanced
Channels:	1
Source Impedance:	100 ohm
Output EQ:	None
Input Connector:	Analog Balanced
Channels:	1
Channel:	Ch1
Termination:	200 kohm
Input Bandwidth:	AC (<10 Hz) - 90k (192 kHz SR)
Device Delay:	0.000 s
Input EQ:	None

#### • References

dBr G:	100.0 mVrms
dBm (Output Power):	600.0 ohm
W(watts) (Output Power):	8.000 ohm
Shared Frequency Reference:	1.00000 kHz
dBrA:	1.000 Vrms
dBrB:	1.000 Vrms
dBrA Offset:	0.000 dB
dBrB Offset:	0.000 dB
dB SPL1:	10.00 mVrms
dB SPL2:	10.00 mVrms
dB SPL1 Calibrator Level:	94.000 dB SPL
dB SPL2 Calibrator Level:	94.000 dB SPL
dBm (Input Power):	600.0 ohm
W(watts) (Input Power):	8.000 ohm

#### • DCX

DC Output 1:	0.000 V
DC Output 1:	Off
DC Output 2:	0.000 V
DC Output 2:	Off
Port A (hex):	00
Port B (hex):	00
Port C (hex):	00



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Port D (hex): 00

### Mic 2k 200k termination : Verify Connections

Waveform: Sine

Generator Level: -42.300 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 1:45:40.051 PM)

Ch1 286.3 mVrms

Gain (5/2/2023 1:45:40.051 PM)

Ch1 33.656 dB

THD+N Ratio (5/2/2023 1:45:40.051 PM)

Ch1 ---- %

Frequency (5/2/2023 1:45:40.051 PM)

Ch1 ---- Hz

## Sequence Report



Mic 2k 200k termination : Stepped Frequency Sweep MIC 2K

Generator Level: -42.300 dBu

DC Offset: 0.000 V

EQ: None

Start Frequency: 20.0000 kHz

Stop Frequency: 20.0000 Hz

Step Type: Logarithmic

Number of Points: 10

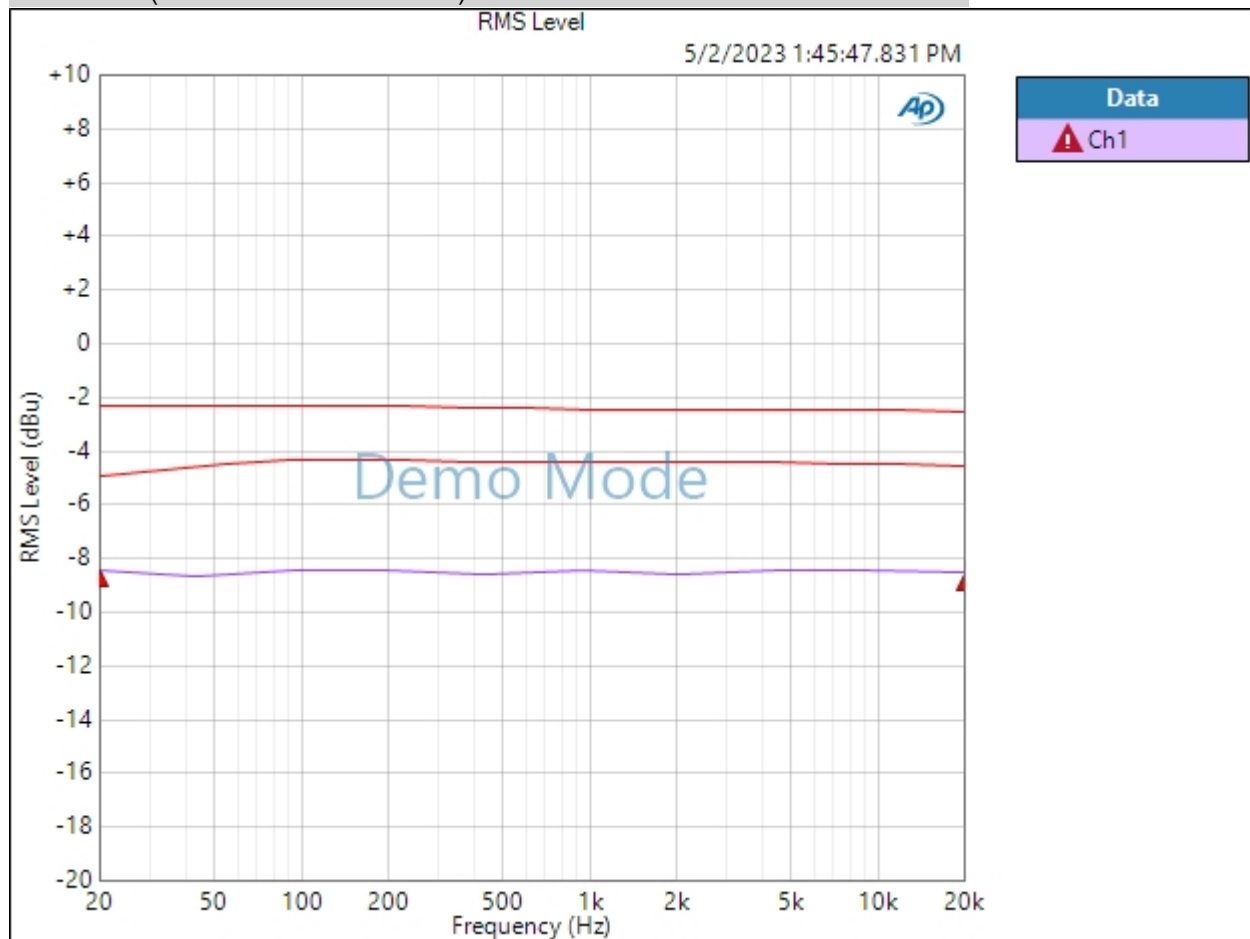
Weighting Filter: Signal Path

High-pass Filter: 20 Hz

Phase Ref Channel: Ch1

Measured 1 5/2/2023 1:45:47 PM

RMS Level (5/2/2023 1:45:47.831 PM)



Ch1 Failed Lower Limit

5/2/2023 1:48 PM

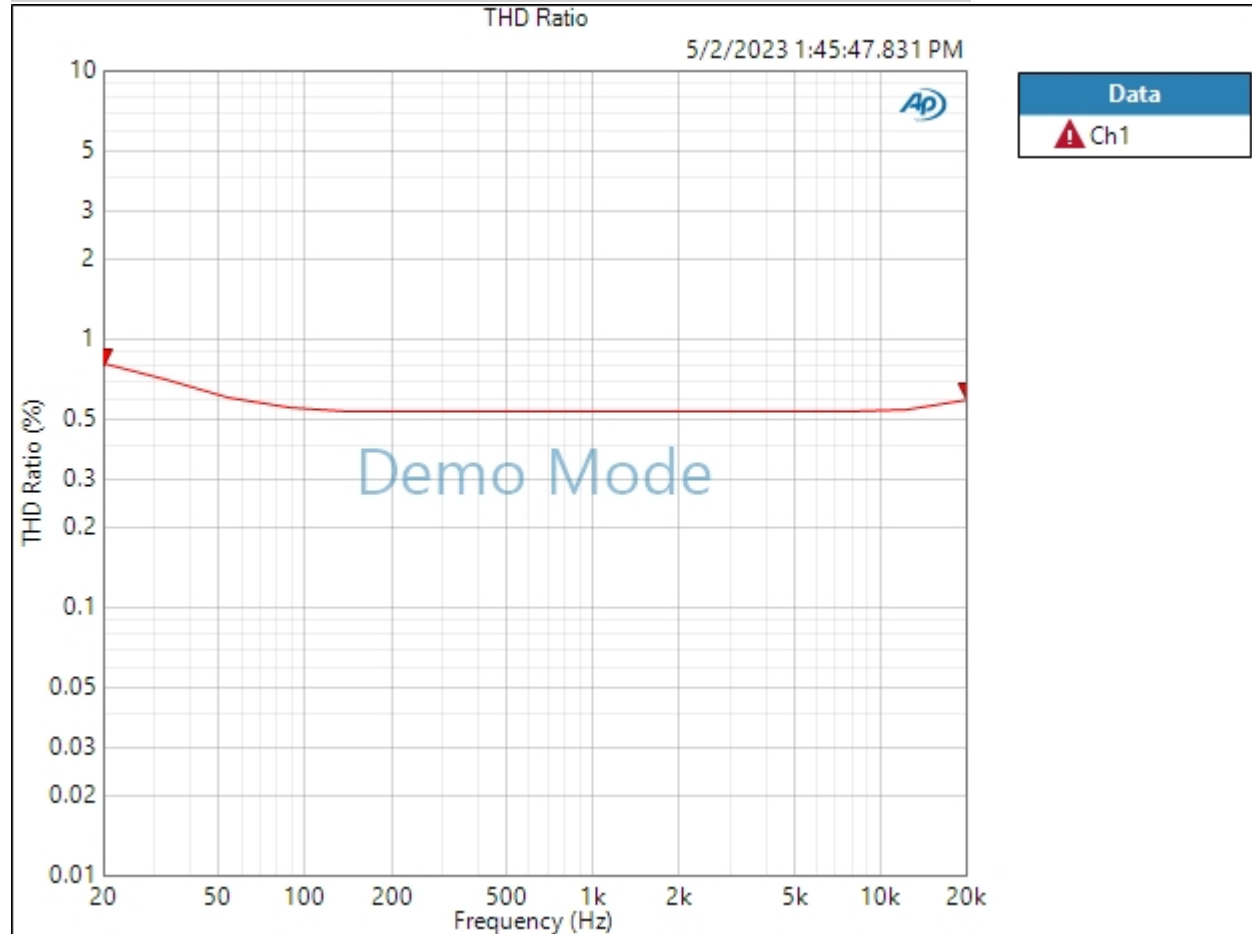
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Result: ▲ FAILED

THD Ratio (5/2/2023 1:45:47.831 PM)



Ch1 ▲ Failed Upper Limit

Result: ▲ FAILED

## Sequence Report



Mic 2k 15dB PAD 200k termination : Signal Path Setup

Output Connector: Analog Balanced  
Channels: 1  
Source Impedance: 100 ohm  
Output EQ: None  
Input Connector: Analog Balanced  
Channels: 1  
Channel: Ch1  
Termination: 200 kohm  
Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)  
Device Delay: 0.000 s  
Input EQ: None

### • References

dBr G: 100.0 mVrms  
dBm (Output Power): 600.0 ohm  
W(watts) (Output Power): 8.000 ohm  
Shared Frequency Reference: 1.00000 kHz  
dBrA: 1.000 Vrms  
dBrB: 1.000 Vrms  
dBrA Offset: 0.000 dB  
dBrB Offset: 0.000 dB  
dBSPL1: 10.00 mVrms  
dBSPL2: 10.00 mVrms  
dBSPL1 Calibrator Level: 94.000 dB SPL  
dBSPL2 Calibrator Level: 94.000 dB SPL  
dBm (Input Power): 600.0 ohm  
W(watts) (Input Power): 8.000 ohm

### • DCX

DC Output 1: 0.000 V  
DC Output 1: Off  
DC Output 2: 0.000 V  
DC Output 2: Off  
Port A (hex): 00  
Port B (hex): 00  
Port C (hex): 00

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Port D (hex): 00

Mic 2k 15dB PAD 200k termination : Verify Connections

Waveform: Sine

Generator Level: -42.300 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 1:45:54.047 PM)

Ch1 282.9 mVrms

Gain (5/2/2023 1:45:54.047 PM)

Ch1 33.551 dB

THD+N Ratio (5/2/2023 1:45:54.047 PM)

Ch1 ---- %

Frequency (5/2/2023 1:45:54.047 PM)

Ch1 ---- Hz

## Sequence Report



Mic 2k 15dB PAD 200k termination : Stepped Frequency Sweep 15dB PAD

Generator Level: -42.000 dBu

DC Offset: 0.000 V

EQ: None

Start Frequency: 20.0000 kHz

Stop Frequency: 20.0000 Hz

Step Type: Logarithmic

Number of Points: 10

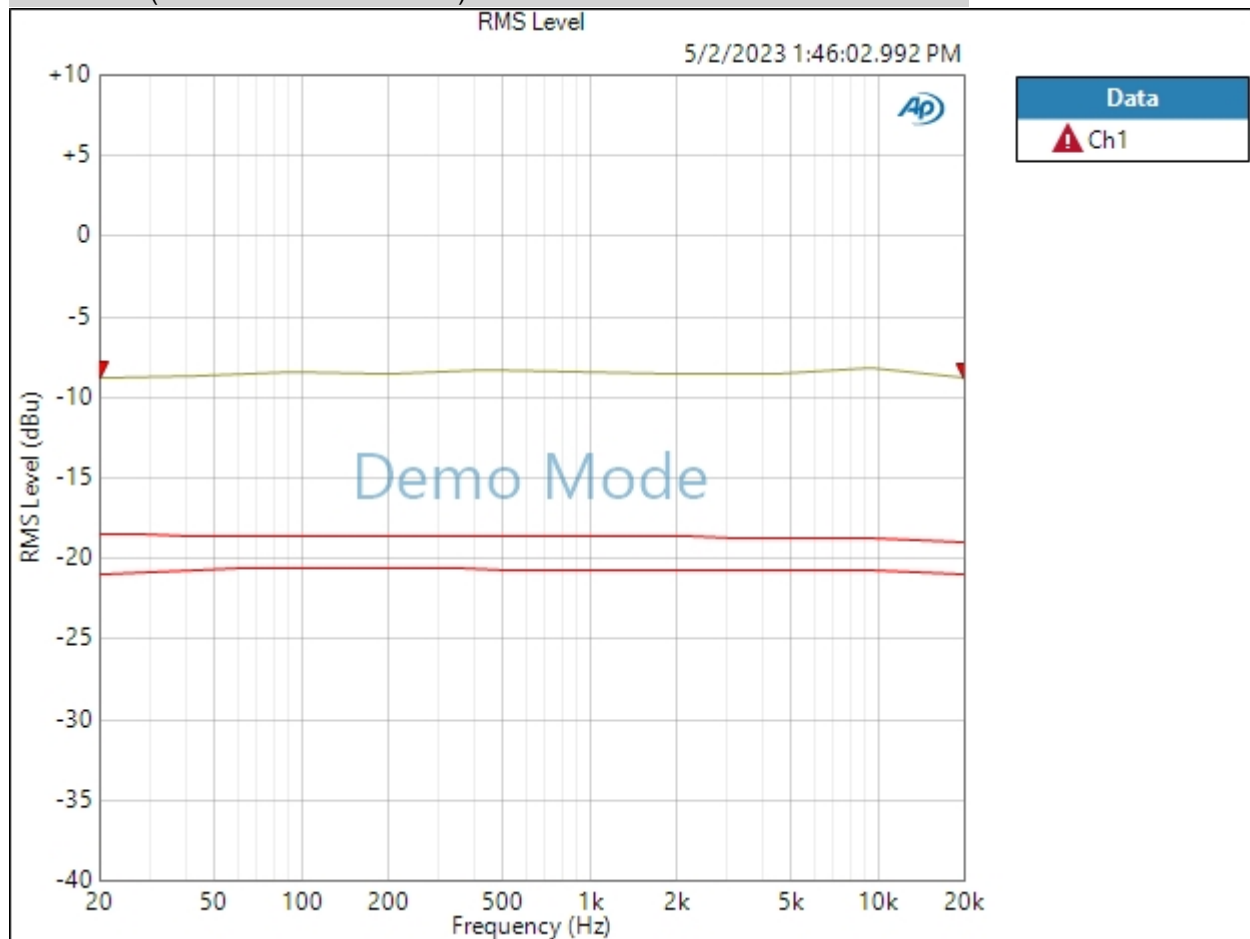
Weighting Filter: Signal Path

High-pass Filter: 20 Hz

Phase Ref Channel: Ch1

Measured 1 5/2/2023 1:46:02 PM

RMS Level (5/2/2023 1:46:02.992 PM)



Ch1 Failed Upper Limit

5/2/2023 1:48 PM

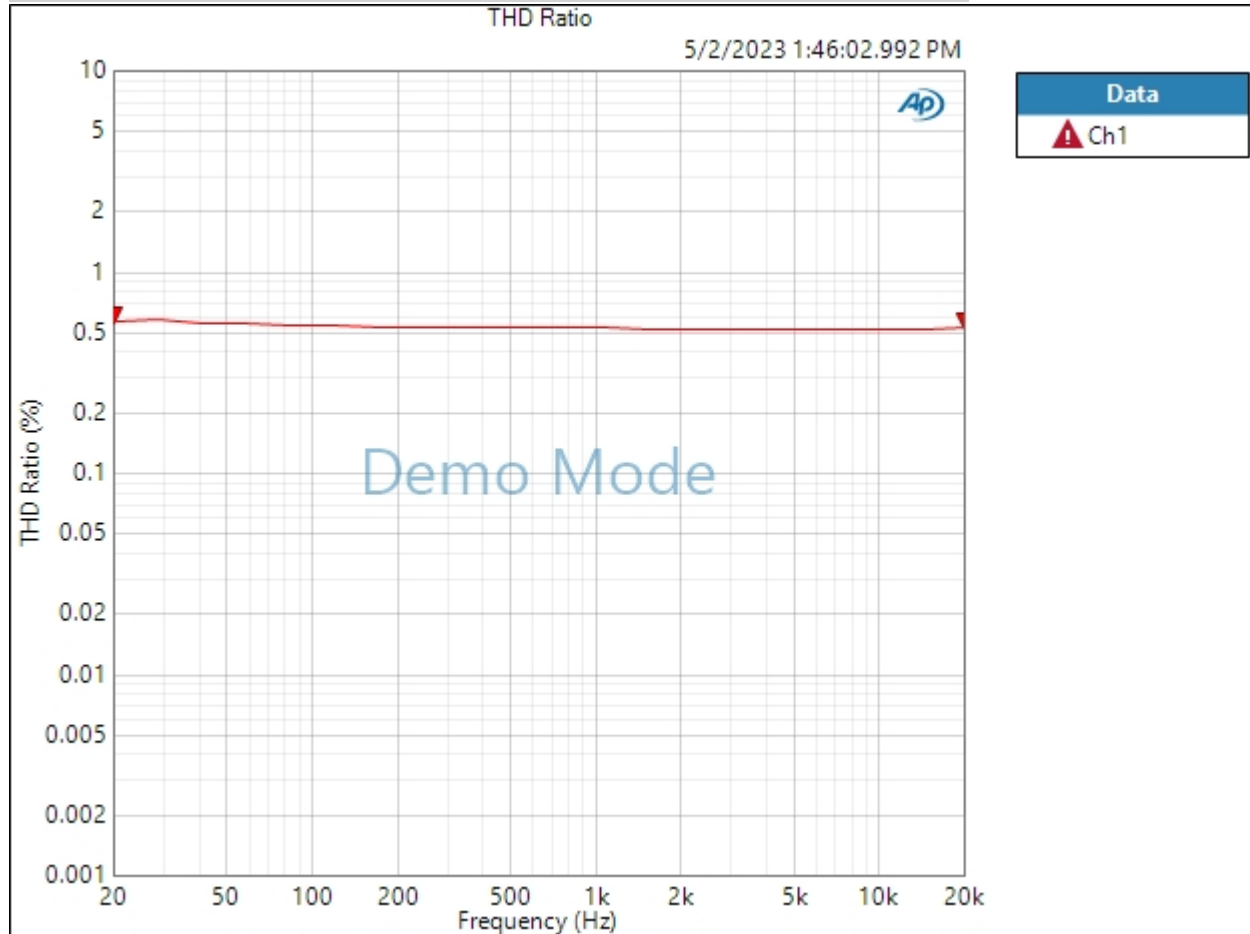
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## Sequence Report



Result: ▲ FAILED

THD Ratio (5/2/2023 1:46:02.992 PM)



Ch1 ▲ Failed Upper Limit

Result: ▲ FAILED

## Sequence Report



### Line Gain -10 200kTermination : Signal Path Setup

Output Connector: Analog Balanced  
Channels: 1  
Source Impedance: 100 ohm  
Output EQ: None  
Input Connector: Analog Balanced  
Channels: 1  
Channel: Ch1  
Termination: 200 kohm  
Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)  
Device Delay: 0.000 s  
Input EQ: None

#### • References

dBr G: 100.0 mVrms  
dBm (Output Power): 600.0 ohm  
W(watts) (Output Power): 8.000 ohm  
Shared Frequency Reference: 1.00000 kHz  
dBrA: 1.000 Vrms  
dBrB: 1.000 Vrms  
dBrA Offset: 0.000 dB  
dBrB Offset: 0.000 dB  
dBSPL1: 10.00 mVrms  
dBSPL2: 10.00 mVrms  
dBSPL1 Calibrator Level: 94.000 dB SPL  
dBSPL2 Calibrator Level: 94.000 dB SPL  
dBm (Input Power): 600.0 ohm  
W(watts) (Input Power): 8.000 ohm

#### • DCX

DC Output 1: 0.000 V  
DC Output 1: Off  
DC Output 2: 0.000 V  
DC Output 2: Off  
Port A (hex): 00  
Port B (hex): 00  
Port C (hex): 00



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Port D (hex): 00

### Line Gain -10 200kTermination : Verify Connections

Waveform: Sine

Generator Level: 0.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 1:46:08.101 PM)

Ch1 290.9 mVrms

Gain (5/2/2023 1:46:08.101 PM)

Ch1 -8.506 dB

THD+N Ratio (5/2/2023 1:46:08.101 PM)

Ch1 ---- %

Frequency (5/2/2023 1:46:08.101 PM)

Ch1 ---- Hz

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Line Gain -10 200kTermination : Stepped Frequency Sweep -10

Generator Level: 0.000 dBu

DC Offset: 0.000 V

EQ: None

Start Frequency: 20.0000 kHz

Stop Frequency: 20.0000 Hz

Step Type: Logarithmic

Number of Points: 15

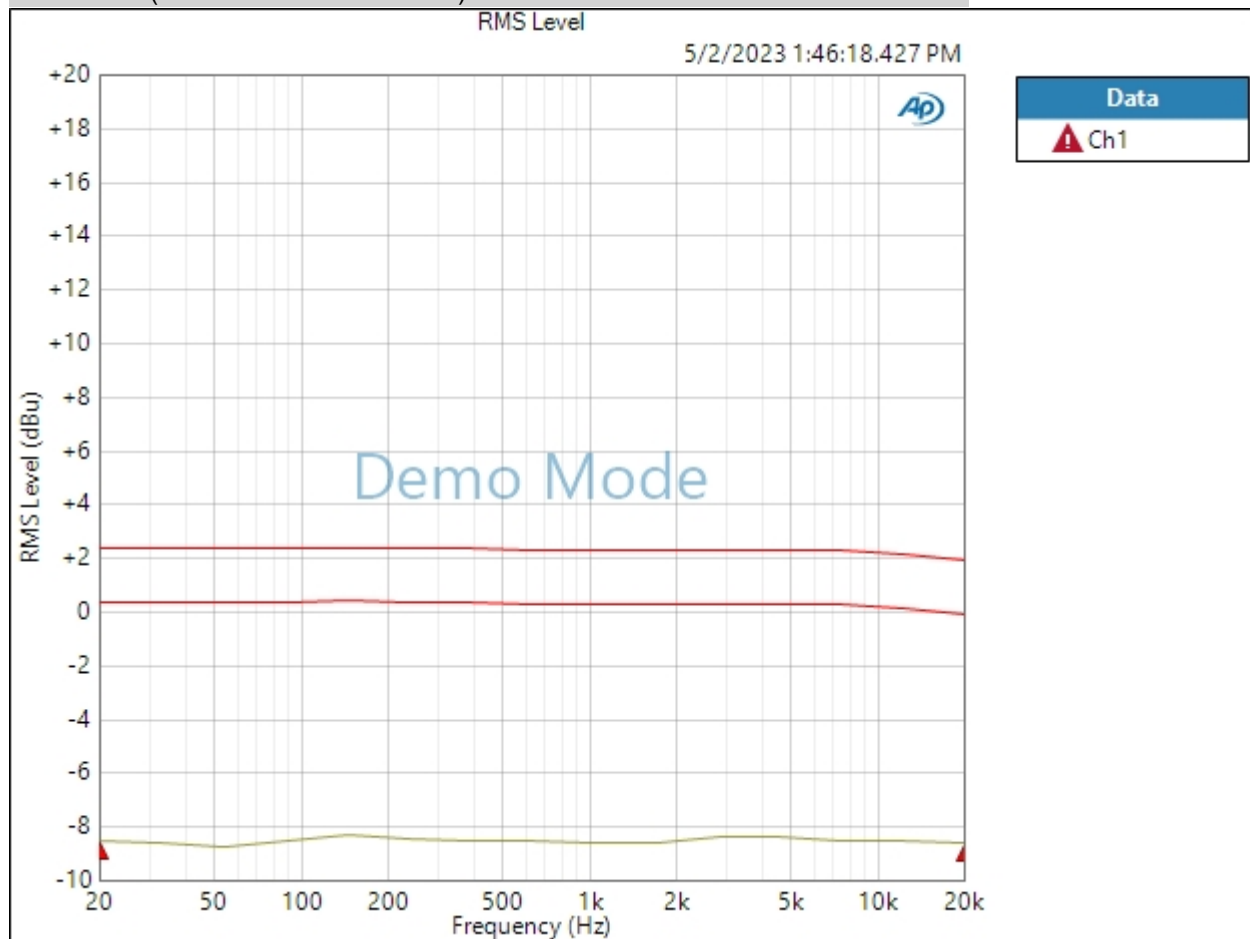
Weighting Filter: Signal Path

High-pass Filter: 20 Hz

Phase Ref Channel: Ch1

Measured 1 5/2/2023 1:46:18 PM

RMS Level (5/2/2023 1:46:18.427 PM)



Ch1 Failed Lower Limit

5/2/2023 1:48 PM

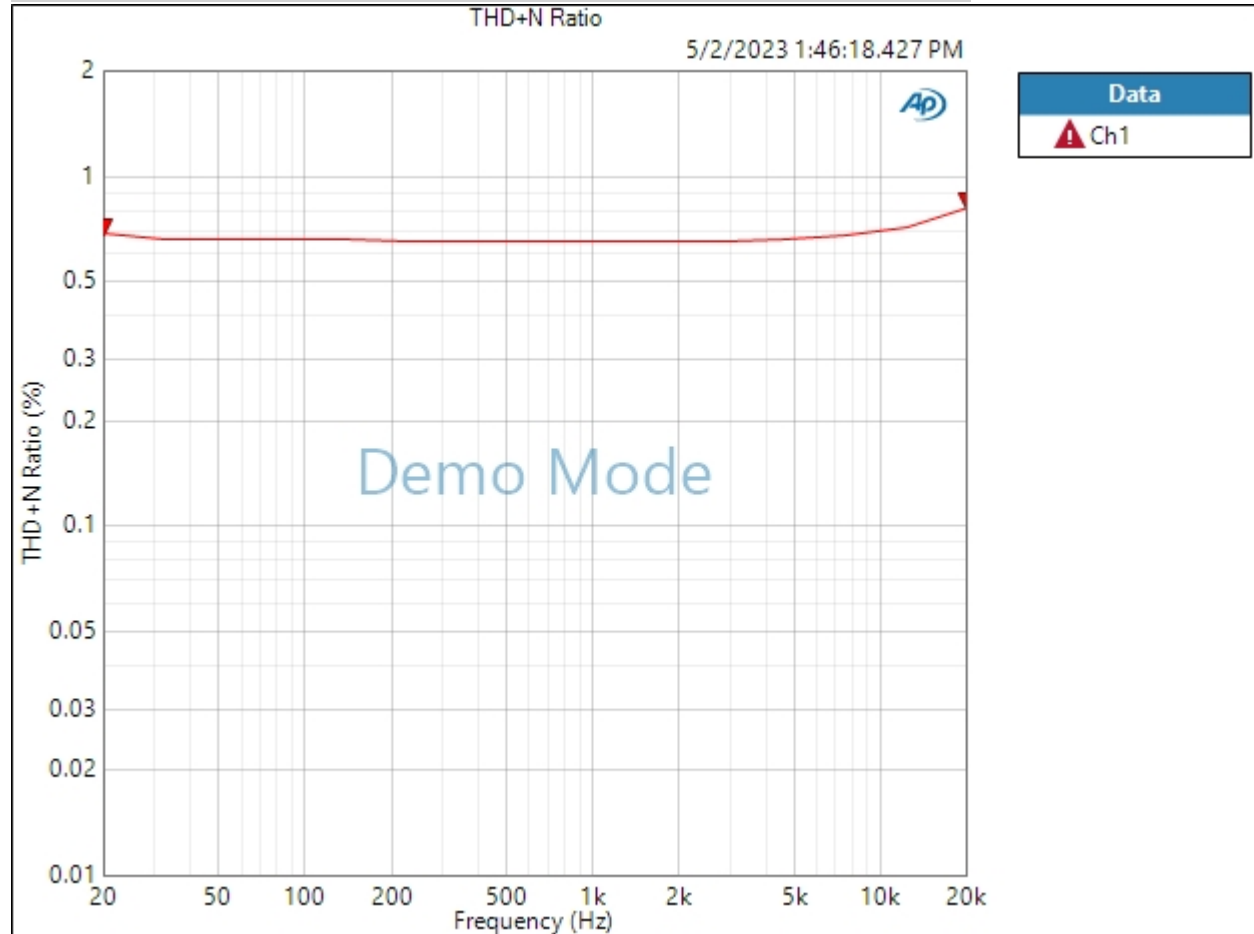
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## Sequence Report



Result: ▲ FAILED

THD+N Ratio (5/2/2023 1:46:18.427 PM)



Ch1 ▲ Failed Upper Limit

Result: ▲ FAILED

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### Line Gain -10 600 Termination : Signal Path Setup

Output Connector:	Analog Balanced
Channels:	1
Source Impedance:	100 ohm
Output EQ:	None
Input Connector:	Analog Balanced
Channels:	1
Channel:	Ch1
Termination:	600 ohm
Input Bandwidth:	AC (<10 Hz) - 90k (192 kHz SR)
Device Delay:	0.000 s
Input EQ:	None

#### • References

dBr G:	100.0 mVrms
dBm (Output Power):	600.0 ohm
W(watts) (Output Power):	8.000 ohm
Shared Frequency Reference:	1.00000 kHz
dBrA:	1.000 Vrms
dBrB:	1.000 Vrms
dBrA Offset:	0.000 dB
dBrB Offset:	0.000 dB
dB SPL1:	10.00 mVrms
dB SPL2:	10.00 mVrms
dB SPL1 Calibrator Level:	94.000 dB SPL
dB SPL2 Calibrator Level:	94.000 dB SPL
dBm (Input Power):	600.0 ohm
W(watts) (Input Power):	8.000 ohm

#### • DCX

DC Output 1:	0.000 V
DC Output 1:	Off
DC Output 2:	0.000 V
DC Output 2:	Off
Port A (hex):	00
Port B (hex):	00
Port C (hex):	00

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Port D (hex): 00

### Line Gain -10 600 Termination : Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 1:46:23.672 PM)

Ch1 280.0 mVrms

Gain (5/2/2023 1:46:23.672 PM)

Ch1 1.161 dB

THD+N Ratio (5/2/2023 1:46:23.672 PM)

Ch1 ---- %

Frequency (5/2/2023 1:46:23.672 PM)

Ch1 ---- Hz

### Line Gain -10 600 Termination : Level and Gain -10

Waveform: Sine

Generator Level: -10.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 1:46:26.351 PM)

Channel	Lower Limit	Value	Upper Limit	
Ch1	-11.500 dBu	-8.640 dBu	-8.500 dBu	✓

Result: ✓ PASSED

## Sequence Report



### Line Gain +5 200kTermination : Signal Path Setup

Output Connector: Analog Balanced  
Channels: 1  
Source Impedance: 100 ohm  
Output EQ: None  
Input Connector: Analog Balanced  
Channels: 1  
Channel: Ch1  
Termination: 200 kohm  
Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)  
Device Delay: 0.000 s  
Input EQ: None

#### • References

dBr G: 100.0 mVrms  
dBm (Output Power): 600.0 ohm  
W(watts) (Output Power): 8.000 ohm  
Shared Frequency Reference: 1.00000 kHz  
dBrA: 1.000 Vrms  
dBrB: 1.000 Vrms  
dBrA Offset: 0.000 dB  
dBrB Offset: 0.000 dB  
dBSPL1: 10.00 mVrms  
dBSPL2: 10.00 mVrms  
dBSPL1 Calibrator Level: 94.000 dB SPL  
dBSPL2 Calibrator Level: 94.000 dB SPL  
dBm (Input Power): 600.0 ohm  
W(watts) (Input Power): 8.000 ohm

#### • DCX

DC Output 1: 0.000 V  
DC Output 1: Off  
DC Output 2: 0.000 V  
DC Output 2: Off  
Port A (hex): 00  
Port B (hex): 00  
Port C (hex): 00

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Port D (hex): 00

### Line Gain +5 200kTermination : Verify Connections

Waveform: Sine

Generator Level: 0.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 1:46:31.556 PM)

Ch1 296.3 mVrms

Gain (5/2/2023 1:46:31.556 PM)

Ch1 -8.346 dB

THD+N Ratio (5/2/2023 1:46:31.556 PM)

Ch1 ---- %

Frequency (5/2/2023 1:46:31.556 PM)

Ch1 ---- Hz

## Sequence Report



Line Gain +5 200kTermination : Stepped Frequency Sweep +5

Generator Level: 0.000 dBu

DC Offset: 0.000 V

EQ: None

Start Frequency: 20.0000 kHz

Stop Frequency: 20.0000 Hz

Step Type: Logarithmic

Number of Points: 15

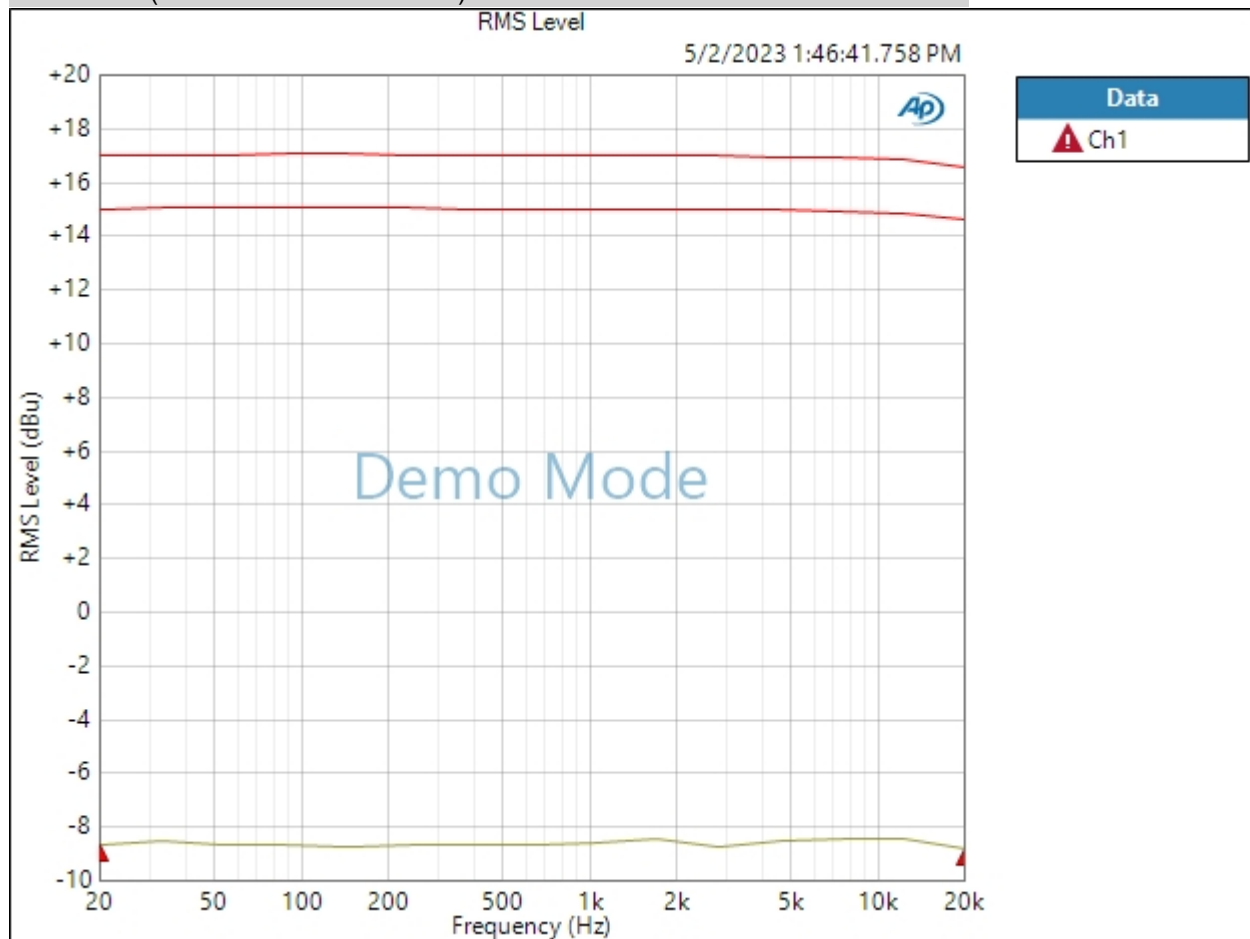
Weighting Filter: Signal Path

High-pass Filter: 20 Hz

Phase Ref Channel: Ch1

Measured 1 5/2/2023 1:46:41 PM

RMS Level (5/2/2023 1:46:41.758 PM)



Ch1 Failed Lower Limit

5/2/2023 1:48 PM

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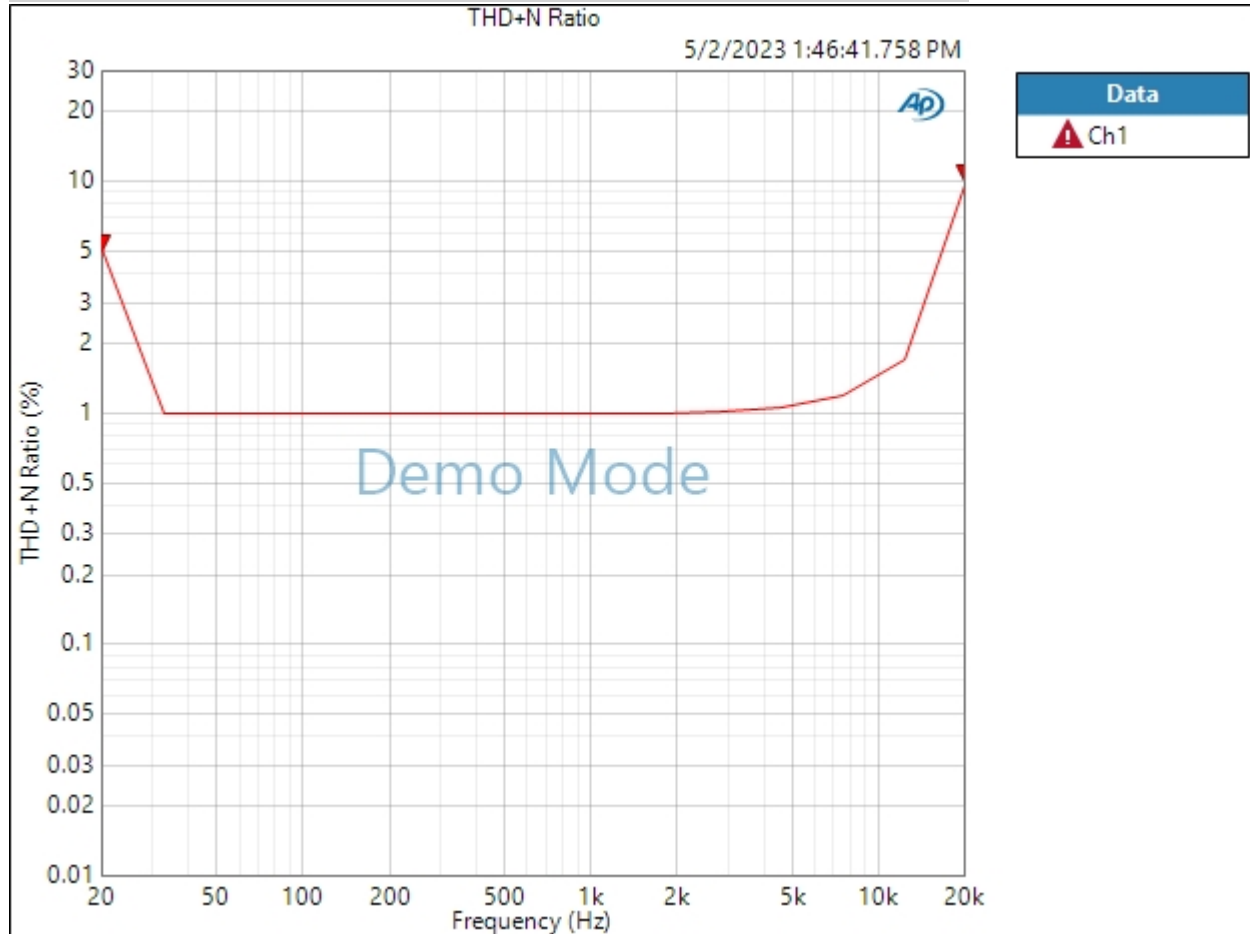


## Sequence Report



Result: ▲ FAILED

THD+N Ratio (5/2/2023 1:46:41.758 PM)



Ch1 ▲ Failed Upper Limit

Result: ▲ FAILED

## Sequence Report



### Line Gain +5 600 Termination : Signal Path Setup

Output Connector:	Analog Balanced
Channels:	1
Source Impedance:	100 ohm
Output EQ:	None
Input Connector:	Analog Balanced
Channels:	1
Channel:	Ch1
Termination:	600 ohm
Input Bandwidth:	AC (<10 Hz) - 90k (192 kHz SR)
Device Delay:	0.000 s
Input EQ:	None

#### • References

dBr G:	100.0 mVrms
dBm (Output Power):	600.0 ohm
W(watts) (Output Power):	8.000 ohm
Shared Frequency Reference:	1.00000 kHz
dBrA:	1.000 Vrms
dBrB:	1.000 Vrms
dBrA Offset:	0.000 dB
dBrB Offset:	0.000 dB
dB SPL1:	10.00 mVrms
dB SPL2:	10.00 mVrms
dB SPL1 Calibrator Level:	94.000 dB SPL
dB SPL2 Calibrator Level:	94.000 dB SPL
dBm (Input Power):	600.0 ohm
W(watts) (Input Power):	8.000 ohm

#### • DCX

DC Output 1:	0.000 V
DC Output 1:	Off
DC Output 2:	0.000 V
DC Output 2:	Off
Port A (hex):	00
Port B (hex):	00
Port C (hex):	00

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Port D (hex): 00

### Line Gain +5 600 Termination : Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 1:46:48.055 PM)

Ch1 292.7 mVrms

Gain (5/2/2023 1:46:48.055 PM)

Ch1 1.547 dB

THD+N Ratio (5/2/2023 1:46:48.055 PM)

Ch1 ---- %

Frequency (5/2/2023 1:46:48.055 PM)

Ch1 ---- Hz

### Line Gain +5 600 Termination : Level and Gain +5


Waveform: Sine

Generator Level: -10.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 1:46:51.821 PM)

Channel	Lower Limit	Value	Upper Limit	
Ch1	+3.500 dBu	-8.632 dBu	+6.500 dBu	

Result:  FAILED

## Sequence Report



### Line Gain -5 600 Termination : Signal Path Setup

Output Connector: Analog Balanced  
Channels: 1  
Source Impedance: 100 ohm  
Output EQ: None  
Input Connector: Analog Balanced  
Channels: 1  
Channel: Ch1  
Termination: 600 ohm  
Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)  
Device Delay: 0.000 s  
Input EQ: None

#### • References

dBr G: 100.0 mVrms  
dBm (Output Power): 600.0 ohm  
W(watts) (Output Power): 8.000 ohm  
Shared Frequency Reference: 1.00000 kHz  
dBrA: 1.000 Vrms  
dBrB: 1.000 Vrms  
dBrA Offset: 0.000 dB  
dBrB Offset: 0.000 dB  
dBSPL1: 10.00 mVrms  
dBSPL2: 10.00 mVrms  
dBSPL1 Calibrator Level: 94.000 dB SPL  
dBSPL2 Calibrator Level: 94.000 dB SPL  
dBm (Input Power): 600.0 ohm  
W(watts) (Input Power): 8.000 ohm

#### • DCX

DC Output 1: 0.000 V  
DC Output 1: Off  
DC Output 2: 0.000 V  
DC Output 2: Off  
Port A (hex): 00  
Port B (hex): 00  
Port C (hex): 00

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Port D (hex): 00

### Line Gain -5 600 Termination : Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 1:46:58.549 PM)

Ch1 300.1 mVrms

Gain (5/2/2023 1:46:58.549 PM)

Ch1 1.763 dB

THD+N Ratio (5/2/2023 1:46:58.549 PM)

Ch1 ---- %

Frequency (5/2/2023 1:46:58.549 PM)

Ch1 ---- Hz

### Line Gain -5 600 Termination : Level and Gain -5

Waveform: Sine

Generator Level: -10.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 1:47:02.344 PM)

Channel	Lower Limit	Value	Upper Limit	
Ch1	-6.500 dBu	-8.410 dBu	-3.500 dBu	

Result:  FAILED

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### Line Gain 0 600 Termination : Signal Path Setup

Output Connector: Analog Balanced  
Channels: 1  
Source Impedance: 100 ohm  
Output EQ: None  
Input Connector: Analog Balanced  
Channels: 1  
Channel: Ch1  
Termination: 600 ohm  
Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)  
Device Delay: 0.000 s  
Input EQ: None

#### • References

dBr G: 100.0 mVrms  
dBm (Output Power): 600.0 ohm  
W(watts) (Output Power): 8.000 ohm  
Shared Frequency Reference: 1.00000 kHz  
dBrA: 1.000 Vrms  
dBrB: 1.000 Vrms  
dBrA Offset: 0.000 dB  
dBrB Offset: 0.000 dB  
dBSPL1: 10.00 mVrms  
dBSPL2: 10.00 mVrms  
dBSPL1 Calibrator Level: 94.000 dB SPL  
dBSPL2 Calibrator Level: 94.000 dB SPL  
dBm (Input Power): 600.0 ohm  
W(watts) (Input Power): 8.000 ohm

#### • DCX

DC Output 1: 0.000 V  
DC Output 1: Off  
DC Output 2: 0.000 V  
DC Output 2: Off  
Port A (hex): 00  
Port B (hex): 00  
Port C (hex): 00

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Port D (hex): 00

### Line Gain 0 600 Termination : Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 1:47:08.550 PM)

Ch1 297.1 mVrms

Gain (5/2/2023 1:47:08.550 PM)

Ch1 1.675 dB

THD+N Ratio (5/2/2023 1:47:08.550 PM)

Ch1 ---- %

Frequency (5/2/2023 1:47:08.550 PM)

Ch1 ---- Hz

### Line Gain 0 600 Termination : Level and Gain 0


Waveform: Sine

Generator Level: -10.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 1:47:12.420 PM)

Channel	Lower Limit	Value	Upper Limit	
Ch1	-1.500 dBu	-8.560 dBu	+1.500 dBu	

Result:  FAILED

## Sequence Report



### Line Gain +10 600 Termination : Signal Path Setup

Output Connector: Analog Balanced  
Channels: 1  
Source Impedance: 100 ohm  
Output EQ: None  
Input Connector: Analog Balanced  
Channels: 1  
Channel: Ch1  
Termination: 600 ohm  
Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)  
Device Delay: 0.000 s  
Input EQ: None

#### • References

dBr G: 100.0 mVrms  
dBm (Output Power): 600.0 ohm  
W(watts) (Output Power): 8.000 ohm  
Shared Frequency Reference: 1.00000 kHz  
dBrA: 1.000 Vrms  
dBrB: 1.000 Vrms  
dBrA Offset: 0.000 dB  
dBrB Offset: 0.000 dB  
dBSPL1: 10.00 mVrms  
dBSPL2: 10.00 mVrms  
dBSPL1 Calibrator Level: 94.000 dB SPL  
dBSPL2 Calibrator Level: 94.000 dB SPL  
dBm (Input Power): 600.0 ohm  
W(watts) (Input Power): 8.000 ohm

#### • DCX

DC Output 1: 0.000 V  
DC Output 1: Off  
DC Output 2: 0.000 V  
DC Output 2: Off  
Port A (hex): 00  
Port B (hex): 00  
Port C (hex): 00



## Sequence Report



Port D (hex): 00

### Line Gain +10 600 Termination : Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 1:47:18.046 PM)

Ch1 287.0 mVrms

Gain (5/2/2023 1:47:18.046 PM)

Ch1 1.375 dB

THD+N Ratio (5/2/2023 1:47:18.046 PM)

Ch1 ---- %

Frequency (5/2/2023 1:47:18.046 PM)

Ch1 ---- Hz

### Line Gain +10 600 Termination : Level and Gain +10

Waveform: Sine

Generator Level: -10.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 1:47:21.941 PM)

Channel	Lower Limit	Value	Upper Limit
Ch1	+8.500 dBu	-8.521 dBu	+11.500 dBu



Result: FAILED

## Sequence Report



Line Gain +10 200k Termination Level Hi : Signal Path Setup

Output Connector: Analog Balanced  
Channels: 1  
Source Impedance: 100 ohm  
Output EQ: None  
Input Connector: Analog Balanced  
Channels: 1  
Channel: Ch1  
Termination: 200 kohm  
Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)  
Device Delay: 0.000 s  
Input EQ: None

### • References

dBr G: 100.0 mVrms  
dBm (Output Power): 600.0 ohm  
W(watts) (Output Power): 8.000 ohm  
Shared Frequency Reference: 1.00000 kHz  
dBrA: 1.000 Vrms  
dBrB: 1.000 Vrms  
dBrA Offset: 0.000 dB  
dBrB Offset: 0.000 dB  
dBSPL1: 10.00 mVrms  
dBSPL2: 10.00 mVrms  
dBSPL1 Calibrator Level: 94.000 dB SPL  
dBSPL2 Calibrator Level: 94.000 dB SPL  
dBm (Input Power): 600.0 ohm  
W(watts) (Input Power): 8.000 ohm

### • DCX

DC Output 1: 0.000 V  
DC Output 1: Off  
DC Output 2: 0.000 V  
DC Output 2: Off  
Port A (hex): 00  
Port B (hex): 00  
Port C (hex): 00

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## Sequence Report



Port D (hex): 00

Line Gain +10 200k Termination Level Hi : Verify Connections

Waveform: Sine

Generator Level: -20.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 1:47:27.332 PM)

Ch1 295.4 mVrms

Gain (5/2/2023 1:47:27.332 PM)

Ch1 11.628 dB

THD+N Ratio (5/2/2023 1:47:27.332 PM)

Ch1 ---- %

Frequency (5/2/2023 1:47:27.332 PM)

Ch1 ---- Hz

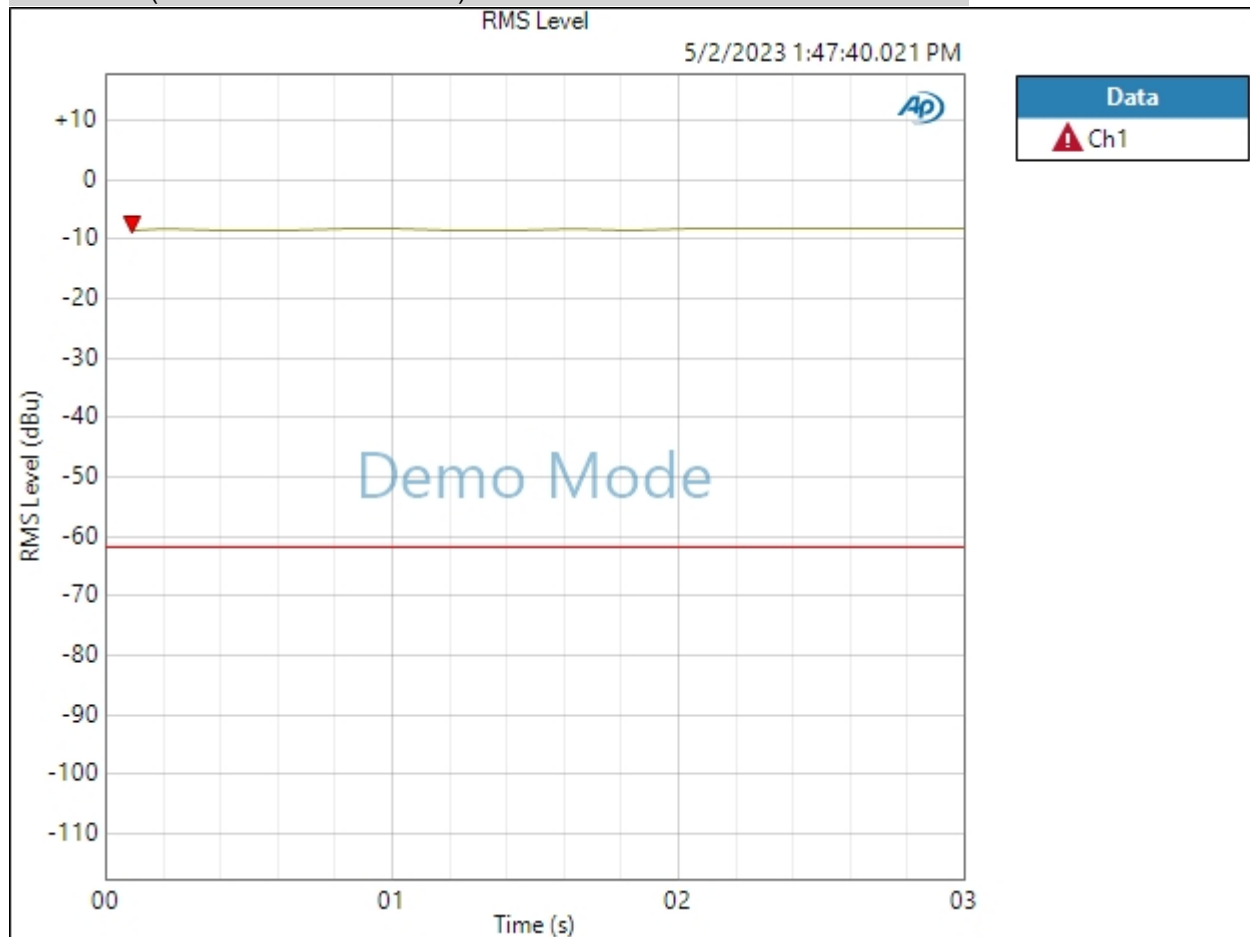
## Sequence Report



Line Gain +10 200k Termination Level Hi : Noise Recorder (RMS) CW

Waveform: None  
Low-pass Filter: 20 kHz  
Weighting Filter: Signal Path  
High-pass Filter: 20 Hz  
Sweep Time: 0.00:00:03.000  
Reading Rate: 10/sec  
Input Bandwidth: Use Signal Path  
Record Acquisition: False  
Measured 1 5/2/2023 1:47:40 PM

RMS Level (5/2/2023 1:47:40.021 PM)



Ch1 ! Failed Upper Limit

Result: ! FAILED

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## Sequence Report



### Line Gain +10 200k Termination Level Low : Signal Path Setup

Output Connector: Analog Balanced  
Channels: 1  
Source Impedance: 100 ohm  
Output EQ: None  
Input Connector: Analog Balanced  
Channels: 1  
Channel: Ch1  
Termination: 200 kohm  
Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)  
Device Delay: 0.000 s  
Input EQ: None

#### • References

dBr G: 100.0 mVrms  
dBm (Output Power): 600.0 ohm  
W(watts) (Output Power): 8.000 ohm  
Shared Frequency Reference: 1.00000 kHz  
dBrA: 1.000 Vrms  
dBrB: 1.000 Vrms  
dBrA Offset: 0.000 dB  
dBrB Offset: 0.000 dB  
dBSPL1: 10.00 mVrms  
dBSPL2: 10.00 mVrms  
dBSPL1 Calibrator Level: 94.000 dB SPL  
dBSPL2 Calibrator Level: 94.000 dB SPL  
dBm (Input Power): 600.0 ohm  
W(watts) (Input Power): 8.000 ohm

#### • DCX

DC Output 1: 0.000 V  
DC Output 1: Off  
DC Output 2: 0.000 V  
DC Output 2: Off  
Port A (hex): 00  
Port B (hex): 00  
Port C (hex): 00

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## Sequence Report



Port D (hex): 00

Line Gain +10 200k Termination Level Low : Verify Connections

Waveform: Sine

Generator Level: -20.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 1:47:45.365 PM)

Ch1 288.7 mVrms

Gain (5/2/2023 1:47:45.365 PM)

Ch1 11.427 dB

THD+N Ratio (5/2/2023 1:47:45.365 PM)

Ch1 ---- %

Frequency (5/2/2023 1:47:45.365 PM)

Ch1 ---- Hz

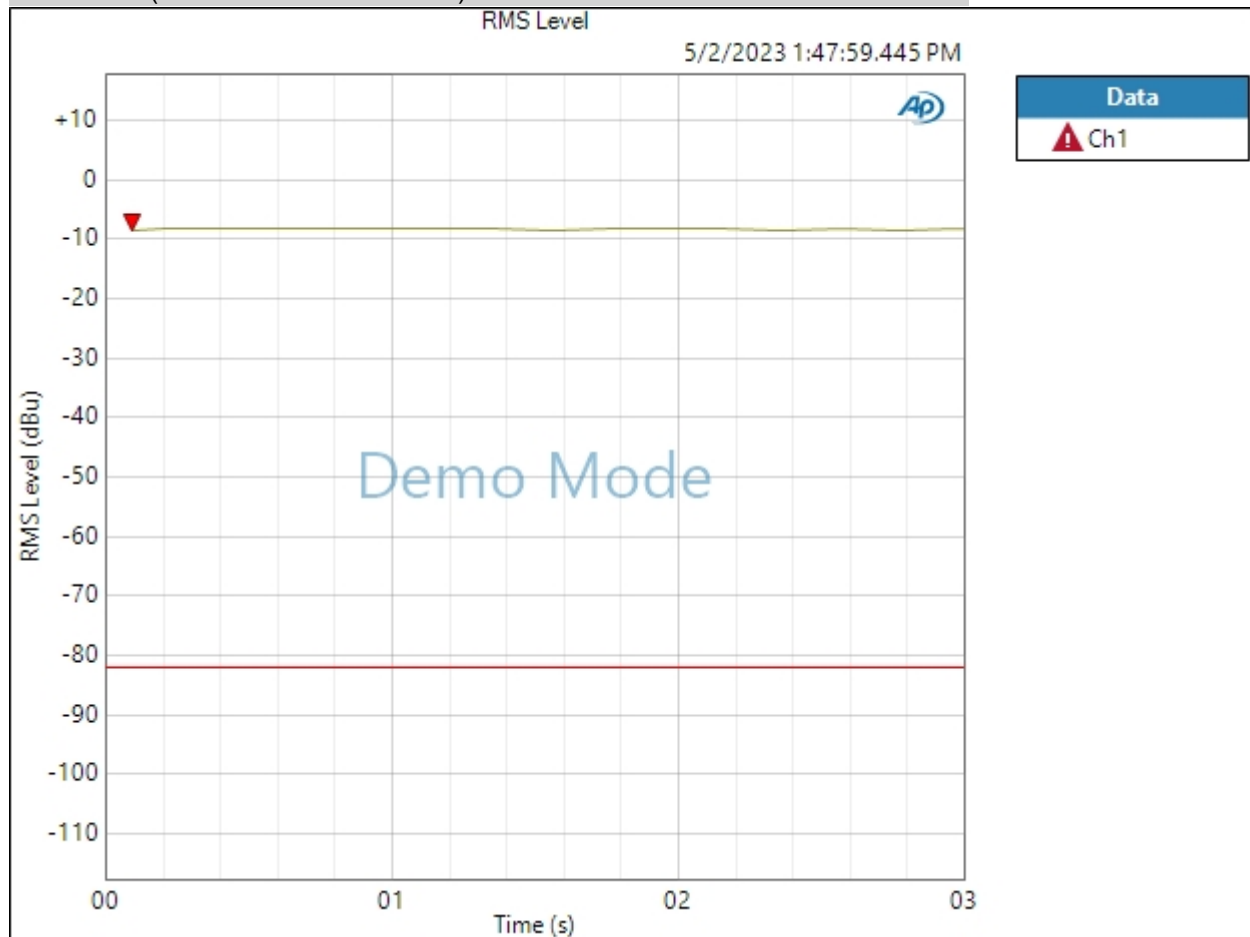
## Sequence Report



Line Gain +10 200k Termination Level Low : Noise Recorder (RMS) CCW

Waveform: None  
Low-pass Filter: 20 kHz  
Weighting Filter: Signal Path  
High-pass Filter: 20 Hz  
Sweep Time: 0.00:00:03.000  
Reading Rate: 10/sec  
Input Bandwidth: Use Signal Path  
Record Acquisition: False  
Measured 1 5/2/2023 1:47:59 PM

RMS Level (5/2/2023 1:47:59.445 PM)



Ch1 Failed Upper Limit

Result: FAILED

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## Sequence Report



Hi Z Gain -10 2.2M 200k Termination : Signal Path Setup

Output Connector: Analog Balanced  
Channels: 2  
Source Impedance: 100 ohm  
Output EQ: None  
Input Connector: Analog Balanced  
Channels: 1  
Channel: Ch1  
Termination: 200 kohm  
Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)  
Device Delay: 0.000 s  
Input EQ: None

### • References

dBr G: 100.0 mVrms  
dBm (Output Power): 600.0 ohm  
W(watts) (Output Power): 8.000 ohm  
Shared Frequency Reference: 1.00000 kHz  
dBrA: 1.000 Vrms  
dBrB: 1.000 Vrms  
dBrA Offset: 0.000 dB  
dBrB Offset: 0.000 dB  
dBSPL1: 10.00 mVrms  
dBSPL2: 10.00 mVrms  
dBSPL1 Calibrator Level: 94.000 dB SPL  
dBSPL2 Calibrator Level: 94.000 dB SPL  
dBm (Input Power): 600.0 ohm  
W(watts) (Input Power): 8.000 ohm

### • DCX

DC Output 1: 0.000 V  
DC Output 1: Off  
DC Output 2: 0.000 V  
DC Output 2: Off  
Port A (hex): 00  
Port B (hex): 00  
Port C (hex): 00

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## Sequence Report



Port D (hex): 00

### Hi Z Gain -10 2.2M 200k Termination : Verify Connections

Waveform: Sine

Generator Level: -22.300 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 1:48:04.869 PM)

Ch1 287.8 mVrms

Gain (5/2/2023 1:48:04.869 PM)

Ch1 13.699 dB

THD+N Ratio (5/2/2023 1:48:04.869 PM)

Ch1 ---- %

Frequency (5/2/2023 1:48:04.869 PM)

Ch1 ---- Hz

### Hi Z Gain -10 2.2M 200k Termination : Level and Gain 2.2M


Waveform: Sine

Generator Level: -22.300 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 1:48:10.400 PM)

Channel	Lower Limit	Value	Upper Limit	
Ch1	-2.000 dBu	-8.479 dBu	+2.000 dBu	

Result:  FAILED

## Sequence Report



Hi Z Gain -10 47k 200k Termination : Signal Path Setup

Output Connector: Analog Unbalanced  
Channels: 2  
Source Impedance: 50 ohm  
Output EQ: None  
Input Connector: Analog Balanced  
Channels: 1  
Channel: Ch1  
Termination: 200 kohm  
Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)  
Device Delay: 0.000 s  
Input EQ: None

### • References

dBr G: 100.0 mVrms  
dBm (Output Power): 600.0 ohm  
W(watts) (Output Power): 8.000 ohm  
Shared Frequency Reference: 1.00000 kHz  
dBrA: 1.000 Vrms  
dBrB: 1.000 Vrms  
dBrA Offset: 0.000 dB  
dBrB Offset: 0.000 dB  
dBSPL1: 10.00 mVrms  
dBSPL2: 10.00 mVrms  
dBSPL1 Calibrator Level: 94.000 dB SPL  
dBSPL2 Calibrator Level: 94.000 dB SPL  
dBm (Input Power): 600.0 ohm  
W(watts) (Input Power): 8.000 ohm

### • DCX

DC Output 1: 0.000 V  
DC Output 1: Off  
DC Output 2: 0.000 V  
DC Output 2: Off  
Port A (hex): 00  
Port B (hex): 00  
Port C (hex): 00

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## Sequence Report



Port D (hex): 00

### Hi Z Gain -10 47k 200k Termination : Verify Connections

Waveform: Sine

Generator Level: -22.300 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 1:48:16.050 PM)

Ch1 286.6 mVrms

Gain (5/2/2023 1:48:16.050 PM)

Ch1 13.665 dB

THD+N Ratio (5/2/2023 1:48:16.050 PM)

Ch1 ---- %

Frequency (5/2/2023 1:48:16.050 PM)

Ch1 ---- Hz

### Hi Z Gain -10 47k 200k Termination : Level and Gain 47K


Waveform: Sine

Generator Level: -22.300 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 1:48:19.914 PM)

Channel	Lower Limit	Value	Upper Limit	
Ch1	-8.000 dBu	-8.635 dBu	-4.000 dBu	

Result:  FAILED

## Sequence Report



### Dummy Signal Path For Report : Signal Path Setup

Output Connector:	Analog Unbalanced
Channels:	2
Source Impedance:	50 ohm
Output EQ:	None
Input Connector:	Analog Unbalanced
Channels:	2
Termination:	100 kohm
Input Bandwidth:	AC (<10 Hz) - 90k (192 kHz SR)
Device Delay:	0.000 s
Input EQ:	None

#### • References

dBr G:	100.0 mVrms
dBm (Output Power):	600.0 ohm
W(watts) (Output Power):	8.000 ohm
Shared Frequency Reference:	1.00000 kHz
dBrA:	1.000 Vrms
dBrB:	1.000 Vrms
dBrA Offset:	0.000 dB
dBrB Offset:	0.000 dB
dB SPL1:	10.00 mVrms
dB SPL2:	10.00 mVrms
dB SPL1 Calibrator Level:	94.000 dB SPL
dB SPL2 Calibrator Level:	94.000 dB SPL
dBm (Input Power):	600.0 ohm
W(watts) (Input Power):	8.000 ohm

#### • DCX

DC Output 1:	0.000 V
DC Output 1:	Off
DC Output 2:	0.000 V
DC Output 2:	Off
Port A (hex):	00
Port B (hex):	00
Port C (hex):	00
Port D (hex):	00

## Sequence Report



### Dummy Signal Path For Report : Verify Connections

Waveform: Sine  
Generator Level: 100.0 mVrms  
DC Offset: 0.000 V  
Frequency: 1.00000 kHz

### RMS Level (5/2/2023 1:48:23.562 PM)

Ch1 287.2 mVrms  
Ch2 286.0 mVrms