

Summary

Mic500 200k Termination	
Signal Path Setup	PASSED
Stepped Frequency Sweep MIC 500	▲ FAILED
Mic 2k 200k termination	
Signal Path Setup	PASSED
Stepped Frequency Sweep MIC 2K	🛕 FAILED
Mic 2k 15dB PAD 200k termination	
Signal Path Setup	PASSED
Stepped Frequency Sweep 15dB PAD	▲ FAILED
Line Gain -10 200kTermination	
Signal Path Setup	PASSED
Stepped Frequency Sweep -10	▲ FAILED
Line Gain -10 600 Termination	
Signal Path Setup	PASSED
Level and Gain -10	▲ FAILED
Line Gain +5 200kTermination	
Signal Path Setup	PASSED
Stepped Frequency Sweep +5	▼ PASSED
Line Gain +5 600 Termination	
Signal Path Setup	PASSED
Level and Gain +5	PASSED
Line Gain -5 600 Termination	
Signal Path Setup	PASSED
Level and Gain -5	▼ PASSED
Line Gain 0 600 Termination	
Signal Path Setup	PASSED
Level and Gain 0	▼ PASSED
Line Gain +10 600 Termination	
Signal Path Setup	PASSED
Level and Gain +10	♥ PASSED
Line Gain +10 200k Termination Level Hi	
Signal Path Setup	PASSED
Noise Recorder (RMS) CW	▲ FAILED
Line Gain +10 200k Termination Level Low	
Signal Path Setup	PASSED
Noise Recorder (RMS) CCW	▲ FAILED
Hi Z Gain -10 2.2M 200k Termination	

Signal Path Setup		♡ PA	ASSED
Level and Gain 2.2M		♡ PA	ASSED
Hi Z Gain -10 47k 200k 7	Termination		
Signal Path Setup		♡ PA	ASSED
Level and Gain 47K		▲ FA	AILED
Dummy Signal Path For	Report		
Signal Path Setup		♡ PA	ASSED
Sequence Result:			
Sequence Result:	▲ FAILED		

5/7/2023 5:07 PM Page 2 of 45



Mic500 200k Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms 10.00 mVrms dBSPL2: dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL

• DCX

DCX is not detected.

dBm (Input Power):

W(watts) (Input Power):

5/7/2023 5:07 PM Page 3 of 45

600.0 ohm



Mic500 200k Termination : Verify Connections

Waveform: Sine

Generator Level: -42.300 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/7/2023 5:04:11.384 PM)

Ch1 71.11 mVrms

Gain (5/7/2023 5:04:11.384 PM)

Ch1 21.558 dB

THD+N Ratio (5/7/2023 5:04:11.384 PM)

Ch1 ---- %

Frequency (5/7/2023 5:04:11.384 PM)

Ch1 ---- Hz

5/7/2023 5:07 PM Page 4 of 45



Mic500 200k Termination: Stepped Frequency Sweep MIC 500

Generator Level: -42.300 dBu
DC Offset: 0.000 V
EQ: None

Start Frequency: 20.0000 kHz
Stop Frequency: 20.0000 Hz
Step Type: Logarithmic

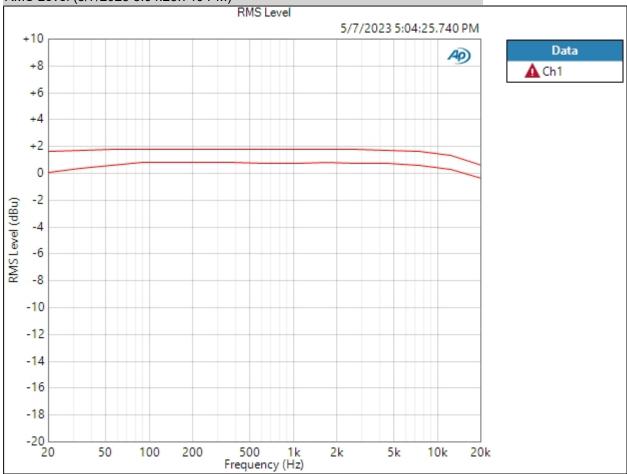
Number of Points: 10

Weighting Filter: Signal Path

High-pass Filter: 20 Hz Phase Ref Channel: Ch1

Measured 1 5/7/2023 5:04:25 PM

RMS Level (5/7/2023 5:04:25.740 PM)

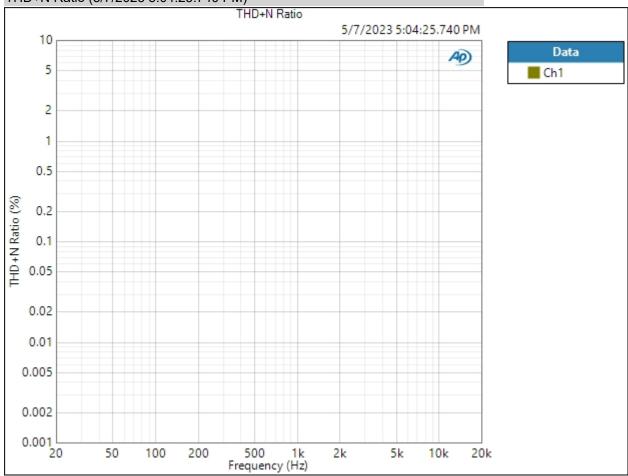


5/7/2023 5:07 PM Page 5 of 45



Result: A FAILED

THD+N Ratio (5/7/2023 5:04:25.740 PM)



Result: V PASSED

THD Ratio (5/7/2023 5:04:25.740 PM)

5/7/2023 5:07 PM Page 6 of 45



Result: 🛕 FAILED

5/7/2023 5:07 PM Page 7 of 45



Mic 2k 200k termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1
Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms 10.00 mVrms dBSPL2: dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

DCX is not detected.

W(watts) (Input Power):

5/7/2023 5:07 PM Page 8 of 45



Mic 2k 200k termination : Verify Connections

Waveform: Sine

Generator Level: -42.300 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/7/2023 5:04:31.360 PM)

Ch1 71.12 mVrms

Gain (5/7/2023 5:04:31.360 PM)

Ch1 21.554 dB

THD+N Ratio (5/7/2023 5:04:31.360 PM)

Ch1 ---- %

Frequency (5/7/2023 5:04:31.360 PM)

Ch1 ---- Hz

5/7/2023 5:07 PM Page 9 of 45



Mic 2k 200k termination : Stepped Frequency Sweep MIC 2K

Generator Level: -42.300 dBu
DC Offset: 0.000 V
EQ: None

Start Frequency: 20.0000 kHz
Stop Frequency: 20.0000 Hz
Step Type: Logarithmic

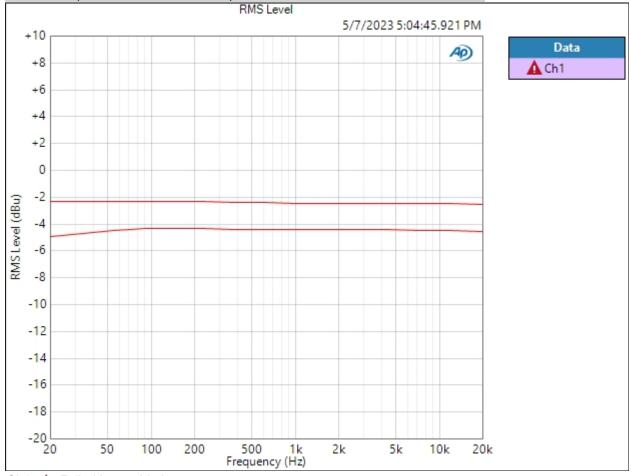
Number of Points: 10

Weighting Filter: Signal Path High-pass Filter: 20 Hz

Phase Ref Channel: Ch1

Measured 1 5/7/2023 5:04:45 PM

RMS Level (5/7/2023 5:04:45.921 PM)

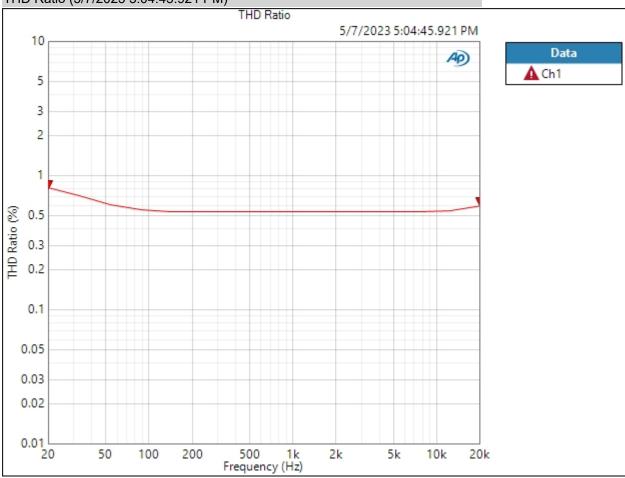


5/7/2023 5:07 PM Page 10 of 45



Result: A FAILED

THD Ratio (5/7/2023 5:04:45.921 PM)



Result: A FAILED

5/7/2023 5:07 PM Page 11 of 45



Mic 2k 15dB PAD 200k termination : Signal Path Setup
Output Connector:

Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1
Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms 10.00 mVrms dBSPL2: dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm W(watts) (Input Power): 8.000 ohm

• DCX

DCX is not detected.

5/7/2023 5:07 PM Page 12 of 45



Mic 2k 15dB PAD 200k termination : Verify Connections

Waveform: Sine

Generator Level: -42.300 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/7/2023 5:04:51.437 PM)

Ch1 71.07 mVrms

Gain (5/7/2023 5:04:51.437 PM)

Ch1 21.551 dB

THD+N Ratio (5/7/2023 5:04:51.437 PM)

Ch1 ---- %

Frequency (5/7/2023 5:04:51.437 PM)

Ch1 ---- Hz

5/7/2023 5:07 PM Page 13 of 45



Mic 2k 15dB PAD 200k termination : Stepped Frequency Sweep 15dB PAD

Generator Level: -42.000 dBu
DC Offset: 0.000 V
EQ: None

Start Frequency: 20.0000 kHz
Stop Frequency: 20.0000 Hz
Step Type: Logarithmic

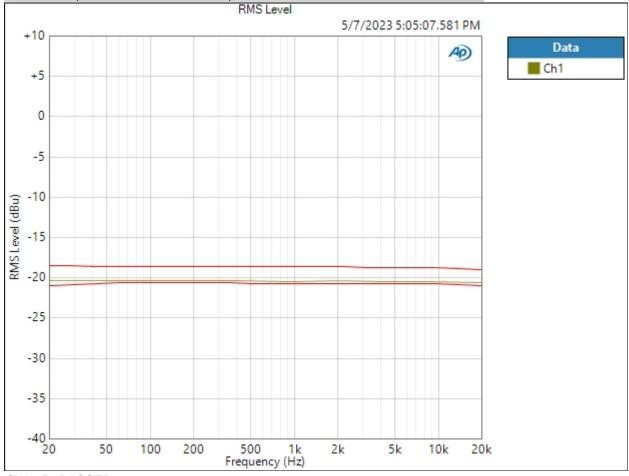
Number of Points: 10

Weighting Filter: Signal Path High-pass Filter: 20 Hz

Phase Ref Channel: Ch1

Measured 1 5/7/2023 5:05:07 PM

RMS Level (5/7/2023 5:05:07.581 PM)



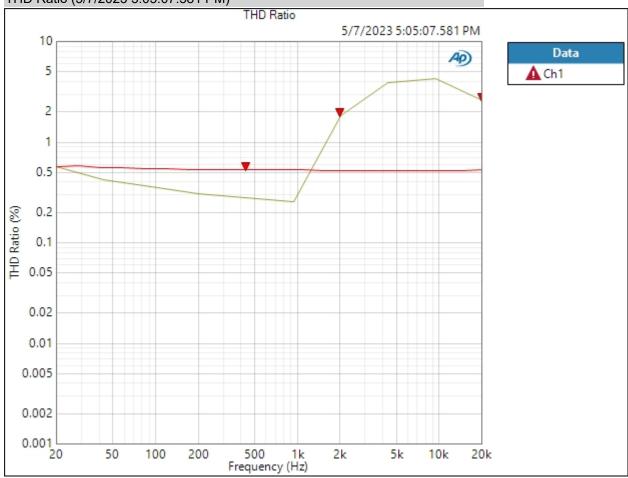
Ch1 🔮 PASSED

5/7/2023 5:07 PM Page 14 of 45



Result: V PASSED

THD Ratio (5/7/2023 5:05:07.581 PM)



Ch1 A Failed Upper Limit

Result: A FAILED

5/7/2023 5:07 PM Page 15 of 45



Line Gain -10 200kTermination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms 10.00 mVrms dBSPL2: dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

DCX is not detected.

W(watts) (Input Power):

5/7/2023 5:07 PM Page 16 of 45



Line Gain -10 200kTermination : Verify Connections

Waveform: Sine

Generator Level: 0.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/7/2023 5:05:13.713 PM)

Ch1 8.402 Vrms

Gain (5/7/2023 5:05:13.713 PM)

Ch1 20.731 dB

THD+N Ratio (5/7/2023 5:05:13.713 PM)

Ch1 5.650031 %

Frequency (5/7/2023 5:05:13.713 PM)

Ch1 1.00000 kHz

5/7/2023 5:07 PM Page 17 of 45



Line Gain -10 200kTermination: Stepped Frequency Sweep -10

Generator Level: 0.000 dBu
DC Offset: 0.000 V
EQ: None

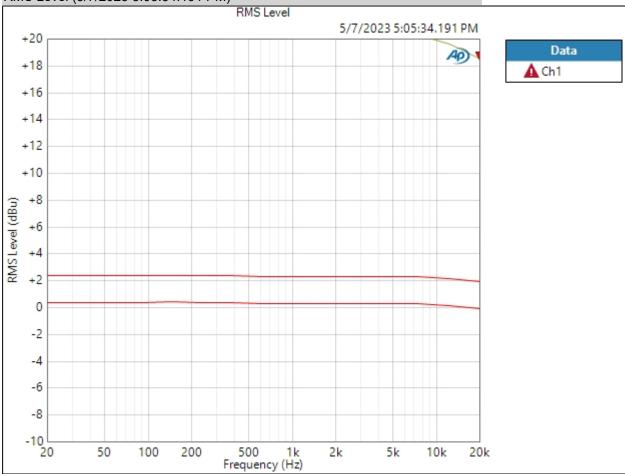
Start Frequency: 20.0000 kHz
Stop Frequency: 20.0000 Hz
Step Type: Logarithmic

Number of Points: 15

Weighting Filter: Signal Path
High-pass Filter: 20 Hz
Phase Ref Channel: Ch1

Measured 1 5/7/2023 5:05:34 PM

RMS Level (5/7/2023 5:05:34.191 PM)



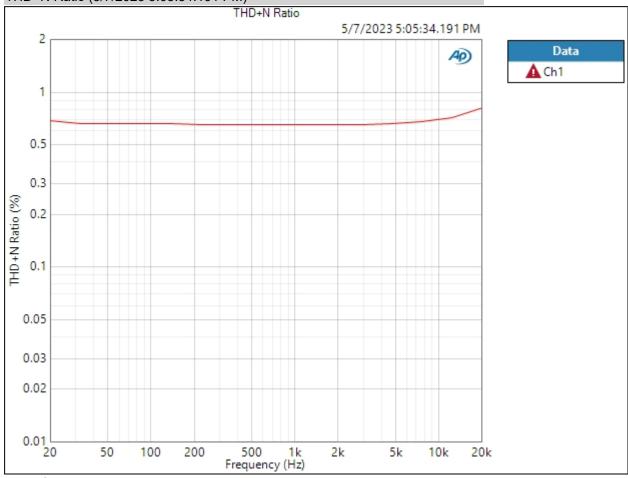
Ch1 A Failed Upper Limit

5/7/2023 5:07 PM Page 18 of 45



Result: A FAILED

THD+N Ratio (5/7/2023 5:05:34.191 PM)



Ch1 A Failed Upper Limit

Result: A FAILED

5/7/2023 5:07 PM Page 19 of 45



Line Gain -10 600 Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 600 ohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms 10.00 mVrms dBSPL2: dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

DCX is not detected.

W(watts) (Input Power):

5/7/2023 5:07 PM Page 20 of 45



Line Gain -10 600 Termination: Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/7/2023 5:05:40.126 PM)

Ch1 2.464 Vrms

Gain (5/7/2023 5:05:40.126 PM)

Ch1 20.053 dB

THD+N Ratio (5/7/2023 5:05:40.126 PM)

Ch1 0.667448 %

Frequency (5/7/2023 5:05:40.126 PM)

Ch1 1.00000 kHz

Line Gain -10 600 Termination: Level and Gain -10

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/7/2023 5:05:45.301 PM)

Channel Lower Limit Value Upper Limit
Ch1 -11.500 dBu +10.053 dBu -8.500 dBu ▲

Result: A FAILED

5/7/2023 5:07 PM Page 21 of 45



Line Gain +5 200kTermination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms 10.00 mVrms dBSPL2: dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

DCX is not detected.

W(watts) (Input Power):

5/7/2023 5:07 PM Page 22 of 45



Line Gain +5 200kTermination : Verify Connections

Waveform: Sine

Generator Level: 0.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/7/2023 5:05:51.126 PM)

Ch1 5.010 Vrms

Gain (5/7/2023 5:05:51.126 PM)

Ch1 16.217 dB

THD+N Ratio (5/7/2023 5:05:51.126 PM)

Ch1 0.263096 %

Frequency (5/7/2023 5:05:51.126 PM)

Ch1 1.00000 kHz

5/7/2023 5:07 PM Page 23 of 45



Line Gain +5 200kTermination : Stepped Frequency Sweep +5

Generator Level: 0.000 dBu
DC Offset: 0.000 V
EQ: None

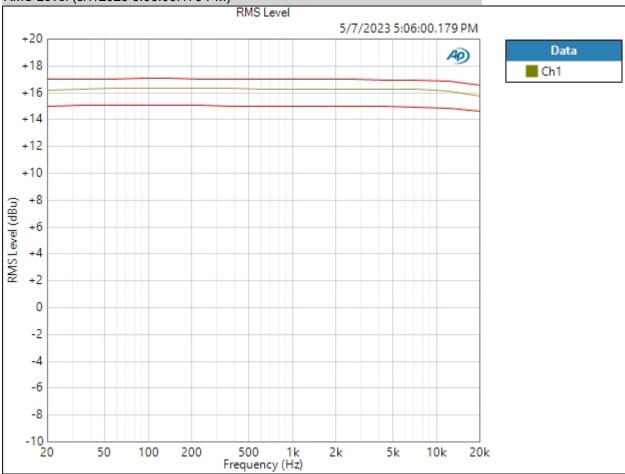
Start Frequency: 20.0000 kHz
Stop Frequency: 20.0000 Hz
Step Type: Logarithmic

Number of Points: 15

Weighting Filter: Signal Path
High-pass Filter: 20 Hz
Phase Ref Channel: Ch1

Measured 1 5/7/2023 5:06:00 PM

RMS Level (5/7/2023 5:06:00.179 PM)



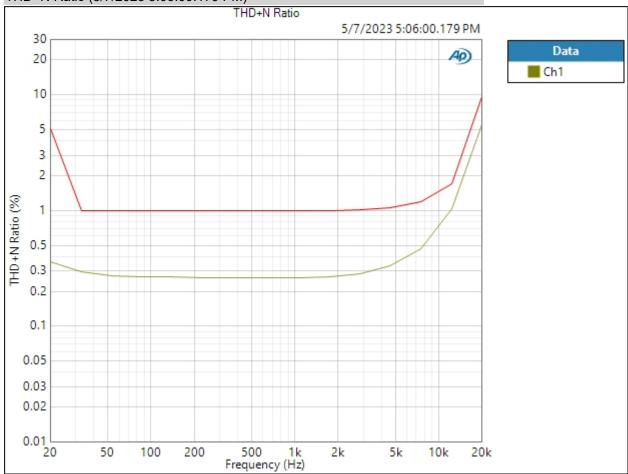
Ch1 🔮 PASSED

5/7/2023 5:07 PM Page 24 of 45



Result: V PASSED

THD+N Ratio (5/7/2023 5:06:00.179 PM)



Ch1 🔮 PASSED

Result: V PASSED

5/7/2023 5:07 PM Page 25 of 45



Line Gain +5 600 Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 600 ohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms 10.00 mVrms dBSPL2: dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

DCX is not detected.

W(watts) (Input Power):

5/7/2023 5:07 PM Page 26 of 45



Line Gain +5 600 Termination : Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/7/2023 5:06:06.298 PM)

Ch1 1.395 Vrms

Gain (5/7/2023 5:06:06.298 PM)

Ch1 15.112 dB

THD+N Ratio (5/7/2023 5:06:06.298 PM)

Ch1 0.276856 %

Frequency (5/7/2023 5:06:06.298 PM)

Ch1 1.00000 kHz

Line Gain +5 600 Termination: Level and Gain +5

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/7/2023 5:06:09.474 PM)

Result: V PASSED

5/7/2023 5:07 PM Page 27 of 45



Line Gain -5 600 Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 600 ohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms 10.00 mVrms dBSPL2: dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL

• DCX

DCX is not detected.

dBm (Input Power):

W(watts) (Input Power):

5/7/2023 5:07 PM Page 28 of 45

600.0 ohm



Line Gain -5 600 Termination: Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu DC Offset: 0.000 V 1.00000 kHz

Frequency:

RMS Level (5/7/2023 5:06:15.372 PM)

Ch1 432.2 mVrms

Gain (5/7/2023 5:06:15.372 PM)

Ch1 4.933 dB

THD+N Ratio (5/7/2023 5:06:15.372 PM)

Ch1 0.183953 %

Frequency (5/7/2023 5:06:15.372 PM)

Ch1 1.00000 kHz

Line Gain -5 600 Termination: Level and Gain -5

Waveform: Sine

Generator Level: -10.000 dBu DC Offset: 0.000 V Frequency: 1.00000 kHz

RMS Level (5/7/2023 5:06:18.689 PM)

Upper Limit Channel Lower Limit Value -3.500 dBu Ch1 -6.500 dBu -5.067 dBu **3**

Result: V PASSED

Page 29 of 45 5/7/2023 5:07 PM



Line Gain 0 600 Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 600 ohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms 10.00 mVrms dBSPL2: dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

DCX is not detected.

W(watts) (Input Power):

5/7/2023 5:07 PM Page 30 of 45



Line Gain 0 600 Termination: Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/7/2023 5:06:24.669 PM)

Ch1 754.1 mVrms

Gain (5/7/2023 5:06:24.669 PM)

Ch1 9.767 dB

THD+N Ratio (5/7/2023 5:06:24.669 PM)

Ch1 0.165846 %

Frequency (5/7/2023 5:06:24.669 PM)

Ch1 1.00000 kHz

Line Gain 0 600 Termination: Level and Gain 0

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/7/2023 5:06:27.951 PM)

Channel Lower Limit Value Upper Limit
Ch1 -1.500 dBu -0.233 dBu +1.500 dBu ❖

Result: V PASSED

5/7/2023 5:07 PM Page 31 of 45



Line Gain +10 600 Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 600 ohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms 10.00 mVrms dBSPL2: dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

DCX is not detected.

W(watts) (Input Power):

5/7/2023 5:07 PM Page 32 of 45



Line Gain +10 600 Termination: Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/7/2023 5:06:33.863 PM)

Ch1 2.465 Vrms

Gain (5/7/2023 5:06:33.863 PM)

Ch1 20.055 dB

THD+N Ratio (5/7/2023 5:06:33.863 PM)

Ch1 0.663292 %

Frequency (5/7/2023 5:06:33.863 PM)

Ch1 1.00000 kHz

Line Gain +10 600 Termination: Level and Gain +10

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/7/2023 5:06:37.189 PM)

Result: V PASSED

5/7/2023 5:07 PM Page 33 of 45



Line Gain +10 200k Termination Level Hi : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms 10.00 mVrms dBSPL2: dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

DCX is not detected.

W(watts) (Input Power):

5/7/2023 5:07 PM Page 34 of 45



Line Gain +10 200k Termination Level Hi: Verify Connections

Waveform: Sine

Generator Level: -20.000 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/7/2023 5:06:43.248 PM)

Ch1 888.0 mVrms

Gain (5/7/2023 5:06:43.248 PM)

Ch1 21.187 dB

THD+N Ratio (5/7/2023 5:06:43.248 PM)

Ch1 2.277261 %

Frequency (5/7/2023 5:06:43.248 PM)

Ch1 1.00000 kHz

5/7/2023 5:07 PM Page 35 of 45



Line Gain +10 200k Termination Level Hi : Noise Recorder (RMS) CW

Waveform: None
Low-pass Filter: 20 kHz
Weighting Filter: Signal Path

High-pass Filter: 20 Hz

Sweep Time: 0.00:00:03.000

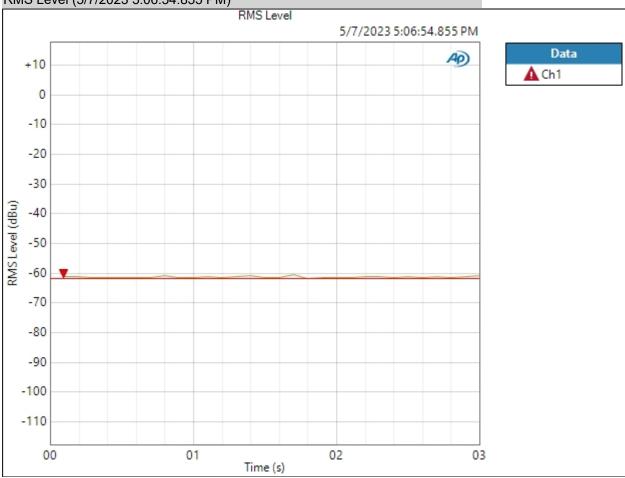
Reading Rate: 10/sec

Input Bandwidth: Use Signal Path

Record Acquisition: False

Measured 1 5/7/2023 5:06:54 PM

RMS Level (5/7/2023 5:06:54.855 PM)



Result: A FAILED

5/7/2023 5:07 PM Page 36 of 45



Line Gain +10 200k Termination Level Low: Signal Path Setup

Output Connector: Analog Balanced

Channels:

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm W(watts) (Input Power): 8.000 ohm

• DCX

DCX is not detected.

5/7/2023 5:07 PM Page 37 of 45



Line Gain +10 200k Termination Level Low: Verify Connections

Waveform: Sine

Generator Level: -20.000 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/7/2023 5:07:01.039 PM)

Ch1 888.0 mVrms

Gain (5/7/2023 5:07:01.039 PM)

Ch1 21.187 dB

THD+N Ratio (5/7/2023 5:07:01.039 PM)

Ch1 2.269391 %

Frequency (5/7/2023 5:07:01.039 PM)

Ch1 1.00000 kHz

5/7/2023 5:07 PM Page 38 of 45



Line Gain +10 200k Termination Level Low: Noise Recorder (RMS) CCW

Waveform: None
Low-pass Filter: 20 kHz
Weighting Filter: Signal Path

High-pass Filter: 20 Hz

Sweep Time: 0.00:00:03.000

Reading Rate: 10/sec

Input Bandwidth: Use Signal Path

Record Acquisition: False

Measured 1 5/7/2023 5:07:14 PM

RMS Level (5/7/2023 5:07:14.377 PM)



Result: A FAILED

5/7/2023 5:07 PM Page 39 of 45



Hi Z Gain -10 2.2M 200k Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 2

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm W(watts) (Input Power): 8.000 ohm

• DCX

DCX is not detected.

5/7/2023 5:07 PM Page 40 of 45



Hi Z Gain -10 2.2M 200k Termination: Verify Connections

Waveform: Sine

Generator Level: -22.300 dBu
DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/7/2023 5:07:20.085 PM)

Ch1 20.05 mVrms

Gain (5/7/2023 5:07:20.085 PM)

Ch1 -9.450 dB

THD+N Ratio (5/7/2023 5:07:20.085 PM)

Ch1 ---- %

Frequency (5/7/2023 5:07:20.085 PM)

Ch1 ---- Hz

Hi Z Gain -10 2.2M 200k Termination: Level and Gain 2.2M

Waveform: Sine

Generator Level: -22.300 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/7/2023 5:07:23.842 PM)

Channel Lower Limit Value Upper Limit
Ch1 -2.000 dBu -1.112 dBu +2.000 dBu ❖

Result: V PASSED

5/7/2023 5:07 PM Page 41 of 45



Hi Z Gain -10 47k 200k Termination : Signal Path Setup
Output Connector:
Analog Unbalanced

Channels: 2

Source Impedance: 50 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms 10.00 mVrms dBSPL2: dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL

• DCX

DCX is not detected.

dBm (Input Power):

W(watts) (Input Power):

5/7/2023 5:07 PM Page 42 of 45

600.0 ohm



Hi Z Gain -10 47k 200k Termination : Verify Connections

Waveform: Sine

Generator Level: -22.300 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/7/2023 5:07:29.920 PM)

Ch1 20.12 mVrms

Gain (5/7/2023 5:07:29.920 PM)

Ch1 -9.369 dB

THD+N Ratio (5/7/2023 5:07:29.920 PM)

Ch1 ---- %

Frequency (5/7/2023 5:07:29.920 PM)

Ch1 ---- Hz

Hi Z Gain -10 47k 200k Termination: Level and Gain 47K

Waveform: Sine

Generator Level: -22.300 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/7/2023 5:07:35.288 PM)

Channel Lower Limit Value Upper Limit
Ch1 -8.000 dBu -31.754 dBu -4.000 dBu ▲

Result: A FAILED

5/7/2023 5:07 PM Page 43 of 45



Dummy Signal Path For Report : Signal Path Setup

Output Connector: Analog Unbalanced

Channels: 2

Source Impedance: 50 ohm
Output EQ: None

Input Connector: Analog Unbalanced

Channels: 2

Termination: 100 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms 94.000 dBSPL dBSPL1 Calibrator Level: dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

DCX is not detected.

W(watts) (Input Power):

5/7/2023 5:07 PM Page 44 of 45



Dummy Signal Path For Report : Verify Connections

Waveform: Sine

Generator Level: 100.0 mVrms

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/7/2023 5:07:39.515 PM)

Ch1 14.32 uVrms Ch2 7.346 uVrms

5/7/2023 5:07 PM Page 45 of 45