

## Sequence Report



### Summary

#### Mic500 200k Termination

Signal Path Setup PASSED

Stepped Frequency Sweep MIC 500 FAILED

#### Mic 2k 200k termination

Signal Path Setup PASSED

Stepped Frequency Sweep MIC 2K FAILED

#### Mic 2k 15dB PAD 200k termination

Signal Path Setup PASSED

Stepped Frequency Sweep 15dB PAD FAILED

#### Line Gain -10 200kTermination

Signal Path Setup PASSED

Stepped Frequency Sweep -10 FAILED

#### Line Gain -10 600 Termination

Signal Path Setup PASSED

Level and Gain -10 PASSED

#### Line Gain +5 200kTermination

Signal Path Setup PASSED

Stepped Frequency Sweep +5 FAILED

#### Line Gain +5 600 Termination

Signal Path Setup PASSED

Level and Gain +5 FAILED

#### Line Gain -5 600 Termination

Signal Path Setup PASSED

Level and Gain -5 FAILED

#### Line Gain 0 600 Termination

Signal Path Setup PASSED

Level and Gain 0 FAILED

#### Line Gain +10 600 Termination

Signal Path Setup PASSED

Level and Gain +10 FAILED

#### Line Gain +10 200k Termination Level Hi

Signal Path Setup PASSED

Noise Recorder (RMS) CW FAILED

#### Line Gain +10 200k Termination Level Low

Signal Path Setup PASSED

Noise Recorder (RMS) CCW FAILED

#### Hi Z Gain -10 2.2M 200k Termination

|                                    |          |
|------------------------------------|----------|
| Signal Path Setup                  | ✓ PASSED |
| Level and Gain 2.2M                | ⚠ FAILED |
| Hi Z Gain -10 47k 200k Termination |          |
| Signal Path Setup                  | ✓ PASSED |
| Level and Gain 47K                 | ⚠ FAILED |
| DummyPathForReport                 |          |
| Signal Path Setup                  | ✓ PASSED |
| Sequence Result:                   |          |
| Sequence Result:                   | ⚠ FAILED |

## Sequence Report



### Mic500 200k Termination : Signal Path Setup

|                   |                                |
|-------------------|--------------------------------|
| Output Connector: | Analog Balanced                |
| Channels:         | 1                              |
| Source Impedance: | 100 ohm                        |
| Output EQ:        | None                           |
| Input Connector:  | Analog Balanced                |
| Channels:         | 1                              |
| Channel:          | Ch1                            |
| Termination:      | 200 kohm                       |
| Input Bandwidth:  | AC (<10 Hz) - 90k (192 kHz SR) |
| Device Delay:     | 0.000 s                        |
| Input EQ:         | None                           |

#### • References

|                             |               |
|-----------------------------|---------------|
| dBr G:                      | 100.0 mVrms   |
| dBm (Output Power):         | 600.0 ohm     |
| W(watts) (Output Power):    | 8.000 ohm     |
| Shared Frequency Reference: | 1.00000 kHz   |
| dBrA:                       | 1.000 Vrms    |
| dBrB:                       | 1.000 Vrms    |
| dBrA Offset:                | 0.000 dB      |
| dBrB Offset:                | 0.000 dB      |
| dB SPL1:                    | 10.00 mVrms   |
| dB SPL2:                    | 10.00 mVrms   |
| dB SPL1 Calibrator Level:   | 94.000 dB SPL |
| dB SPL2 Calibrator Level:   | 94.000 dB SPL |
| dBm (Input Power):          | 600.0 ohm     |
| W(watts) (Input Power):     | 8.000 ohm     |

#### • DCX

|               |         |
|---------------|---------|
| DC Output 1:  | 0.000 V |
| DC Output 1:  | Off     |
| DC Output 2:  | 0.000 V |
| DC Output 2:  | Off     |
| Port A (hex): | 00      |
| Port B (hex): | 00      |
| Port C (hex): | 00      |

## Sequence Report



Port D (hex): 00

### Mic500 200k Termination : Verify Connections

Waveform: Sine

Generator Level: -42.300 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (4/29/2023 10:45:15.609 PM)

Ch1 282.1 mVrms

Gain (4/29/2023 10:45:15.609 PM)

Ch1 33.526 dB

THD+N Ratio (4/29/2023 10:45:15.609 PM)

Ch1 ---- %

Frequency (4/29/2023 10:45:15.609 PM)

Ch1 ---- Hz

## Sequence Report



Mic500 200k Termination : Stepped Frequency Sweep MIC 500

Generator Level: -42.300 dBu

DC Offset: 0.000 V

EQ: None

Start Frequency: 20.0000 kHz

Stop Frequency: 20.0000 Hz

Step Type: Logarithmic

Number of Points: 10

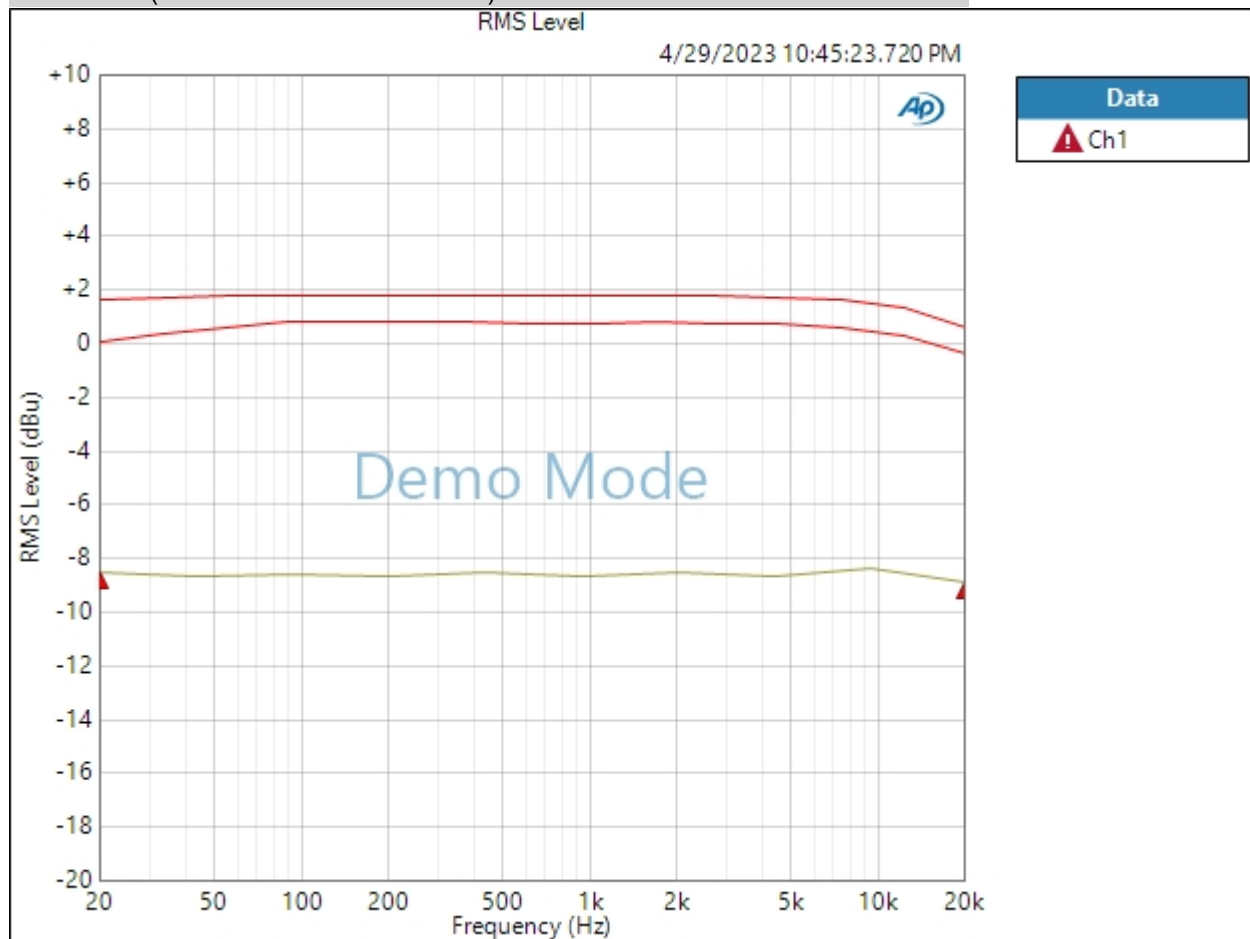
Weighting Filter: Signal Path

High-pass Filter: 20 Hz

Phase Ref Channel: Ch1

Measured 1 4/29/2023 10:45:23 PM

RMS Level (4/29/2023 10:45:23.720 PM)



Ch1 Failed Lower Limit

4/29/2023 10:48 PM

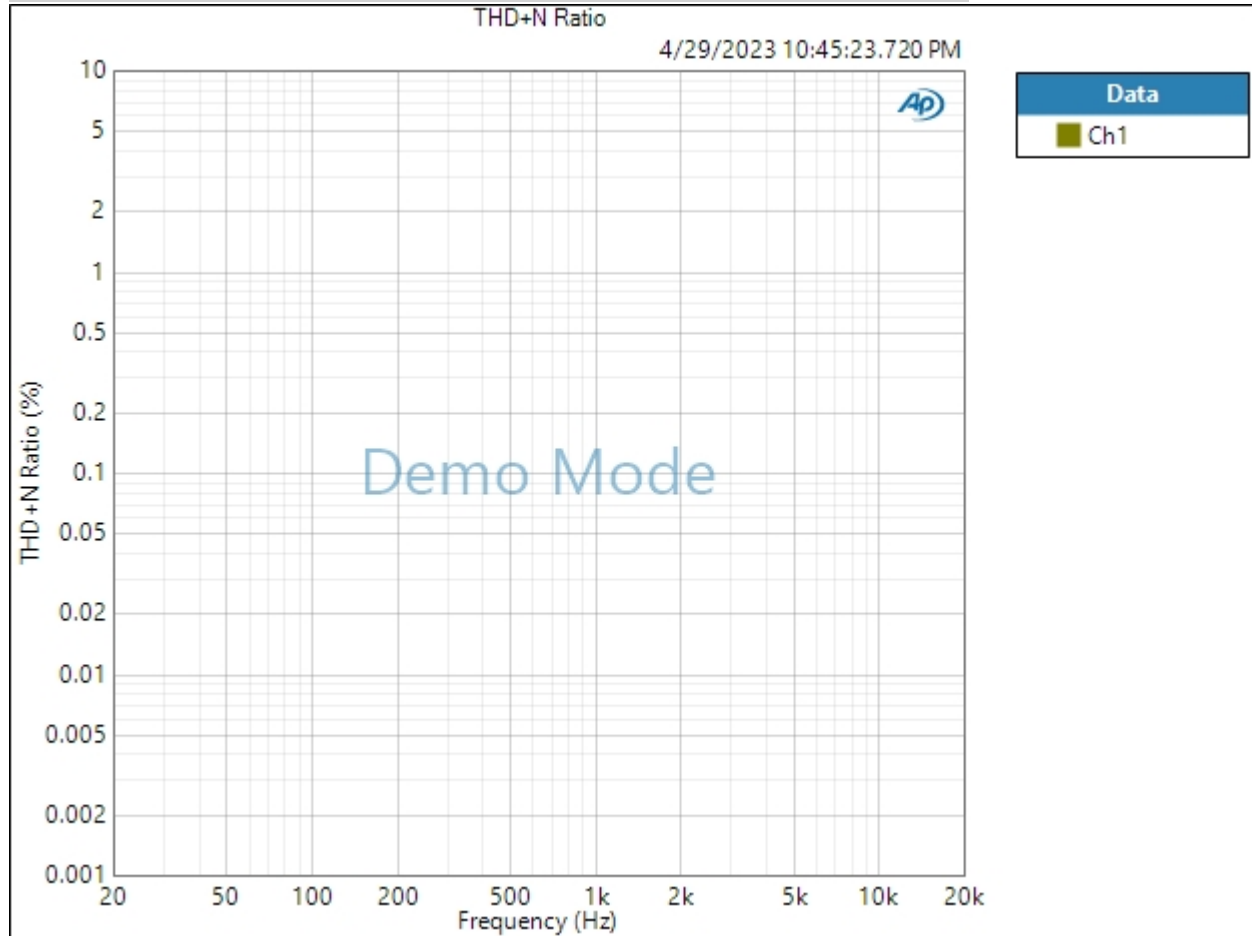
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## Sequence Report



Result: ▲ FAILED

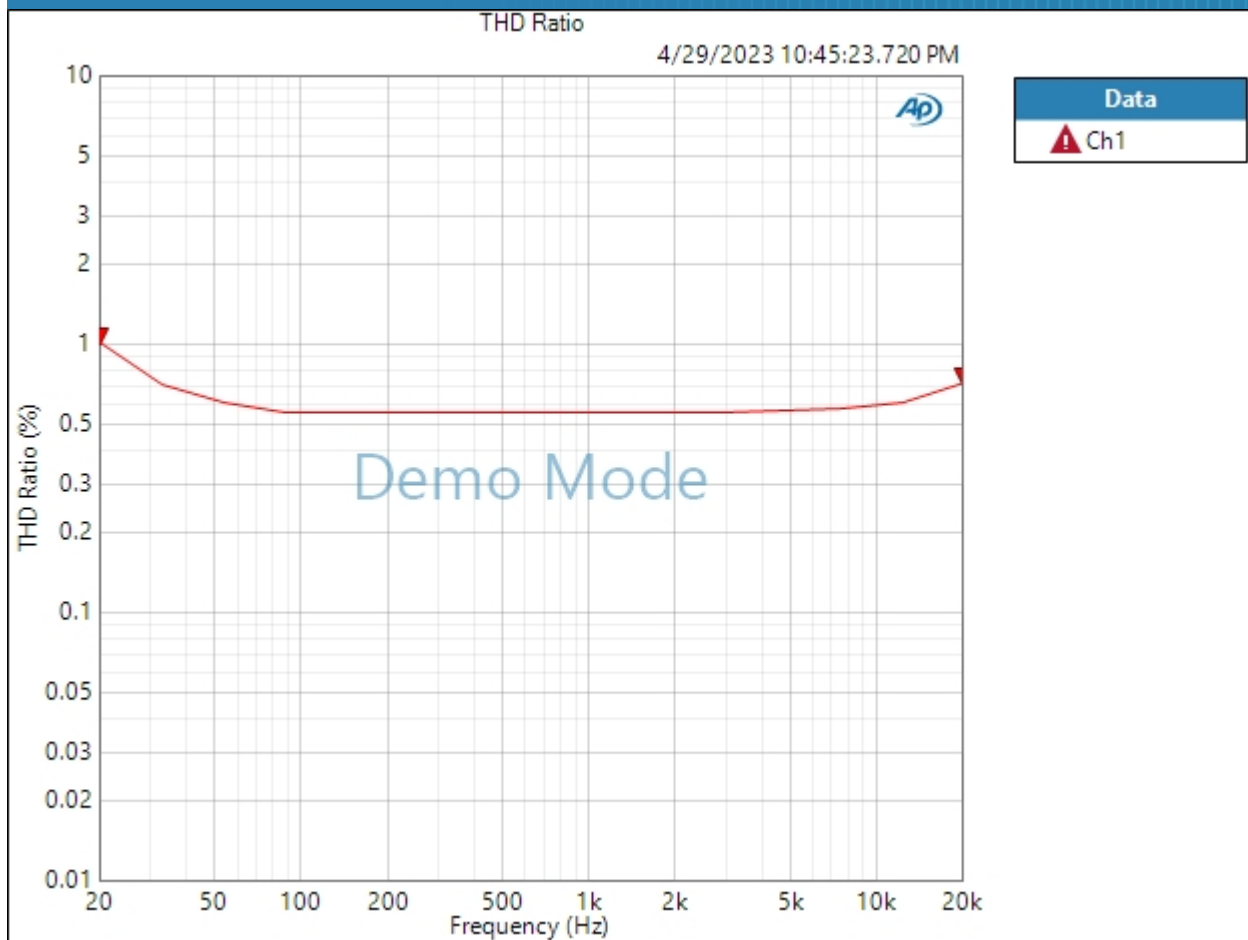
THD+N Ratio (4/29/2023 10:45:23.720 PM)



Result: ✔ PASSED

THD Ratio (4/29/2023 10:45:23.720 PM)

## Sequence Report



Ch1 Failed Upper Limit

Result: FAILED

## Sequence Report



### Mic 2k 200k termination : Signal Path Setup

|                   |                                |
|-------------------|--------------------------------|
| Output Connector: | Analog Balanced                |
| Channels:         | 1                              |
| Source Impedance: | 100 ohm                        |
| Output EQ:        | None                           |
| Input Connector:  | Analog Balanced                |
| Channels:         | 1                              |
| Channel:          | Ch1                            |
| Termination:      | 200 kohm                       |
| Input Bandwidth:  | AC (<10 Hz) - 90k (192 kHz SR) |
| Device Delay:     | 0.000 s                        |
| Input EQ:         | None                           |

#### • References

|                             |               |
|-----------------------------|---------------|
| dBr G:                      | 100.0 mVrms   |
| dBm (Output Power):         | 600.0 ohm     |
| W(watts) (Output Power):    | 8.000 ohm     |
| Shared Frequency Reference: | 1.00000 kHz   |
| dBrA:                       | 1.000 Vrms    |
| dBrB:                       | 1.000 Vrms    |
| dBrA Offset:                | 0.000 dB      |
| dBrB Offset:                | 0.000 dB      |
| dB SPL1:                    | 10.00 mVrms   |
| dB SPL2:                    | 10.00 mVrms   |
| dB SPL1 Calibrator Level:   | 94.000 dB SPL |
| dB SPL2 Calibrator Level:   | 94.000 dB SPL |
| dBm (Input Power):          | 600.0 ohm     |
| W(watts) (Input Power):     | 8.000 ohm     |

#### • DCX

|               |         |
|---------------|---------|
| DC Output 1:  | 0.000 V |
| DC Output 1:  | Off     |
| DC Output 2:  | 0.000 V |
| DC Output 2:  | Off     |
| Port A (hex): | 00      |
| Port B (hex): | 00      |
| Port C (hex): | 00      |



## Sequence Report



Port D (hex): 00

### Mic 2k 200k termination : Verify Connections

Waveform: Sine

Generator Level: -42.300 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (4/29/2023 10:45:28.898 PM)

Ch1 288.8 mVrms

Gain (4/29/2023 10:45:28.898 PM)

Ch1 33.729 dB

THD+N Ratio (4/29/2023 10:45:28.898 PM)

Ch1 ---- %

Frequency (4/29/2023 10:45:28.898 PM)

Ch1 ---- Hz

## Sequence Report



Mic 2k 200k termination : Stepped Frequency Sweep MIC 2K

Generator Level: -42.300 dBu

DC Offset: 0.000 V

EQ: None

Start Frequency: 20.0000 kHz

Stop Frequency: 20.0000 Hz

Step Type: Logarithmic

Number of Points: 10

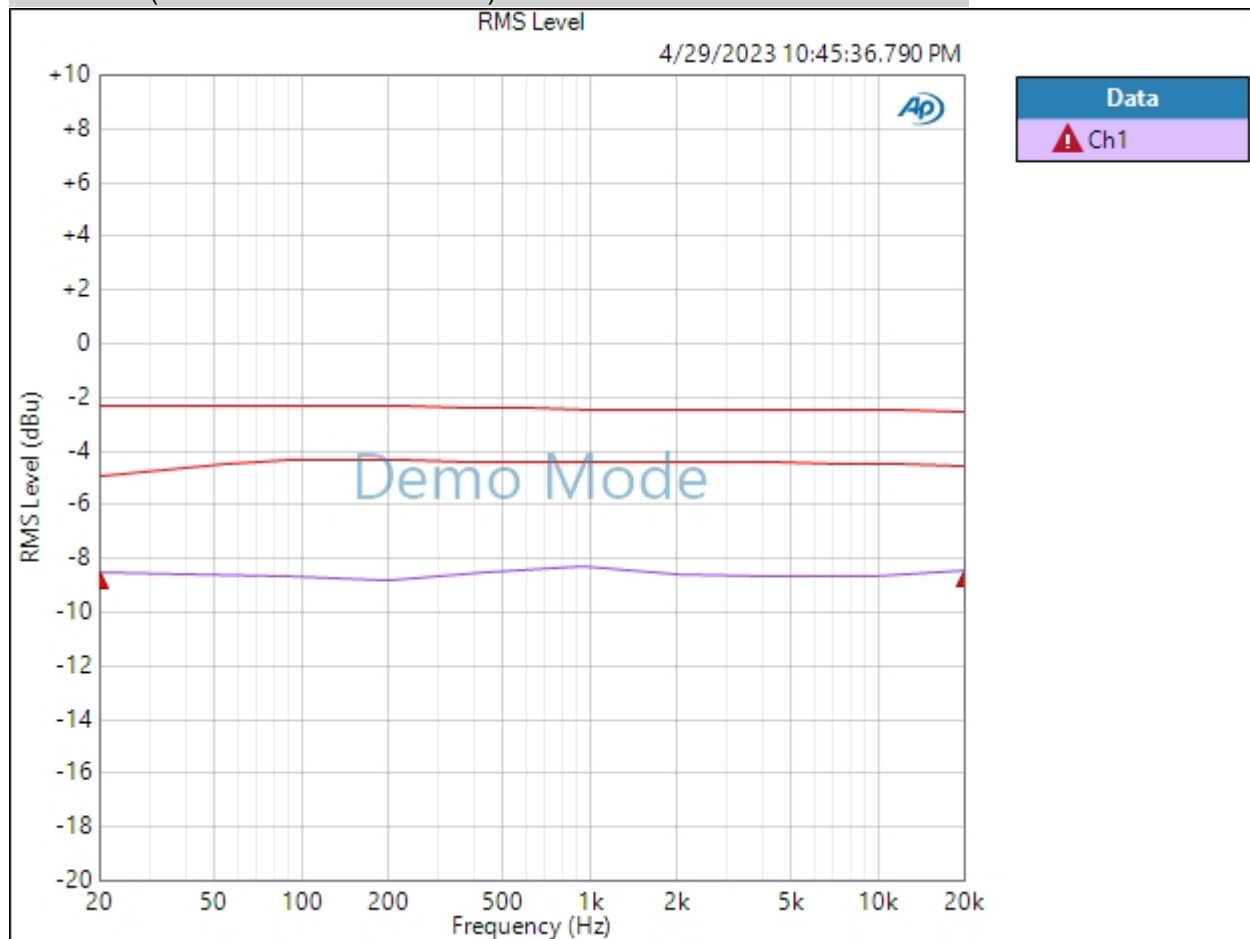
Weighting Filter: Signal Path

High-pass Filter: 20 Hz

Phase Ref Channel: Ch1

Measured 1 4/29/2023 10:45:36 PM

RMS Level (4/29/2023 10:45:36.790 PM)

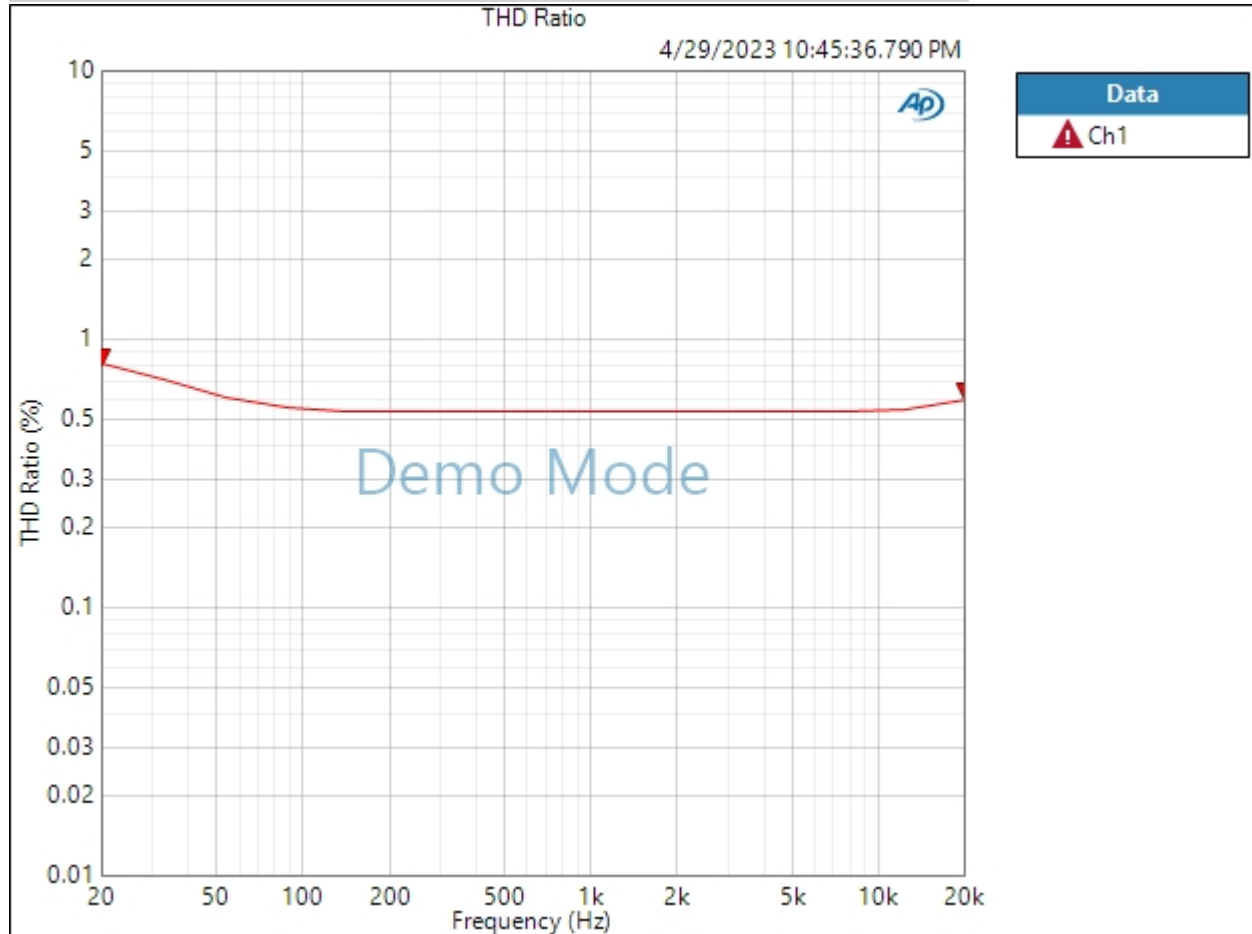


## Sequence Report



Result: ▲ FAILED

THD Ratio (4/29/2023 10:45:36.790 PM)



Ch1 ▲ Failed Upper Limit

Result: ▲ FAILED

## Sequence Report



Mic 2k 15dB PAD 200k termination : Signal Path Setup

Output Connector: Analog Balanced  
Channels: 1  
Source Impedance: 100 ohm  
Output EQ: None  
Input Connector: Analog Balanced  
Channels: 1  
Channel: Ch1  
Termination: 200 kohm  
Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)  
Device Delay: 0.000 s  
Input EQ: None

### • References

dBr G: 100.0 mVrms  
dBm (Output Power): 600.0 ohm  
W(watts) (Output Power): 8.000 ohm  
Shared Frequency Reference: 1.00000 kHz  
dBrA: 1.000 Vrms  
dBrB: 1.000 Vrms  
dBrA Offset: 0.000 dB  
dBrB Offset: 0.000 dB  
dBSPL1: 10.00 mVrms  
dBSPL2: 10.00 mVrms  
dBSPL1 Calibrator Level: 94.000 dB SPL  
dBSPL2 Calibrator Level: 94.000 dB SPL  
dBm (Input Power): 600.0 ohm  
W(watts) (Input Power): 8.000 ohm

### • DCX

DC Output 1: 0.000 V  
DC Output 1: Off  
DC Output 2: 0.000 V  
DC Output 2: Off  
Port A (hex): 00  
Port B (hex): 00  
Port C (hex): 00

4/29/2023 10:48 PM

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## Sequence Report



Port D (hex): 00

Mic 2k 15dB PAD 200k termination : Verify Connections

Waveform: Sine

Generator Level: -42.300 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (4/29/2023 10:45:42.375 PM)

Ch1 273.1 mVrms

Gain (4/29/2023 10:45:42.375 PM)

Ch1 33.245 dB

THD+N Ratio (4/29/2023 10:45:42.375 PM)

Ch1 ---- %

Frequency (4/29/2023 10:45:42.375 PM)

Ch1 ---- Hz

## Sequence Report



Mic 2k 15dB PAD 200k termination : Stepped Frequency Sweep 15dB PAD

Generator Level: -42.000 dBu

DC Offset: 0.000 V

EQ: None

Start Frequency: 20.0000 kHz

Stop Frequency: 20.0000 Hz

Step Type: Logarithmic

Number of Points: 10

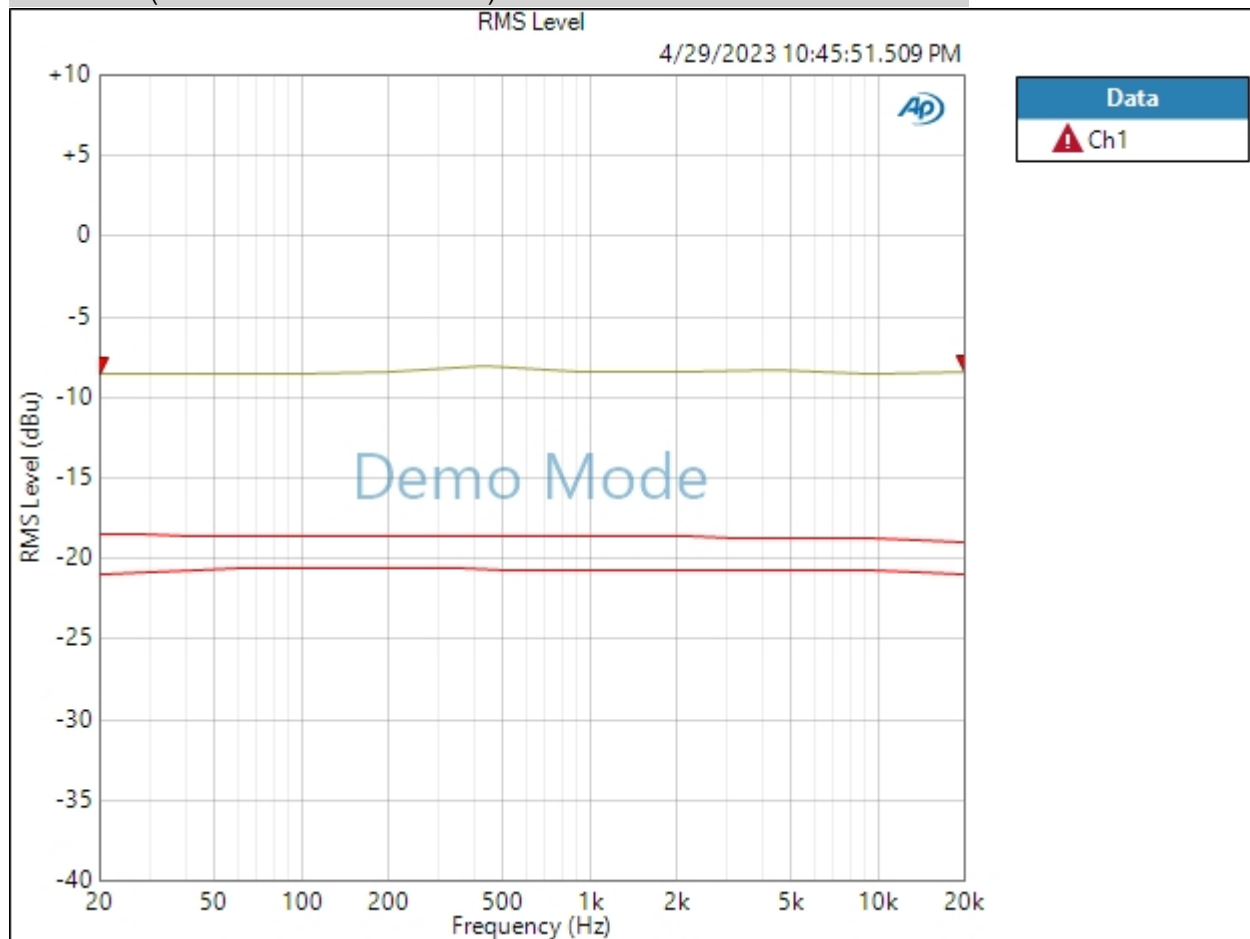
Weighting Filter: Signal Path

High-pass Filter: 20 Hz

Phase Ref Channel: Ch1

Measured 1 4/29/2023 10:45:51 PM

RMS Level (4/29/2023 10:45:51.509 PM)



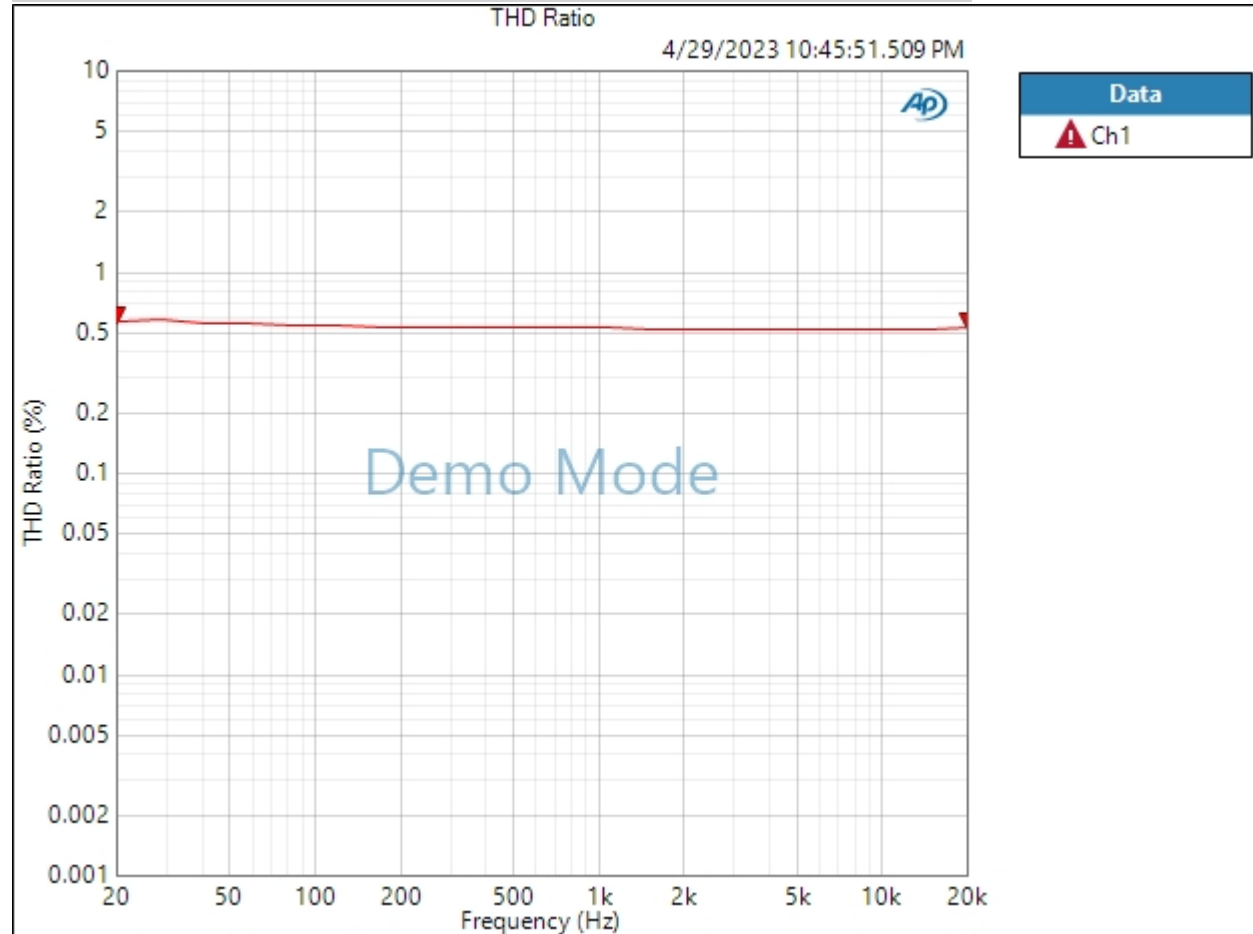
Ch1 Failed Upper Limit

## Sequence Report



Result: ▲ FAILED

THD Ratio (4/29/2023 10:45:51.509 PM)



Ch1 ▲ Failed Upper Limit

Result: ▲ FAILED

## Sequence Report



### Line Gain -10 200kTermination : Signal Path Setup

|                   |                                |
|-------------------|--------------------------------|
| Output Connector: | Analog Balanced                |
| Channels:         | 1                              |
| Source Impedance: | 100 ohm                        |
| Output EQ:        | None                           |
| Input Connector:  | Analog Balanced                |
| Channels:         | 1                              |
| Channel:          | Ch1                            |
| Termination:      | 200 kohm                       |
| Input Bandwidth:  | AC (<10 Hz) - 90k (192 kHz SR) |
| Device Delay:     | 0.000 s                        |
| Input EQ:         | None                           |

#### • References

|                             |               |
|-----------------------------|---------------|
| dBr G:                      | 100.0 mVrms   |
| dBm (Output Power):         | 600.0 ohm     |
| W(watts) (Output Power):    | 8.000 ohm     |
| Shared Frequency Reference: | 1.00000 kHz   |
| dBrA:                       | 1.000 Vrms    |
| dBrB:                       | 1.000 Vrms    |
| dBrA Offset:                | 0.000 dB      |
| dBrB Offset:                | 0.000 dB      |
| dB SPL1:                    | 10.00 mVrms   |
| dB SPL2:                    | 10.00 mVrms   |
| dB SPL1 Calibrator Level:   | 94.000 dB SPL |
| dB SPL2 Calibrator Level:   | 94.000 dB SPL |
| dBm (Input Power):          | 600.0 ohm     |
| W(watts) (Input Power):     | 8.000 ohm     |

#### • DCX

|               |         |
|---------------|---------|
| DC Output 1:  | 0.000 V |
| DC Output 1:  | Off     |
| DC Output 2:  | 0.000 V |
| DC Output 2:  | Off     |
| Port A (hex): | 00      |
| Port B (hex): | 00      |
| Port C (hex): | 00      |



## Sequence Report



Port D (hex): 00

### Line Gain -10 200kTermination : Verify Connections

Waveform: Sine

Generator Level: 0.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (4/29/2023 10:45:56.755 PM)

Ch1 289.3 mVrms

Gain (4/29/2023 10:45:56.755 PM)

Ch1 -8.554 dB

THD+N Ratio (4/29/2023 10:45:56.755 PM)

Ch1 ---- %

Frequency (4/29/2023 10:45:56.755 PM)

Ch1 ---- Hz

## Sequence Report



Line Gain -10 200kTermination : Stepped Frequency Sweep -10

Generator Level: 0.000 dBu

DC Offset: 0.000 V

EQ: None

Start Frequency: 20.0000 kHz

Stop Frequency: 20.0000 Hz

Step Type: Logarithmic

Number of Points: 15

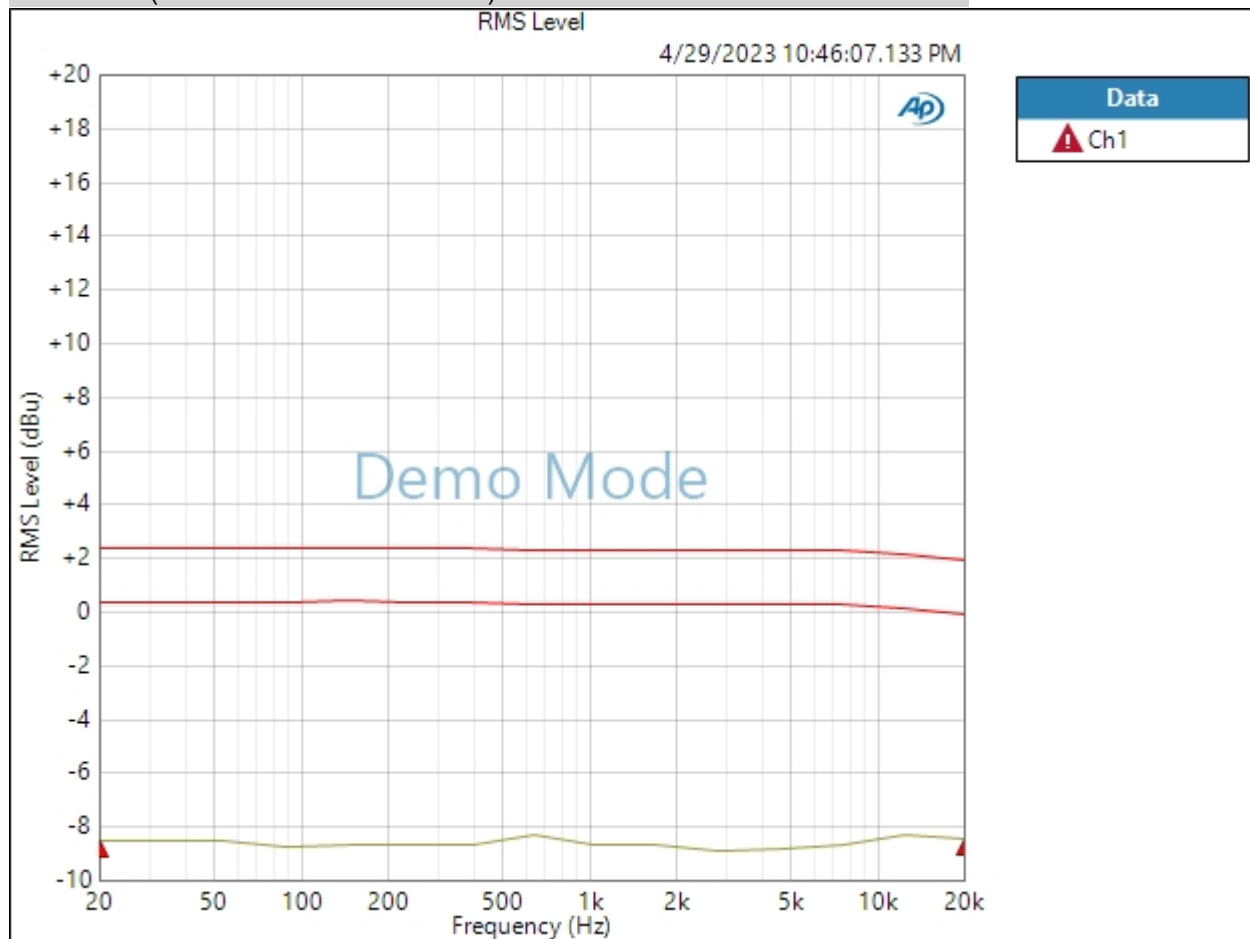
Weighting Filter: Signal Path

High-pass Filter: 20 Hz

Phase Ref Channel: Ch1

Measured 1 4/29/2023 10:46:07 PM

RMS Level (4/29/2023 10:46:07.133 PM)



Ch1 Failed Lower Limit

4/29/2023 10:48 PM

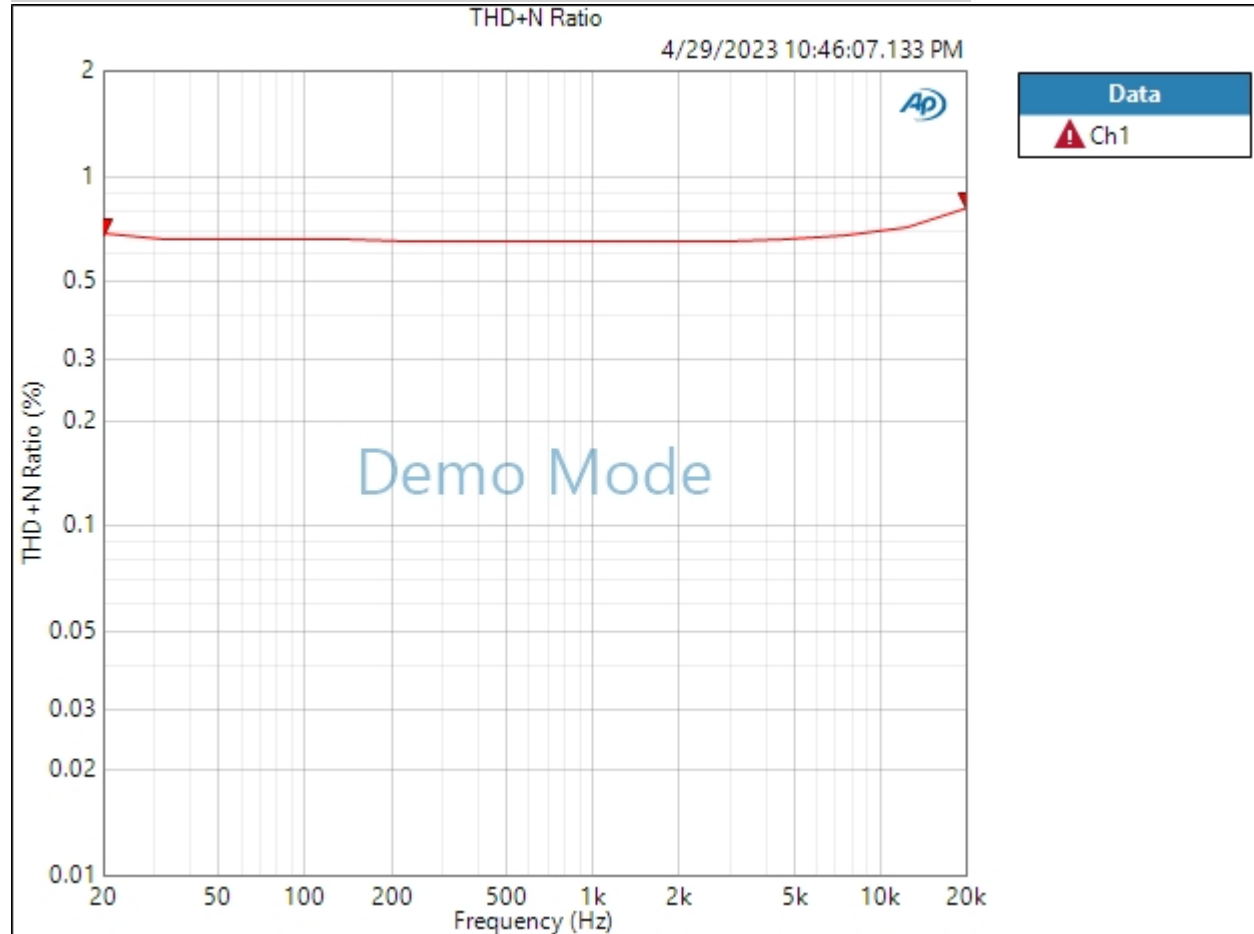
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## Sequence Report



Result: ▲ FAILED

THD+N Ratio (4/29/2023 10:46:07.133 PM)



Ch1 ▲ Failed Upper Limit

Result: ▲ FAILED

## Sequence Report



### Line Gain -10 600 Termination : Signal Path Setup

|                   |                                |
|-------------------|--------------------------------|
| Output Connector: | Analog Balanced                |
| Channels:         | 1                              |
| Source Impedance: | 100 ohm                        |
| Output EQ:        | None                           |
| Input Connector:  | Analog Balanced                |
| Channels:         | 1                              |
| Channel:          | Ch1                            |
| Termination:      | 600 ohm                        |
| Input Bandwidth:  | AC (<10 Hz) - 90k (192 kHz SR) |
| Device Delay:     | 0.000 s                        |
| Input EQ:         | None                           |

#### • References

|                             |               |
|-----------------------------|---------------|
| dBr G:                      | 100.0 mVrms   |
| dBm (Output Power):         | 600.0 ohm     |
| W(watts) (Output Power):    | 8.000 ohm     |
| Shared Frequency Reference: | 1.00000 kHz   |
| dBrA:                       | 1.000 Vrms    |
| dBrB:                       | 1.000 Vrms    |
| dBrA Offset:                | 0.000 dB      |
| dBrB Offset:                | 0.000 dB      |
| dB SPL1:                    | 10.00 mVrms   |
| dB SPL2:                    | 10.00 mVrms   |
| dB SPL1 Calibrator Level:   | 94.000 dB SPL |
| dB SPL2 Calibrator Level:   | 94.000 dB SPL |
| dBm (Input Power):          | 600.0 ohm     |
| W(watts) (Input Power):     | 8.000 ohm     |

#### • DCX

|               |         |
|---------------|---------|
| DC Output 1:  | 0.000 V |
| DC Output 1:  | Off     |
| DC Output 2:  | 0.000 V |
| DC Output 2:  | Off     |
| Port A (hex): | 00      |
| Port B (hex): | 00      |
| Port C (hex): | 00      |

## Sequence Report



Port D (hex): 00

### Line Gain -10 600 Termination : Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

#### RMS Level (4/29/2023 10:46:12.534 PM)

Ch1 291.8 mVrms

#### Gain (4/29/2023 10:46:12.534 PM)

Ch1 1.519 dB

#### THD+N Ratio (4/29/2023 10:46:12.534 PM)

Ch1 ---- %

#### Frequency (4/29/2023 10:46:12.534 PM)

Ch1 ---- Hz

### Line Gain -10 600 Termination : Level and Gain -10

Waveform: Sine

Generator Level: -10.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

#### RMS Level (4/29/2023 10:46:16.474 PM)

| Channel | Lower Limit | Value      | Upper Limit |   |
|---------|-------------|------------|-------------|---|
| Ch1     | -11.500 dBu | -8.697 dBu | -8.500 dBu  | ✓ |

Result: ✓ PASSED

## Sequence Report



### Line Gain +5 200kTermination : Signal Path Setup

Output Connector: Analog Balanced  
Channels: 1  
Source Impedance: 100 ohm  
Output EQ: None  
Input Connector: Analog Balanced  
Channels: 1  
Channel: Ch1  
Termination: 200 kohm  
Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)  
Device Delay: 0.000 s  
Input EQ: None

#### • References

dBr G: 100.0 mVrms  
dBm (Output Power): 600.0 ohm  
W(watts) (Output Power): 8.000 ohm  
Shared Frequency Reference: 1.00000 kHz  
dBrA: 1.000 Vrms  
dBrB: 1.000 Vrms  
dBrA Offset: 0.000 dB  
dBrB Offset: 0.000 dB  
dBSPL1: 10.00 mVrms  
dBSPL2: 10.00 mVrms  
dBSPL1 Calibrator Level: 94.000 dB SPL  
dBSPL2 Calibrator Level: 94.000 dB SPL  
dBm (Input Power): 600.0 ohm  
W(watts) (Input Power): 8.000 ohm

#### • DCX

DC Output 1: 0.000 V  
DC Output 1: Off  
DC Output 2: 0.000 V  
DC Output 2: Off  
Port A (hex): 00  
Port B (hex): 00  
Port C (hex): 00

## Sequence Report



Port D (hex): 00

Line Gain +5 200kTermination : Verify Connections

Waveform: Sine

Generator Level: 0.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (4/29/2023 10:46:21.911 PM)

Ch1 296.3 mVrms

Gain (4/29/2023 10:46:21.911 PM)

Ch1 -8.348 dB

THD+N Ratio (4/29/2023 10:46:21.911 PM)

Ch1 ---- %

Frequency (4/29/2023 10:46:21.911 PM)

Ch1 ---- Hz

## Sequence Report



Line Gain +5 200kTermination : Stepped Frequency Sweep +5

Generator Level: 0.000 dBu

DC Offset: 0.000 V

EQ: None

Start Frequency: 20.0000 kHz

Stop Frequency: 20.0000 Hz

Step Type: Logarithmic

Number of Points: 15

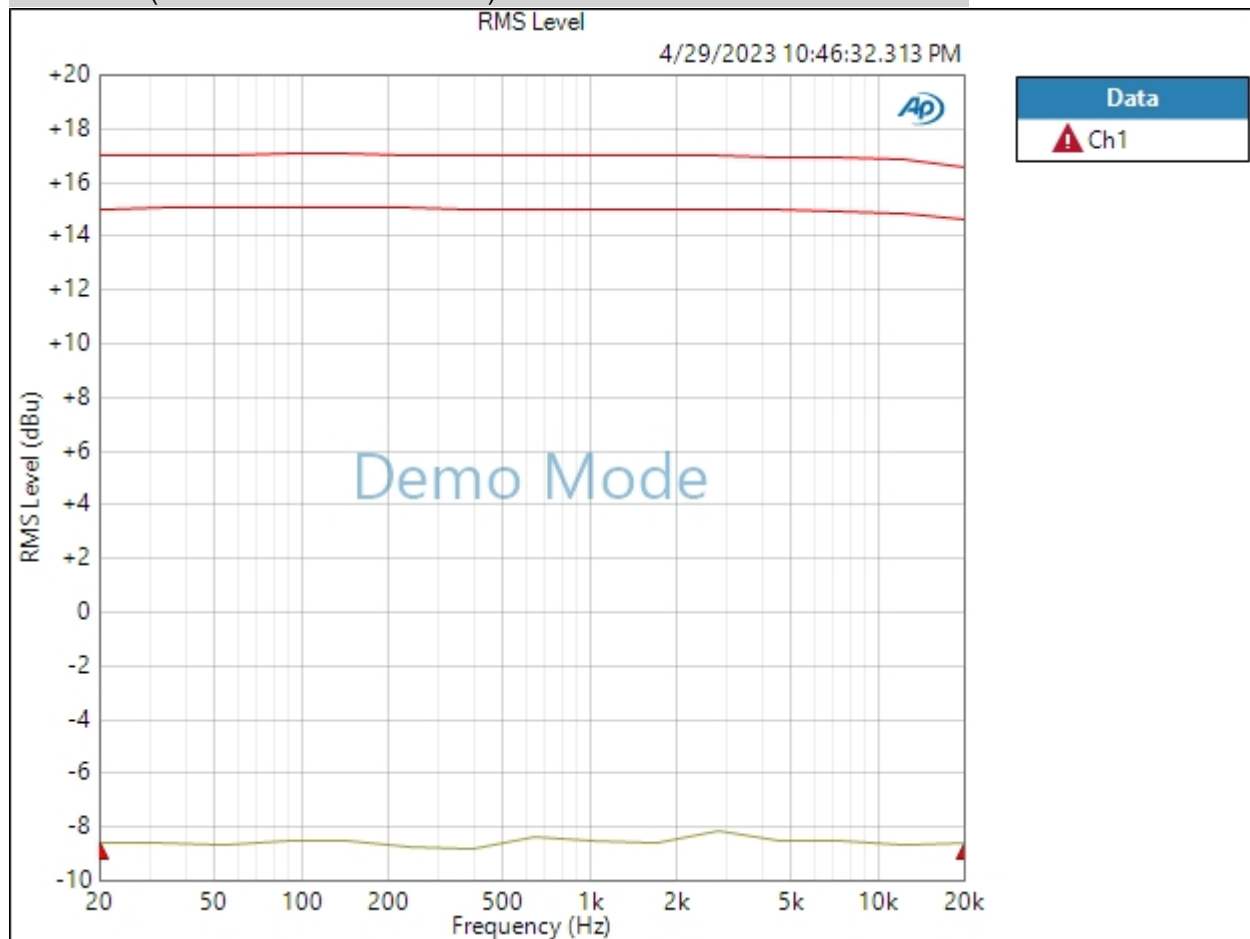
Weighting Filter: Signal Path

High-pass Filter: 20 Hz

Phase Ref Channel: Ch1

Measured 1 4/29/2023 10:46:32 PM

RMS Level (4/29/2023 10:46:32.313 PM)



Ch1 Failed Lower Limit

4/29/2023 10:48 PM

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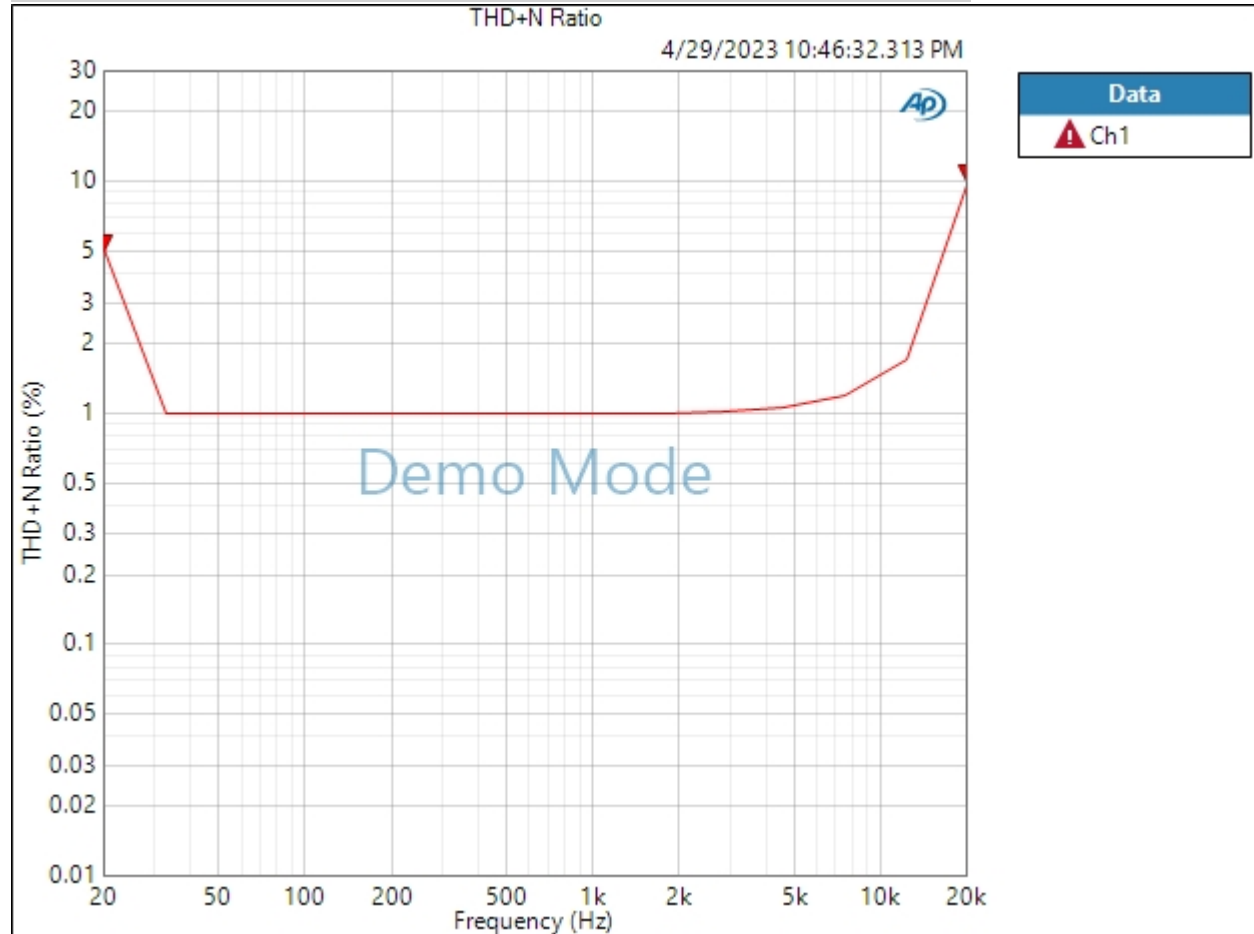


## Sequence Report



Result: ▲ FAILED

THD+N Ratio (4/29/2023 10:46:32.313 PM)



Ch1 ▲ Failed Upper Limit

Result: ▲ FAILED

## Sequence Report



### Line Gain +5 600 Termination : Signal Path Setup

|                   |                                |
|-------------------|--------------------------------|
| Output Connector: | Analog Balanced                |
| Channels:         | 1                              |
| Source Impedance: | 100 ohm                        |
| Output EQ:        | None                           |
| Input Connector:  | Analog Balanced                |
| Channels:         | 1                              |
| Channel:          | Ch1                            |
| Termination:      | 600 ohm                        |
| Input Bandwidth:  | AC (<10 Hz) - 90k (192 kHz SR) |
| Device Delay:     | 0.000 s                        |
| Input EQ:         | None                           |

#### • References

|                             |               |
|-----------------------------|---------------|
| dBr G:                      | 100.0 mVrms   |
| dBm (Output Power):         | 600.0 ohm     |
| W(watts) (Output Power):    | 8.000 ohm     |
| Shared Frequency Reference: | 1.00000 kHz   |
| dBrA:                       | 1.000 Vrms    |
| dBrB:                       | 1.000 Vrms    |
| dBrA Offset:                | 0.000 dB      |
| dBrB Offset:                | 0.000 dB      |
| dB SPL1:                    | 10.00 mVrms   |
| dB SPL2:                    | 10.00 mVrms   |
| dB SPL1 Calibrator Level:   | 94.000 dB SPL |
| dB SPL2 Calibrator Level:   | 94.000 dB SPL |
| dBm (Input Power):          | 600.0 ohm     |
| W(watts) (Input Power):     | 8.000 ohm     |

#### • DCX

|               |         |
|---------------|---------|
| DC Output 1:  | 0.000 V |
| DC Output 1:  | Off     |
| DC Output 2:  | 0.000 V |
| DC Output 2:  | Off     |
| Port A (hex): | 00      |
| Port B (hex): | 00      |
| Port C (hex): | 00      |

## Sequence Report



Port D (hex): 00

### Line Gain +5 600 Termination : Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (4/29/2023 10:46:38.025 PM)

Ch1 290.2 mVrms

Gain (4/29/2023 10:46:38.025 PM)

Ch1 1.471 dB

THD+N Ratio (4/29/2023 10:46:38.025 PM)

Ch1 ---- %

Frequency (4/29/2023 10:46:38.025 PM)

Ch1 ---- Hz

### Line Gain +5 600 Termination : Level and Gain +5

Waveform: Sine

Generator Level: -10.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (4/29/2023 10:46:41.912 PM)

| Channel | Lower Limit | Value      | Upper Limit |   |
|---------|-------------|------------|-------------|---|
| Ch1     | +3.500 dBu  | -8.484 dBu | +6.500 dBu  |  |

Result:  FAILED

## Sequence Report



### Line Gain -5 600 Termination : Signal Path Setup

Output Connector: Analog Balanced  
Channels: 1  
Source Impedance: 100 ohm  
Output EQ: None  
Input Connector: Analog Balanced  
Channels: 1  
Channel: Ch1  
Termination: 600 ohm  
Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)  
Device Delay: 0.000 s  
Input EQ: None

#### • References

dBr G: 100.0 mVrms  
dBm (Output Power): 600.0 ohm  
W(watts) (Output Power): 8.000 ohm  
Shared Frequency Reference: 1.00000 kHz  
dBrA: 1.000 Vrms  
dBrB: 1.000 Vrms  
dBrA Offset: 0.000 dB  
dBrB Offset: 0.000 dB  
dBSPL1: 10.00 mVrms  
dBSPL2: 10.00 mVrms  
dBSPL1 Calibrator Level: 94.000 dB SPL  
dBSPL2 Calibrator Level: 94.000 dB SPL  
dBm (Input Power): 600.0 ohm  
W(watts) (Input Power): 8.000 ohm

#### • DCX

DC Output 1: 0.000 V  
DC Output 1: Off  
DC Output 2: 0.000 V  
DC Output 2: Off  
Port A (hex): 00  
Port B (hex): 00  
Port C (hex): 00

## Sequence Report



Port D (hex): 00

### Line Gain -5 600 Termination : Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (4/29/2023 10:46:47.326 PM)

Ch1 285.1 mVrms

Gain (4/29/2023 10:46:47.326 PM)

Ch1 1.319 dB

THD+N Ratio (4/29/2023 10:46:47.326 PM)

Ch1 ---- %

Frequency (4/29/2023 10:46:47.326 PM)

Ch1 ---- Hz

### Line Gain -5 600 Termination : Level and Gain -5

Waveform: Sine

Generator Level: -10.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (4/29/2023 10:46:51.245 PM)

| Channel | Lower Limit | Value      | Upper Limit |   |
|---------|-------------|------------|-------------|---|
| Ch1     | -6.500 dBu  | -8.557 dBu | -3.500 dBu  |  |

Result:  FAILED

## Sequence Report



### Line Gain 0 600 Termination : Signal Path Setup

Output Connector: Analog Balanced  
Channels: 1  
Source Impedance: 100 ohm  
Output EQ: None  
Input Connector: Analog Balanced  
Channels: 1  
Channel: Ch1  
Termination: 600 ohm  
Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)  
Device Delay: 0.000 s  
Input EQ: None

#### • References

dBr G: 100.0 mVrms  
dBm (Output Power): 600.0 ohm  
W(watts) (Output Power): 8.000 ohm  
Shared Frequency Reference: 1.00000 kHz  
dBrA: 1.000 Vrms  
dBrB: 1.000 Vrms  
dBrA Offset: 0.000 dB  
dBrB Offset: 0.000 dB  
dBSPL1: 10.00 mVrms  
dBSPL2: 10.00 mVrms  
dBSPL1 Calibrator Level: 94.000 dB SPL  
dBSPL2 Calibrator Level: 94.000 dB SPL  
dBm (Input Power): 600.0 ohm  
W(watts) (Input Power): 8.000 ohm

#### • DCX

DC Output 1: 0.000 V  
DC Output 1: Off  
DC Output 2: 0.000 V  
DC Output 2: Off  
Port A (hex): 00  
Port B (hex): 00  
Port C (hex): 00

## Sequence Report



Port D (hex): 00

### Line Gain 0 600 Termination : Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (4/29/2023 10:46:56.679 PM)

Ch1 289.9 mVrms

Gain (4/29/2023 10:46:56.679 PM)

Ch1 1.463 dB

THD+N Ratio (4/29/2023 10:46:56.679 PM)

Ch1 ---- %

Frequency (4/29/2023 10:46:56.679 PM)

Ch1 ---- Hz

### Line Gain 0 600 Termination : Level and Gain 0

Waveform: Sine

Generator Level: -10.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (4/29/2023 10:47:00.549 PM)

| Channel | Lower Limit | Value      | Upper Limit |   |
|---------|-------------|------------|-------------|---|
| Ch1     | -1.500 dBu  | -8.550 dBu | +1.500 dBu  |  |

Result:  FAILED

## Sequence Report



### Line Gain +10 600 Termination : Signal Path Setup

Output Connector: Analog Balanced  
Channels: 1  
Source Impedance: 100 ohm  
Output EQ: None  
Input Connector: Analog Balanced  
Channels: 1  
Channel: Ch1  
Termination: 600 ohm  
Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)  
Device Delay: 0.000 s  
Input EQ: None

#### • References

dBr G: 100.0 mVrms  
dBm (Output Power): 600.0 ohm  
W(watts) (Output Power): 8.000 ohm  
Shared Frequency Reference: 1.00000 kHz  
dBrA: 1.000 Vrms  
dBrB: 1.000 Vrms  
dBrA Offset: 0.000 dB  
dBrB Offset: 0.000 dB  
dBSPL1: 10.00 mVrms  
dBSPL2: 10.00 mVrms  
dBSPL1 Calibrator Level: 94.000 dB SPL  
dBSPL2 Calibrator Level: 94.000 dB SPL  
dBm (Input Power): 600.0 ohm  
W(watts) (Input Power): 8.000 ohm

#### • DCX

DC Output 1: 0.000 V  
DC Output 1: Off  
DC Output 2: 0.000 V  
DC Output 2: Off  
Port A (hex): 00  
Port B (hex): 00  
Port C (hex): 00



## Sequence Report



Port D (hex): 00

### Line Gain +10 600 Termination : Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (4/29/2023 10:47:05.967 PM)

Ch1 285.8 mVrms

Gain (4/29/2023 10:47:05.967 PM)

Ch1 1.339 dB

THD+N Ratio (4/29/2023 10:47:05.967 PM)

Ch1 ---- %

Frequency (4/29/2023 10:47:05.967 PM)

Ch1 ---- Hz

### Line Gain +10 600 Termination : Level and Gain +10

Waveform: Sine

Generator Level: -10.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (4/29/2023 10:47:10.110 PM)

| Channel | Lower Limit | Value      | Upper Limit |
|---------|-------------|------------|-------------|
| Ch1     | +8.500 dBu  | -8.646 dBu | +11.500 dBu |



Result: FAILED

## Sequence Report



Line Gain +10 200k Termination Level Hi : Signal Path Setup

Output Connector: Analog Balanced  
Channels: 1  
Source Impedance: 100 ohm  
Output EQ: None  
Input Connector: Analog Balanced  
Channels: 1  
Channel: Ch1  
Termination: 200 kohm  
Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)  
Device Delay: 0.000 s  
Input EQ: None

### • References

dBr G: 100.0 mVrms  
dBm (Output Power): 600.0 ohm  
W(watts) (Output Power): 8.000 ohm  
Shared Frequency Reference: 1.00000 kHz  
dBrA: 1.000 Vrms  
dBrB: 1.000 Vrms  
dBrA Offset: 0.000 dB  
dBrB Offset: 0.000 dB  
dBSPL1: 10.00 mVrms  
dBSPL2: 10.00 mVrms  
dBSPL1 Calibrator Level: 94.000 dB SPL  
dBSPL2 Calibrator Level: 94.000 dB SPL  
dBm (Input Power): 600.0 ohm  
W(watts) (Input Power): 8.000 ohm

### • DCX

DC Output 1: 0.000 V  
DC Output 1: Off  
DC Output 2: 0.000 V  
DC Output 2: Off  
Port A (hex): 00  
Port B (hex): 00  
Port C (hex): 00

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## Sequence Report



Port D (hex): 00

Line Gain +10 200k Termination Level Hi : Verify Connections

Waveform: Sine

Generator Level: -20.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (4/29/2023 10:47:15.834 PM)

Ch1 291.9 mVrms

Gain (4/29/2023 10:47:15.834 PM)

Ch1 11.523 dB

THD+N Ratio (4/29/2023 10:47:15.834 PM)

Ch1 ---- %

Frequency (4/29/2023 10:47:15.834 PM)

Ch1 ---- Hz

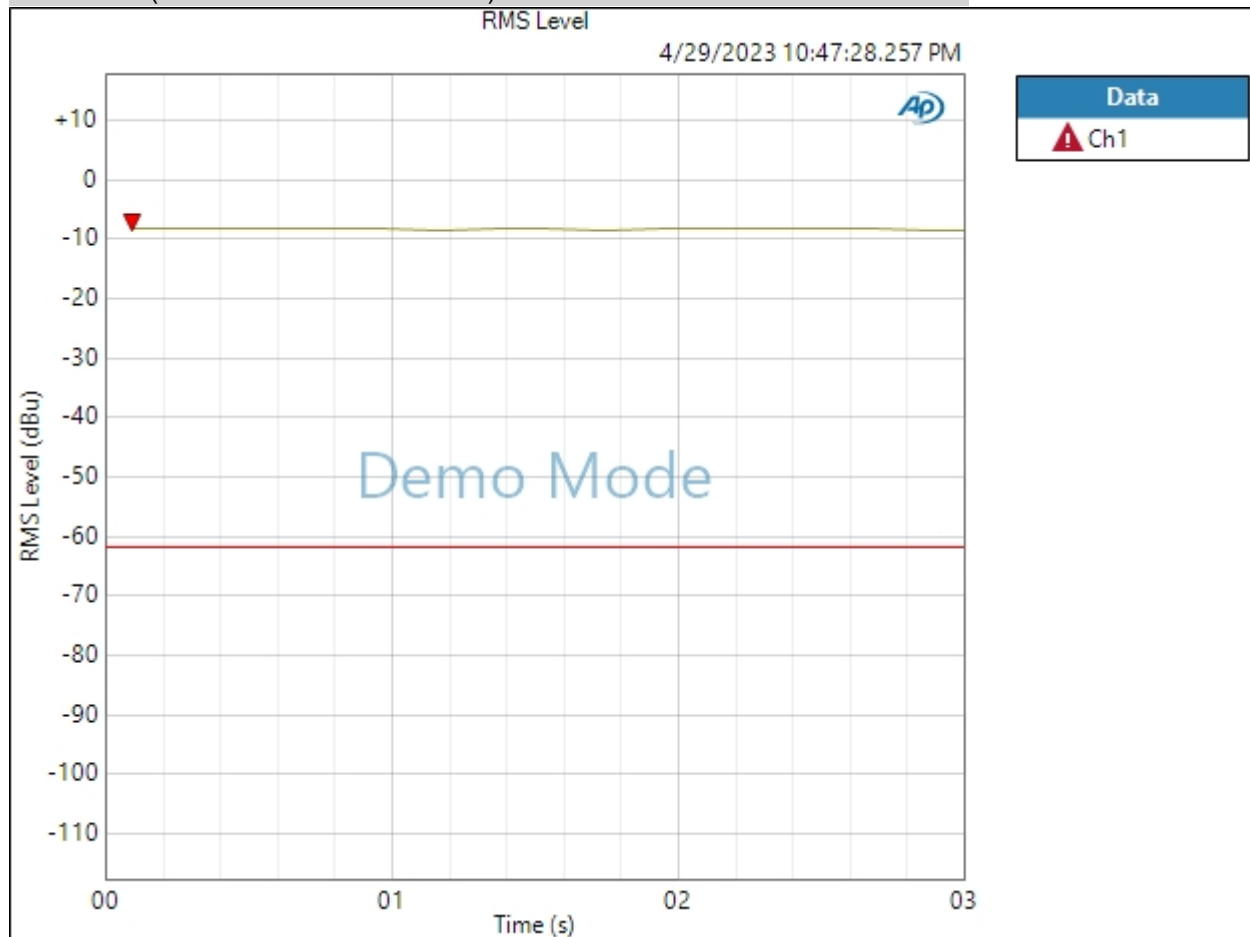
## Sequence Report



Line Gain +10 200k Termination Level Hi : Noise Recorder (RMS) CW

Waveform: None  
Low-pass Filter: 20 kHz  
Weighting Filter: Signal Path  
High-pass Filter: 20 Hz  
Sweep Time: 0.00:00:03.000  
Reading Rate: 10/sec  
Input Bandwidth: Use Signal Path  
Record Acquisition: False  
Measured 1 4/29/2023 10:47:28 PM

RMS Level (4/29/2023 10:47:28.257 PM)



Ch1 Failed Upper Limit

Result: FAILED

4/29/2023 10:48 PM



## Sequence Report



### Line Gain +10 200k Termination Level Low : Signal Path Setup

Output Connector: Analog Balanced  
Channels: 1  
Source Impedance: 100 ohm  
Output EQ: None  
Input Connector: Analog Balanced  
Channels: 1  
Channel: Ch1  
Termination: 200 kohm  
Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)  
Device Delay: 0.000 s  
Input EQ: None

#### • References

dBr G: 100.0 mVrms  
dBm (Output Power): 600.0 ohm  
W(watts) (Output Power): 8.000 ohm  
Shared Frequency Reference: 1.00000 kHz  
dBrA: 1.000 Vrms  
dBrB: 1.000 Vrms  
dBrA Offset: 0.000 dB  
dBrB Offset: 0.000 dB  
dBSPL1: 10.00 mVrms  
dBSPL2: 10.00 mVrms  
dBSPL1 Calibrator Level: 94.000 dB SPL  
dBSPL2 Calibrator Level: 94.000 dB SPL  
dBm (Input Power): 600.0 ohm  
W(watts) (Input Power): 8.000 ohm

#### • DCX

DC Output 1: 0.000 V  
DC Output 1: Off  
DC Output 2: 0.000 V  
DC Output 2: Off  
Port A (hex): 00  
Port B (hex): 00  
Port C (hex): 00

## Sequence Report



Port D (hex): 00

Line Gain +10 200k Termination Level Low : Verify Connections

Waveform: Sine

Generator Level: -20.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (4/29/2023 10:47:33.745 PM)

Ch1 295.5 mVrms

Gain (4/29/2023 10:47:33.745 PM)

Ch1 11.631 dB

THD+N Ratio (4/29/2023 10:47:33.745 PM)

Ch1 ---- %

Frequency (4/29/2023 10:47:33.745 PM)

Ch1 ---- Hz

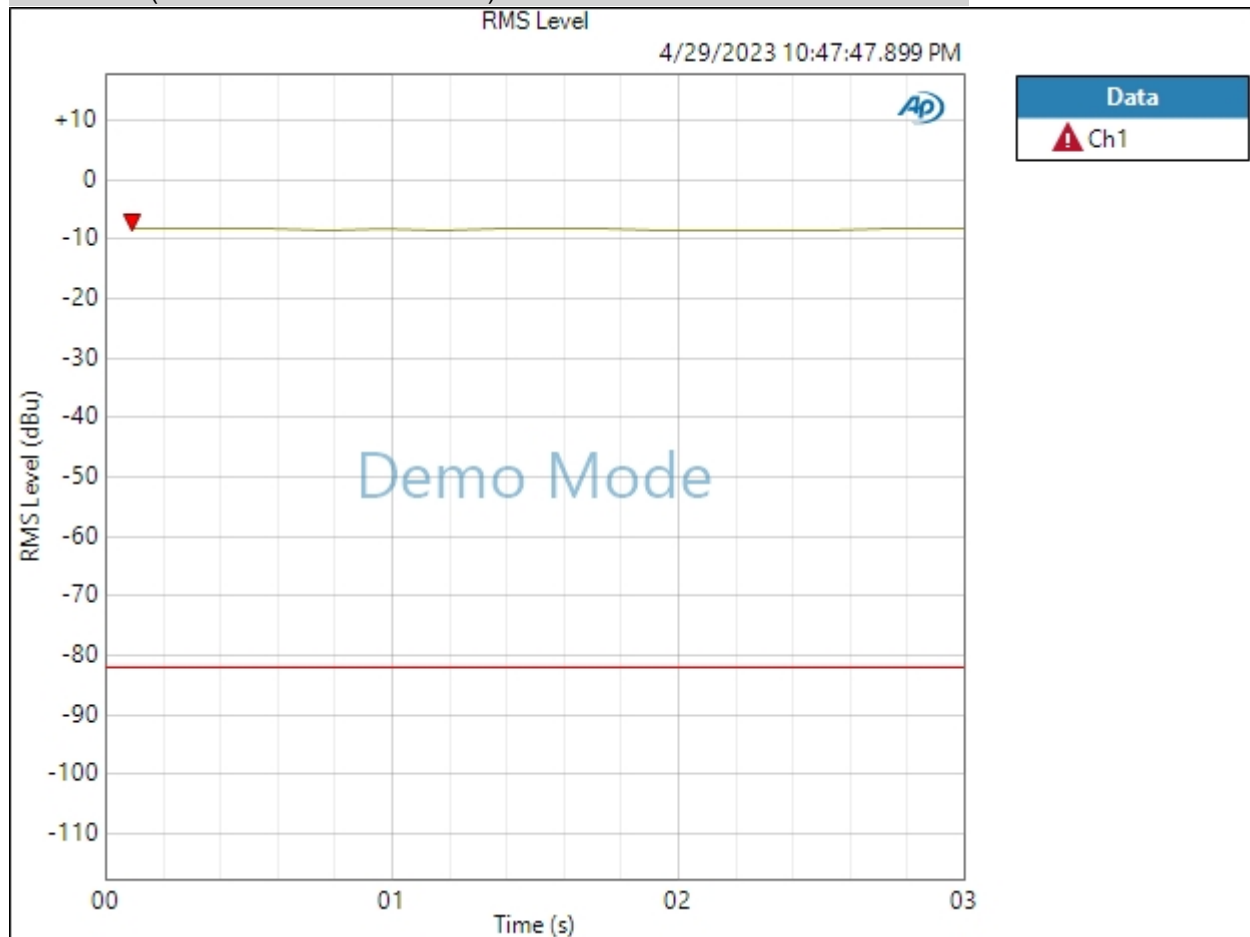
## Sequence Report



Line Gain +10 200k Termination Level Low : Noise Recorder (RMS) CCW

Waveform: None  
Low-pass Filter: 20 kHz  
Weighting Filter: Signal Path  
High-pass Filter: 20 Hz  
Sweep Time: 0.00:00:03.000  
Reading Rate: 10/sec  
Input Bandwidth: Use Signal Path  
Record Acquisition: False  
Measured 1 4/29/2023 10:47:47 PM

RMS Level (4/29/2023 10:47:47.899 PM)



Ch1 Failed Upper Limit

Result: FAILED

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## Sequence Report



### Hi Z Gain -10 2.2M 200k Termination : Signal Path Setup

|                   |                                |
|-------------------|--------------------------------|
| Output Connector: | Analog Balanced                |
| Channels:         | 2                              |
| Source Impedance: | 100 ohm                        |
| Output EQ:        | None                           |
| Input Connector:  | Analog Balanced                |
| Channels:         | 1                              |
| Channel:          | Ch1                            |
| Termination:      | 200 kohm                       |
| Input Bandwidth:  | AC (<10 Hz) - 90k (192 kHz SR) |
| Device Delay:     | 0.000 s                        |
| Input EQ:         | None                           |

#### • References

|                             |               |
|-----------------------------|---------------|
| dBr G:                      | 100.0 mVrms   |
| dBm (Output Power):         | 600.0 ohm     |
| W(watts) (Output Power):    | 8.000 ohm     |
| Shared Frequency Reference: | 1.00000 kHz   |
| dBrA:                       | 1.000 Vrms    |
| dBrB:                       | 1.000 Vrms    |
| dBrA Offset:                | 0.000 dB      |
| dBrB Offset:                | 0.000 dB      |
| dB SPL1:                    | 10.00 mVrms   |
| dB SPL2:                    | 10.00 mVrms   |
| dB SPL1 Calibrator Level:   | 94.000 dB SPL |
| dB SPL2 Calibrator Level:   | 94.000 dB SPL |
| dBm (Input Power):          | 600.0 ohm     |
| W(watts) (Input Power):     | 8.000 ohm     |

#### • DCX

|               |         |
|---------------|---------|
| DC Output 1:  | 0.000 V |
| DC Output 1:  | Off     |
| DC Output 2:  | 0.000 V |
| DC Output 2:  | Off     |
| Port A (hex): | 00      |
| Port B (hex): | 00      |
| Port C (hex): | 00      |

## Sequence Report



Port D (hex): 00

### Hi Z Gain -10 2.2M 200k Termination : Verify Connections

Waveform: Sine

Generator Level: -22.300 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (4/29/2023 10:47:53.239 PM)

Ch1 292.1 mVrms

Gain (4/29/2023 10:47:53.239 PM)

Ch1 13.829 dB

THD+N Ratio (4/29/2023 10:47:53.239 PM)

Ch1 ---- %

Frequency (4/29/2023 10:47:53.239 PM)

Ch1 ---- Hz

### Hi Z Gain -10 2.2M 200k Termination : Level and Gain 2.2M


Waveform: Sine

Generator Level: -22.300 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (4/29/2023 10:47:58.721 PM)

| Channel | Lower Limit | Value      | Upper Limit |   |
|---------|-------------|------------|-------------|---|
| Ch1     | -2.000 dBu  | -8.709 dBu | +2.000 dBu  |  |

Result:  FAILED

## Sequence Report



Hi Z Gain -10 47k 200k Termination : Signal Path Setup

Output Connector: Analog Unbalanced  
Channels: 2  
Source Impedance: 50 ohm  
Output EQ: None  
Input Connector: Analog Balanced  
Channels: 1  
Channel: Ch1  
Termination: 200 kohm  
Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)  
Device Delay: 0.000 s  
Input EQ: None

### • References

dBr G: 100.0 mVrms  
dBm (Output Power): 600.0 ohm  
W(watts) (Output Power): 8.000 ohm  
Shared Frequency Reference: 1.00000 kHz  
dBrA: 1.000 Vrms  
dBrB: 1.000 Vrms  
dBrA Offset: 0.000 dB  
dBrB Offset: 0.000 dB  
dBSPL1: 10.00 mVrms  
dBSPL2: 10.00 mVrms  
dBSPL1 Calibrator Level: 94.000 dB SPL  
dBSPL2 Calibrator Level: 94.000 dB SPL  
dBm (Input Power): 600.0 ohm  
W(watts) (Input Power): 8.000 ohm

### • DCX

DC Output 1: 0.000 V  
DC Output 1: Off  
DC Output 2: 0.000 V  
DC Output 2: Off  
Port A (hex): 00  
Port B (hex): 00  
Port C (hex): 00

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## Sequence Report



Port D (hex): 00

### Hi Z Gain -10 47k 200k Termination : Verify Connections

Waveform: Sine

Generator Level: -22.300 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (4/29/2023 10:48:04.221 PM)

Ch1 279.5 mVrms

Gain (4/29/2023 10:48:04.221 PM)

Ch1 13.445 dB

THD+N Ratio (4/29/2023 10:48:04.221 PM)

Ch1 ---- %

Frequency (4/29/2023 10:48:04.221 PM)

Ch1 ---- Hz

### Hi Z Gain -10 47k 200k Termination : Level and Gain 47K

Waveform: Sine

Generator Level: -22.300 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (4/29/2023 10:48:08.482 PM)

| Channel | Lower Limit | Value      | Upper Limit |   |
|---------|-------------|------------|-------------|---|
| Ch1     | -8.000 dBu  | -8.634 dBu | -4.000 dBu  |  |

Result:  FAILED

## Sequence Report



### DummyPathForReport : Signal Path Setup

|                   |                                |
|-------------------|--------------------------------|
| Output Connector: | Analog Unbalanced              |
| Channels:         | 2                              |
| Source Impedance: | 50 ohm                         |
| Output EQ:        | None                           |
| Input Connector:  | Analog Unbalanced              |
| Channels:         | 2                              |
| Termination:      | 100 kohm                       |
| Input Bandwidth:  | AC (<10 Hz) - 90k (192 kHz SR) |
| Device Delay:     | 0.000 s                        |
| Input EQ:         | None                           |

#### • References

|                             |               |
|-----------------------------|---------------|
| dBr G:                      | 100.0 mVrms   |
| dBm (Output Power):         | 600.0 ohm     |
| W(watts) (Output Power):    | 8.000 ohm     |
| Shared Frequency Reference: | 1.00000 kHz   |
| dBrA:                       | 1.000 Vrms    |
| dBrB:                       | 1.000 Vrms    |
| dBrA Offset:                | 0.000 dB      |
| dBrB Offset:                | 0.000 dB      |
| dB SPL1:                    | 10.00 mVrms   |
| dB SPL2:                    | 10.00 mVrms   |
| dB SPL1 Calibrator Level:   | 94.000 dB SPL |
| dB SPL2 Calibrator Level:   | 94.000 dB SPL |
| dBm (Input Power):          | 600.0 ohm     |
| W(watts) (Input Power):     | 8.000 ohm     |

#### • DCX

|               |         |
|---------------|---------|
| DC Output 1:  | 0.000 V |
| DC Output 1:  | Off     |
| DC Output 2:  | 0.000 V |
| DC Output 2:  | Off     |
| Port A (hex): | 00      |
| Port B (hex): | 00      |
| Port C (hex): | 00      |
| Port D (hex): | 00      |

## Sequence Report



### DummyPathForReport : Verify Connections

Waveform: Sine

Generator Level: 100.0 mVrms

DC Offset: 0.000 V

Frequency: 1.00000 kHz

### RMS Level (4/29/2023 10:48:12.128 PM)

Ch1 285.8 mVrms

Ch2 288.4 mVrms