

# Summary

Mic500 200k Termination	
Signal Path Setup	PASSED
Stepped Frequency Sweep MIC 500	A FAILED
Mic 2k 200k termination	
Signal Path Setup	PASSED
Stepped Frequency Sweep MIC 2K	▲ FAILED
Mic 2k 15dB PAD 200k termination	
Signal Path Setup	PASSED
Stepped Frequency Sweep 15dB PAD	▲ FAILED
Line Gain -10 200kTermination	
Signal Path Setup	PASSED
Stepped Frequency Sweep -10	A FAILED
Line Gain -10 600 Termination	
Signal Path Setup	PASSED
Level and Gain -10	A FAILED
Line Gain +5 200kTermination	
Signal Path Setup	PASSED
Stepped Frequency Sweep +5	A FAILED
Line Gain +5 600 Termination	
Signal Path Setup	PASSED
Level and Gain +5	PASSED
Line Gain -5 600 Termination	
Signal Path Setup	PASSED
Level and Gain -5	PASSED
Line Gain 0 600 Termination	
Signal Path Setup	PASSED
Level and Gain 0	PASSED
Line Gain +10 600 Termination	
Signal Path Setup	PASSED
Level and Gain +10	PASSED
Line Gain +10 200k Termination Level Hi	
Signal Path Setup	PASSED
Noise Recorder (RMS) CW	▲ FAILED
Line Gain +10 200k Termination Level Low	
Signal Path Setup	PASSED
Noise Recorder (RMS) CCW	PASSED
Hi Z Gain -10 2.2M 200k Termination	

Signal Path Setup	♥ PASSED
Level and Gain 2.2M	<b>⊘</b> PASSED
Hi Z Gain -10 47k 200k Termination	
Signal Path Setup	PASSED
Level and Gain 47K	PASSED
Dummy Signal Path For Report	
Signal Path Setup	▼ PASSED
Sequence Result:	
Sequence Result:	

6/1/2023 11:13 AM Page 2 of 45



Mic500 200k Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms 10.00 mVrms dBSPL2: dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

DCX is not detected.

W(watts) (Input Power):

6/1/2023 11:13 AM Page 3 of 45



Mic500 200k Termination : Verify Connections

Waveform: Sine

Generator Level: -42.300 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (6/1/2023 11:10:50.224 AM)

Ch1 1.065 mVrms

Gain (6/1/2023 11:10:50.224 AM)

Ch1 -14.929 dB

THD+N Ratio (6/1/2023 11:10:50.224 AM)

Ch1 58.041292 %

Frequency (6/1/2023 11:10:50.224 AM)

Ch1 120.011 Hz

6/1/2023 11:13 AM Page 4 of 45



Mic500 200k Termination: Stepped Frequency Sweep MIC 500

Generator Level: -42.300 dBu
DC Offset: 0.000 V
EQ: None

Start Frequency: 20.0000 kHz
Stop Frequency: 20.0000 Hz
Step Type: Logarithmic

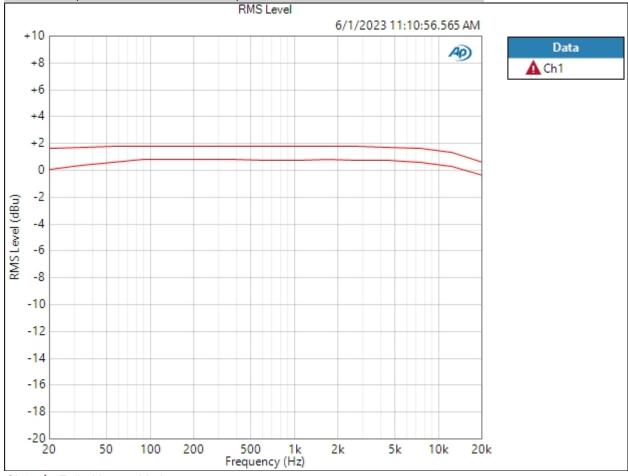
Number of Points: 10

Weighting Filter: Signal Path

High-pass Filter: 20 Hz Phase Ref Channel: Ch1

Measured 1 6/1/2023 11:10:56 AM

#### RMS Level (6/1/2023 11:10:56.565 AM)



6/1/2023 11:13 AM Page 5 of 45



Result: A FAILED

THD+N Ratio (6/1/2023 11:10:56.565 AM)



Result: V PASSED

THD Ratio (6/1/2023 11:10:56.565 AM)

6/1/2023 11:13 AM Page 6 of 45



Result: 🛕 FAILED

6/1/2023 11:13 AM Page 7 of 45



Mic 2k 200k termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1
Channel: Ch1

\_\_\_\_\_

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms 10.00 mVrms dBSPL2: dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

DCX is not detected.

W(watts) (Input Power):

6/1/2023 11:13 AM Page 8 of 45



Mic 2k 200k termination : Verify Connections

Waveform: Sine

Generator Level: -42.300 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (6/1/2023 11:11:02.024 AM)

Ch1 1.029 mVrms

Gain (6/1/2023 11:11:02.024 AM)

Ch1 -15.260 dB

THD+N Ratio (6/1/2023 11:11:02.024 AM)

Ch1 58.627921 %

Frequency (6/1/2023 11:11:02.024 AM)

Ch1 120.024 Hz

6/1/2023 11:13 AM Page 9 of 45



Mic 2k 200k termination : Stepped Frequency Sweep MIC 2K

Generator Level: -42.300 dBu
DC Offset: 0.000 V
EQ: None

Start Frequency: 20.0000 kHz
Stop Frequency: 20.0000 Hz
Step Type: Logarithmic

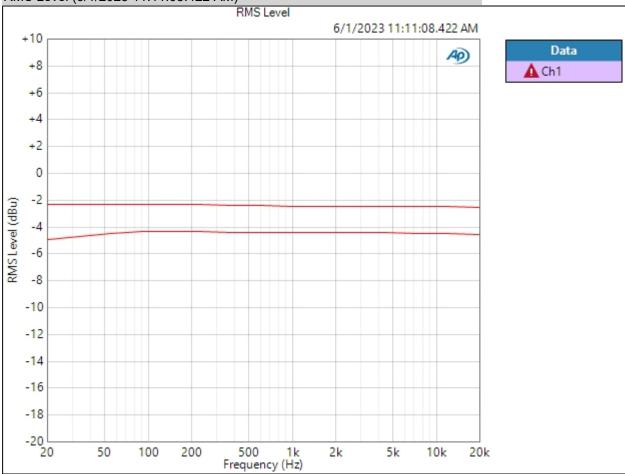
Number of Points: 10

Weighting Filter: Signal Path

High-pass Filter: 20 Hz Phase Ref Channel: Ch1

Measured 1 6/1/2023 11:11:08 AM

#### RMS Level (6/1/2023 11:11:08.422 AM)

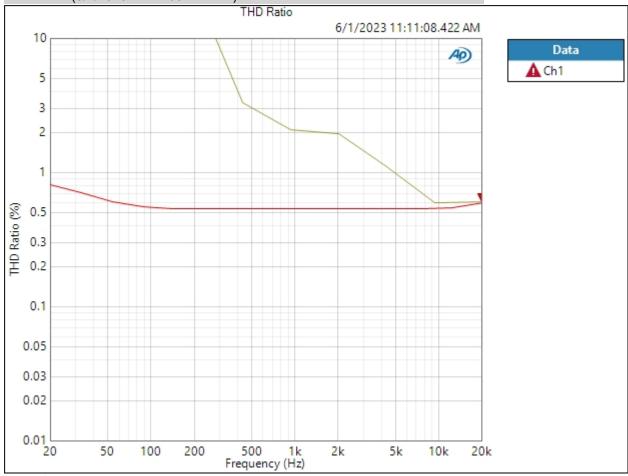


6/1/2023 11:13 AM Page 10 of 45



Result: A FAILED





Ch1 A Failed Upper Limit

Result: A FAILED

6/1/2023 11:13 AM Page 11 of 45



Mic 2k 15dB PAD 200k termination : Signal Path Setup
Output Connector:

Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1
Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm W(watts) (Input Power): 8.000 ohm

• DCX

DCX is not detected.

6/1/2023 11:13 AM Page 12 of 45



Mic 2k 15dB PAD 200k termination : Verify Connections

Waveform: Sine

Generator Level: -42.300 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (6/1/2023 11:11:13.925 AM)

Ch1 1.127 mVrms

Gain (6/1/2023 11:11:13.925 AM)

Ch1 -14.452 dB

THD+N Ratio (6/1/2023 11:11:13.925 AM)

Ch1 57.426691 %

Frequency (6/1/2023 11:11:13.925 AM)

Ch1 120.001 Hz

6/1/2023 11:13 AM Page 13 of 45



#### Mic 2k 15dB PAD 200k termination : Stepped Frequency Sweep 15dB PAD

Generator Level: -42.000 dBu
DC Offset: 0.000 V
EQ: None

Start Frequency: 20.0000 kHz
Stop Frequency: 20.0000 Hz
Step Type: Logarithmic

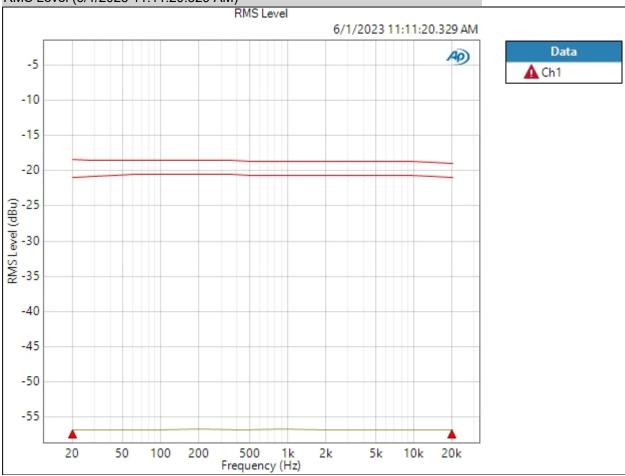
Number of Points: 10

Weighting Filter: Signal Path

High-pass Filter: 20 Hz Phase Ref Channel: Ch1

Measured 1 6/1/2023 11:11:20 AM

#### RMS Level (6/1/2023 11:11:20.329 AM)



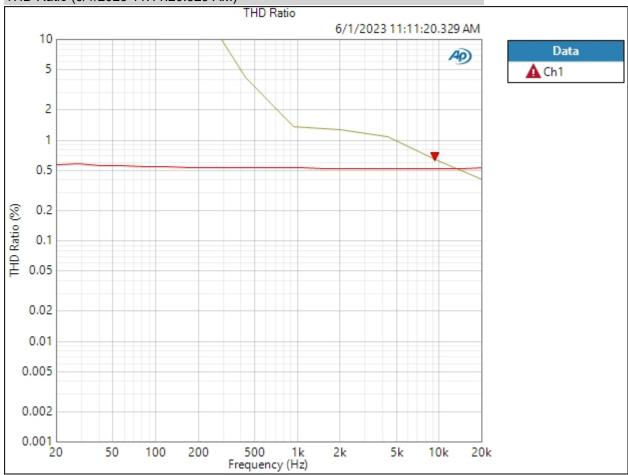
Ch1 A Failed Lower Limit

6/1/2023 11:13 AM Page 14 of 45



Result: A FAILED





Ch1 A Failed Upper Limit

Result: A FAILED

6/1/2023 11:13 AM Page 15 of 45



Line Gain -10 200kTermination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

DCX is not detected.

W(watts) (Input Power):

6/1/2023 11:13 AM Page 16 of 45



Line Gain -10 200kTermination : Verify Connections

Waveform: Sine

Generator Level: 0.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (6/1/2023 11:11:26.164 AM)

Ch1 1.212 mVrms

Gain (6/1/2023 11:11:26.164 AM)

Ch1 -56.118 dB

THD+N Ratio (6/1/2023 11:11:26.164 AM)

Ch1 55.836574 %

Frequency (6/1/2023 11:11:26.164 AM)

Ch1 119.970 Hz

6/1/2023 11:13 AM Page 17 of 45



#### Line Gain -10 200kTermination: Stepped Frequency Sweep -10

Generator Level: 0.000 dBu
DC Offset: 0.000 V
EQ: None

Start Frequency: 20.0000 kHz
Stop Frequency: 20.0000 Hz
Step Type: Logarithmic

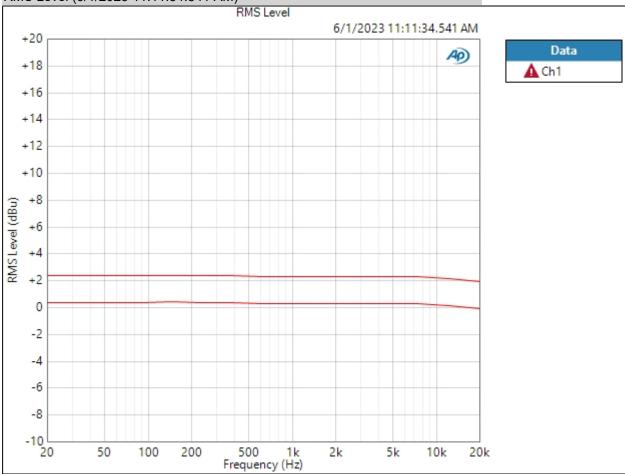
Number of Points: 15

Weighting Filter: Signal Path

High-pass Filter: 20 Hz Phase Ref Channel: Ch1

Measured 1 6/1/2023 11:11:34 AM

#### RMS Level (6/1/2023 11:11:34.541 AM)

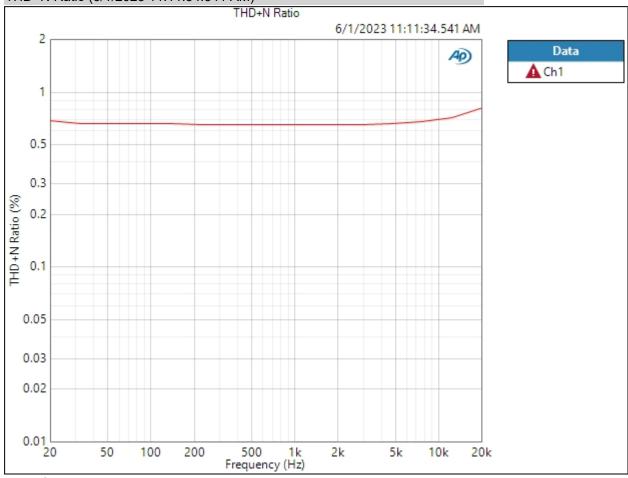


6/1/2023 11:13 AM Page 18 of 45



Result: A FAILED

THD+N Ratio (6/1/2023 11:11:34.541 AM)



Ch1 A Failed Upper Limit

Result: A FAILED

6/1/2023 11:13 AM Page 19 of 45



Line Gain -10 600 Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 600 ohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms 10.00 mVrms dBSPL2: dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

DCX is not detected.

W(watts) (Input Power):

6/1/2023 11:13 AM Page 20 of 45



Line Gain -10 600 Termination: Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (6/1/2023 11:11:40.608 AM)

Ch1 1.061 mVrms

Gain (6/1/2023 11:11:40.608 AM)

Ch1 -47.270 dB

THD+N Ratio (6/1/2023 11:11:40.608 AM)

Ch1 55.772939 %

Frequency (6/1/2023 11:11:40.608 AM)

Ch1 120.016 Hz

Line Gain -10 600 Termination: Level and Gain -10

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (6/1/2023 11:11:43.768 AM)

Channel Lower Limit Value Upper Limit
Ch1 -11.500 dBu -57.267 dBu -8.500 dBu

Result: A FAILED

6/1/2023 11:13 AM Page 21 of 45



Line Gain +5 200kTermination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

DCX is not detected.

W(watts) (Input Power):

6/1/2023 11:13 AM Page 22 of 45



Line Gain +5 200kTermination : Verify Connections

Waveform: Sine

Generator Level: 0.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (6/1/2023 11:11:49.592 AM)

Ch1 6.848 mVrms

Gain (6/1/2023 11:11:49.592 AM)

Ch1 -41.060 dB

THD+N Ratio (6/1/2023 11:11:49.592 AM)

Ch1 54.830889 %

Frequency (6/1/2023 11:11:49.592 AM)

Ch1 120.025 Hz

6/1/2023 11:13 AM Page 23 of 45



#### Line Gain +5 200kTermination : Stepped Frequency Sweep +5

Generator Level: 0.000 dBu
DC Offset: 0.000 V
EQ: None

Start Frequency: 20.0000 kHz
Stop Frequency: 20.0000 Hz
Step Type: Logarithmic

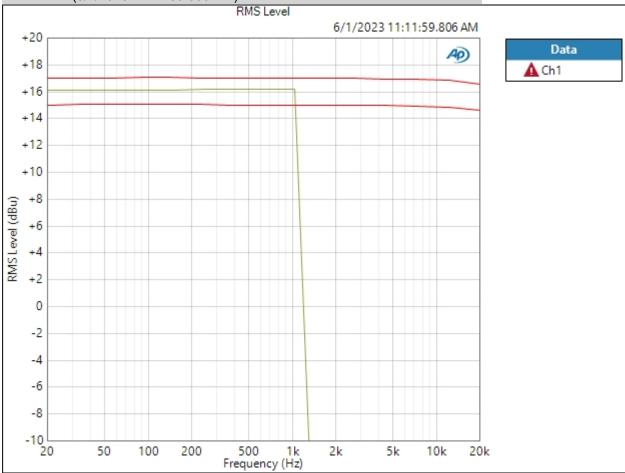
Number of Points: 15

Weighting Filter: Signal Path

High-pass Filter: 20 Hz Phase Ref Channel: Ch1

Measured 1 6/1/2023 11:11:59 AM

#### RMS Level (6/1/2023 11:11:59.806 AM)

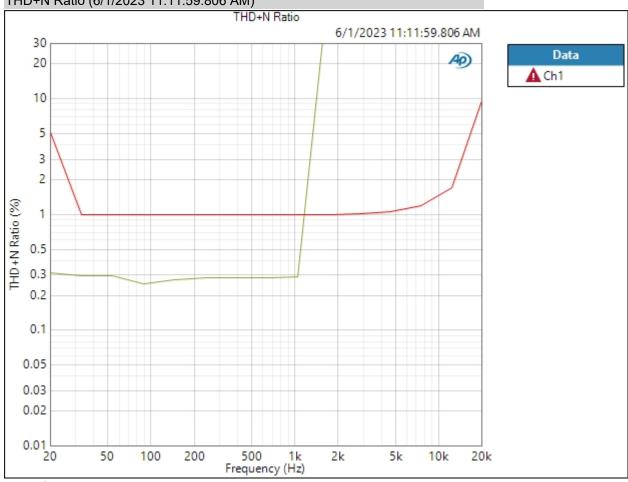


6/1/2023 11:13 AM Page 24 of 45



Result: A FAILED

THD+N Ratio (6/1/2023 11:11:59.806 AM)



Ch1 A Failed Upper Limit

Result: A FAILED

6/1/2023 11:13 AM Page 25 of 45



Line Gain +5 600 Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 600 ohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms 10.00 mVrms dBSPL2: dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

DCX is not detected.

W(watts) (Input Power):

6/1/2023 11:13 AM Page 26 of 45



Line Gain +5 600 Termination : Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (6/1/2023 11:12:05.898 AM)

Ch1 1.383 Vrms

Gain (6/1/2023 11:12:05.898 AM)

Ch1 15.034 dB

THD+N Ratio (6/1/2023 11:12:05.898 AM)

Ch1 0.491287 %

Frequency (6/1/2023 11:12:05.898 AM)

Ch1 1.00000 kHz

Line Gain +5 600 Termination: Level and Gain +5

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (6/1/2023 11:12:09.107 AM)

Result: V PASSED

6/1/2023 11:13 AM Page 27 of 45



Line Gain -5 600 Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 600 ohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms 10.00 mVrms dBSPL2: dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

DCX is not detected.

W(watts) (Input Power):

6/1/2023 11:13 AM Page 28 of 45



Line Gain -5 600 Termination: Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (6/1/2023 11:12:15.074 AM)

Ch1 428.4 mVrms

Gain (6/1/2023 11:12:15.074 AM)

Ch1 4.855 dB

THD+N Ratio (6/1/2023 11:12:15.074 AM)

Ch1 0.426363 %

Frequency (6/1/2023 11:12:15.074 AM)

Ch1 1.00000 kHz

Line Gain -5 600 Termination: Level and Gain -5

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (6/1/2023 11:12:18.364 AM)

Result: V PASSED

6/1/2023 11:13 AM Page 29 of 45



Line Gain 0 600 Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 600 ohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms 10.00 mVrms dBSPL2: dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL

• DCX

DCX is not detected.

W(watts) (Input Power):

dBm (Input Power):

6/1/2023 11:13 AM Page 30 of 45

600.0 ohm



Line Gain 0 600 Termination: Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (6/1/2023 11:12:24.359 AM)

Ch1 747.5 mVrms

Gain (6/1/2023 11:12:24.359 AM)

Ch1 9.690 dB

THD+N Ratio (6/1/2023 11:12:24.359 AM)

Ch1 0.440110 %

Frequency (6/1/2023 11:12:24.359 AM)

Ch1 1.00000 kHz

Line Gain 0 600 Termination: Level and Gain 0

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (6/1/2023 11:12:27.667 AM)

Result: V PASSED

6/1/2023 11:13 AM Page 31 of 45



Line Gain +10 600 Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 600 ohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

DCX is not detected.

W(watts) (Input Power):

6/1/2023 11:13 AM Page 32 of 45



Line Gain +10 600 Termination: Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (6/1/2023 11:12:33.710 AM)

Ch1 2.438 Vrms

Gain (6/1/2023 11:12:33.710 AM)

Ch1 19.959 dB

THD+N Ratio (6/1/2023 11:12:33.710 AM)

Ch1 0.596800 %

Frequency (6/1/2023 11:12:33.710 AM)

Ch1 1.00000 kHz

Line Gain +10 600 Termination: Level and Gain +10

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (6/1/2023 11:12:37.055 AM)

Channel Lower Limit Value Upper Limit
Ch1 +8.500 dBu +9.957 dBu +11.500 dBu ❖

Result: V PASSED

6/1/2023 11:13 AM Page 33 of 45



Line Gain +10 200k Termination Level Hi : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms 10.00 mVrms dBSPL2: dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL

• DCX

DCX is not detected.

dBm (Input Power):

W(watts) (Input Power):

6/1/2023 11:13 AM Page 34 of 45

600.0 ohm



Line Gain +10 200k Termination Level Hi: Verify Connections

Waveform: Sine

Generator Level: -20.000 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (6/1/2023 11:12:43.022 AM)

Ch1 877.2 mVrms

Gain (6/1/2023 11:12:43.022 AM)

Ch1 21.080 dB

THD+N Ratio (6/1/2023 11:12:43.022 AM)

Ch1 1.323537 %

Frequency (6/1/2023 11:12:43.022 AM)

Ch1 1.00000 kHz

6/1/2023 11:13 AM Page 35 of 45



#### Line Gain +10 200k Termination Level Hi: Noise Recorder (RMS) CW

Waveform: None
Low-pass Filter: 20 kHz
Weighting Filter: Signal Path

High-pass Filter: 20 Hz

Sweep Time: 0.00:00:03.000

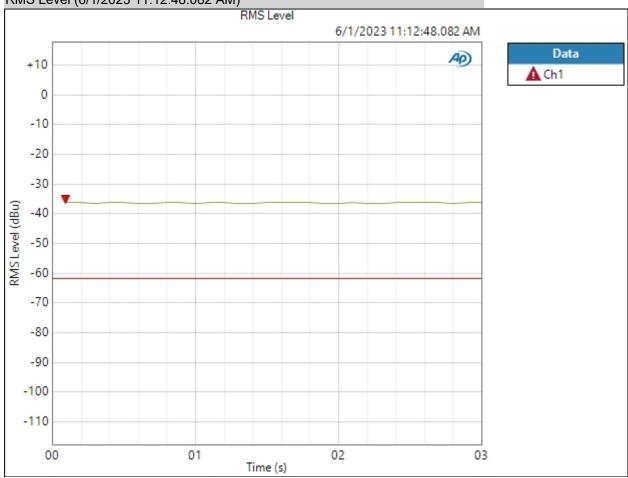
Reading Rate: 10/sec

Input Bandwidth: Use Signal Path

Record Acquisition: False

Measured 1 6/1/2023 11:12:48 AM

#### RMS Level (6/1/2023 11:12:48.082 AM)



Result: A FAILED

6/1/2023 11:13 AM Page 36 of 45



Line Gain +10 200k Termination Level Low: Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm W(watts) (Input Power): 8.000 ohm

• DCX

DCX is not detected.

6/1/2023 11:13 AM Page 37 of 45



Line Gain +10 200k Termination Level Low: Verify Connections

Waveform: Sine

Generator Level: -20.000 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (6/1/2023 11:12:54.270 AM)

Ch1 23.02 uVrms

Gain (6/1/2023 11:12:54.270 AM)

Ch1 -70.559 dB

THD+N Ratio (6/1/2023 11:12:54.270 AM)

Ch1 ---- %

Frequency (6/1/2023 11:12:54.270 AM)

Ch1 ---- Hz

6/1/2023 11:13 AM Page 38 of 45



#### Line Gain +10 200k Termination Level Low: Noise Recorder (RMS) CCW

Waveform: None
Low-pass Filter: 20 kHz
Weighting Filter: Signal Path

High-pass Filter: 20 Hz

Sweep Time: 0.00:00:03.000

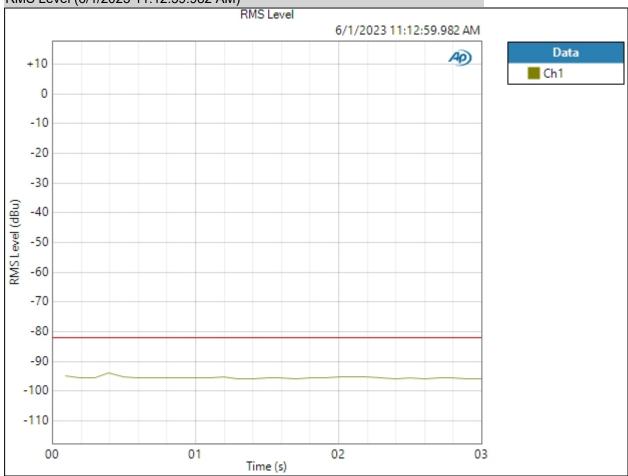
Reading Rate: 10/sec

Input Bandwidth: Use Signal Path

Record Acquisition: False

Measured 1 6/1/2023 11:12:59 AM

#### RMS Level (6/1/2023 11:12:59.982 AM)



Ch1 S PASSED

Result: V PASSED

6/1/2023 11:13 AM Page 39 of 45



Hi Z Gain -10 2.2M 200k Termination : Signal Path Setup

Output Connector: Analog Unbalanced

Channels: 2

Source Impedance: 50 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

DCX is not detected.

W(watts) (Input Power):

6/1/2023 11:13 AM Page 40 of 45



Hi Z Gain -10 2.2M 200k Termination: Verify Connections

Waveform: Sine

Generator Level: -22.300 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (6/1/2023 11:13:05.991 AM)

Ch1 3.615 mVrms

Gain (6/1/2023 11:13:05.991 AM)

Ch1 -24.319 dB

THD+N Ratio (6/1/2023 11:13:05.991 AM)

Ch1 71.978540 %

Frequency (6/1/2023 11:13:05.991 AM)

Ch1 59.9918 Hz

Hi Z Gain -10 2.2M 200k Termination: Level and Gain 2.2M

Waveform: Sine

Generator Level: -22.300 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (6/1/2023 11:13:10.046 AM)

Channel Lower Limit Value Upper Limit
Ch1 -2.000 dBu +0.209 dBu +2.000 dBu ❖

Result: V PASSED

6/1/2023 11:13 AM Page 41 of 45



Hi Z Gain -10 47k 200k Termination : Signal Path Setup

Output Connector: Analog Unbalanced

2 Channels:

Source Impedance: 50 ohm Output EQ: None

Input Connector: **Analog Balanced** 

Channels:

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

0.000 sDevice Delay: Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL

dBSPL2 Calibrator Level: dBm (Input Power): 600.0 ohm W(watts) (Input Power): 8.000 ohm

• DCX

DCX is not detected.

6/1/2023 11:13 AM Page 42 of 45

94.000 dBSPL



Hi Z Gain -10 47k 200k Termination : Verify Connections

Waveform: Sine

Generator Level: -22.300 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (6/1/2023 11:13:15.972 AM)

Ch1 405.8 mVrms

Gain (6/1/2023 11:13:15.972 AM)

Ch1 16.684 dB

THD+N Ratio (6/1/2023 11:13:15.972 AM)

Ch1 0.574557 %

Frequency (6/1/2023 11:13:15.972 AM)

Ch1 1.00000 kHz

Hi Z Gain -10 47k 200k Termination: Level and Gain 47K

Waveform: Sine

Generator Level: -22.300 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (6/1/2023 11:13:19.338 AM)

Result: V PASSED

6/1/2023 11:13 AM Page 43 of 45



Dummy Signal Path For Report : Signal Path Setup

Output Connector: Analog Unbalanced

Channels: 2

Source Impedance: 50 ohm
Output EQ: None

Input Connector: Analog Unbalanced

Channels: 2

Termination: 100 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms 94.000 dBSPL dBSPL1 Calibrator Level: dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

DCX is not detected.

W(watts) (Input Power):

6/1/2023 11:13 AM Page 44 of 45



Dummy Signal Path For Report : Verify Connections

Waveform: Sine

Generator Level: 100.0 mVrms

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (6/1/2023 11:13:23.346 AM)

Ch1 78.16 uVrms Ch2 7.469 uVrms

6/1/2023 11:13 AM Page 45 of 45