

### Summary

Mic500 200k Termination	
Signal Path Setup	🍼 PASSED
Stepped Frequency Sweep MIC 500	🛕 FAILED
Mic 2k 200k termination	
Signal Path Setup	PASSED
Stepped Frequency Sweep MIC 2K	▲ FAILED
Mic 2k 15dB PAD 200k termination	
Signal Path Setup	PASSED
Stepped Frequency Sweep 15dB PAD	🛕 FAILED
Line Gain -10 200kTermination	
Signal Path Setup	PASSED
Stepped Frequency Sweep -10	▲ FAILED
Line Gain -10 600 Termination	
Signal Path Setup	PASSED
Level and Gain -10	PASSED
Line Gain +5 200kTermination	
Signal Path Setup	PASSED
Stepped Frequency Sweep +5	▲ FAILED
Line Gain +5 600 Termination	
Signal Path Setup	PASSED
Level and Gain +5	▲ FAILED
Line Gain -5 600 Termination	
Signal Path Setup	PASSED
Level and Gain -5	▲ FAILED
Line Gain 0 600 Termination	
Signal Path Setup	PASSED
Level and Gain 0	▲ FAILED
Line Gain +10 600 Termination	
Signal Path Setup	PASSED
Level and Gain +10	▲ FAILED
Line Gain +10 200k Termination Level Hi	
Signal Path Setup	PASSED
Noise Recorder (RMS) CW	▲ FAILED
Line Gain +10 200k Termination Level Low	
Signal Path Setup	PASSED
Noise Recorder (RMS) CCW	▲ FAILED
Hi Z Gain -10 2.2M 200k Termination	

Signal Path Setup Level and Gain 2.2M	♥ PASSED ♠ FAILED
Hi Z Gain -10 47k 200k Termination	
Signal Path Setup Level and Gain 47K	▼ PASSED  ▲ FAILED
Dummy Signal Path For Report	
Signal Path Setup	PASSED
Sequence Result:	
Sequence Result: 🛕 FAILED	

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Mic500 200k Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
AG52 Generator Option: Installed
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

8.000 ohm

Device Delay: 0.000 s
Input EQ: None

References

100.0 mVrms dBr G: dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

W(watts) (Input Power):

DC Output 1: 0.000 V
DC Output 1: Off
DC Output 2: 0.000 V
DC Output 2: Off
Port A (hex): 00
Port B (hex): 00



Port C (hex): 00
Port D (hex): 00

Clocks

Output Rate: Track Output SR

Sync Out Level: 3.300 V
Sync Out Polarity: Normal
Timebase Reference: Internal
Jitter: Disabled

Triggers

Source: Off
Input Logic Level: 3.300 V
Edge: Rising

Mic500 200k Termination: Verify Connections

Waveform: Sine

Generator Level: -42.300 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/15/2023 11:17:49.958 AM)

Ch1 291.9 mVrms

Gain (5/15/2023 11:17:49.958 AM)

Ch1 33.825 dB

THD+N Ratio (5/15/2023 11:17:49.958 AM)

Ch1 ---- %

Frequency (5/15/2023 11:17:49.958 AM)

Ch1 ---- Hz

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Mic500 200k Termination: Stepped Frequency Sweep MIC 500

Generator Level: -42.300 dBu
DC Offset: 0.000 V
EQ: None

Start Frequency: 20.0000 kHz
Stop Frequency: 20.0000 Hz
Step Type: Logarithmic

Number of Points: 10

Weighting Filter: Signal Path
High-pass Filter: 20 Hz
Phase Ref Channel: Ch1

Measured 1 5/15/2023 11:17:53 AM

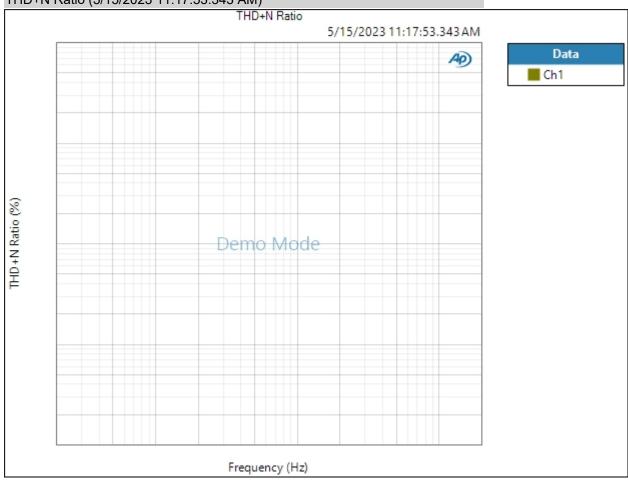
# RMS Level (5/15/2023 11:17:53.343 AM) RMS Level 5/15/2023 11:17:53.343 AM Data Ch1 Prequency (Hz)

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Result: A FAILED

THD+N Ratio (5/15/2023 11:17:53.343 AM)



THD Ratio (5/15/2023 11:17:53.343 AM)

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Result: 🛕 FAILED

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Mic 2k 200k termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
AG52 Generator Option: Installed
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm W(watts) (Input Power): 8.000 ohm

• DCX

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00



Port D (hex): 00

• Clocks

Output Rate: Track Output SR

Sync Out Level: 3.300 V
Sync Out Polarity: Normal
Timebase Reference: Internal
Jitter: Disabled

• Triggers

Source: Off

Input Logic Level: 3.300 V Edge: Rising

Mic 2k 200k termination: Verify Connections

Waveform: Sine

Generator Level: -42.300 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/15/2023 11:17:58.134 AM)

Ch1 286.8 mVrms

Gain (5/15/2023 11:17:58.134 AM)

Ch1 33.670 dB

THD+N Ratio (5/15/2023 11:17:58.134 AM)

Ch1 ---- %

Frequency (5/15/2023 11:17:58.134 AM)

Ch1 ---- Hz

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Mic 2k 200k termination : Stepped Frequency Sweep MIC 2K

Generator Level: -42.300 dBu
DC Offset: 0.000 V
EQ: None

Start Frequency: 20.0000 kHz
Stop Frequency: 20.0000 Hz
Step Type: Logarithmic

Number of Points: 10

Weighting Filter: Signal Path
High-pass Filter: 20 Hz
Phase Ref Channel: Ch1

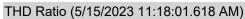
Measured 1 5/15/2023 11:18:01 AM

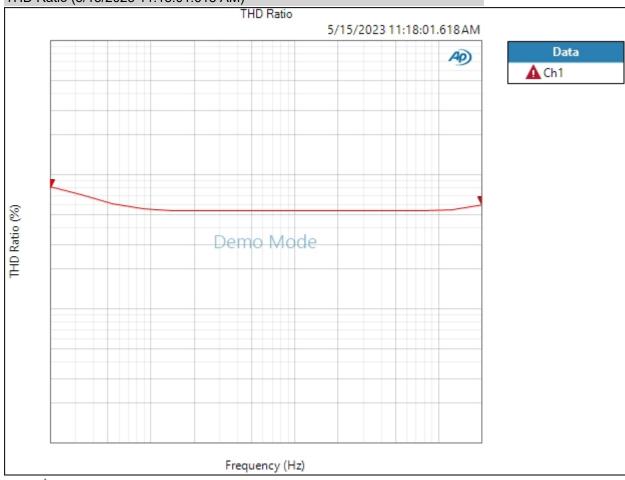
# RMS Level (5/15/2023 11:18:01.618 AM) RMS Level 5/15/2023 11:18:01.618 AM Data Ch1 Prequency (Hz)

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Result: A FAILED





Ch1 A Failed Upper Limit

Result: A FAILED

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Mic 2k 15dB PAD 200k termination : Signal Path Setup
Output Connector:

Analog Balanced

Channels: 1

Source Impedance: 100 ohm
AG52 Generator Option: Installed
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

8.000 ohm

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00

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W(watts) (Input Power):



Port D (hex): 00

• Clocks

Output Rate: Track Output SR

Sync Out Level: 3.300 V
Sync Out Polarity: Normal
Timebase Reference: Internal
Jitter: Disabled

• Triggers

Source: Off

Input Logic Level: 3.300 V
Edge: Rising

Mic 2k 15dB PAD 200k termination: Verify Connections

Waveform: Sine

Generator Level: -42.300 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/15/2023 11:18:06.527 AM)

Ch1 281.5 mVrms

Gain (5/15/2023 11:18:06.527 AM)

Ch1 33.509 dB

THD+N Ratio (5/15/2023 11:18:06.527 AM)

Ch1 ---- %

Frequency (5/15/2023 11:18:06.527 AM)

Ch1 ---- Hz

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Mic 2k 15dB PAD 200k termination : Stepped Frequency Sweep 15dB PAD

Generator Level: -42.000 dBu
DC Offset: 0.000 V
EQ: None

Start Frequency: 20.0000 kHz
Stop Frequency: 20.0000 Hz
Step Type: Logarithmic

Number of Points: 10

Weighting Filter: Signal Path
High-pass Filter: 20 Hz
Phase Ref Channel: Ch1

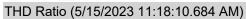
Measured 1 5/15/2023 11:18:10 AM

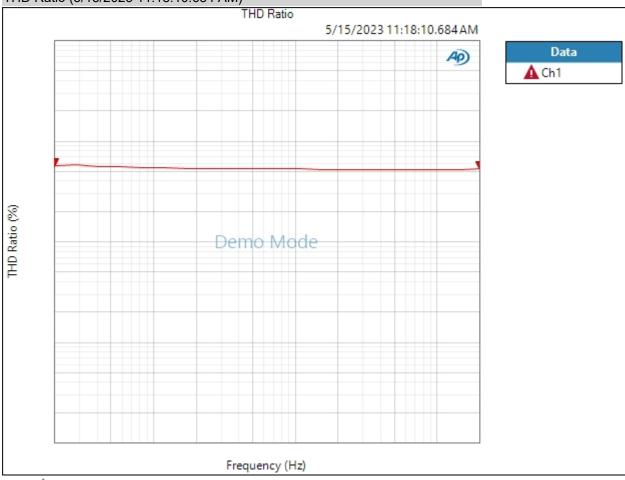
# RMS Level (5/15/2023 11:18:10.684 AM) RMS Level 5/15/2023 11:18:10.684 AM Data Ch1 Frequency (Hz)

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Result: A FAILED





Result: A FAILED

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Line Gain -10 200kTermination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
AG52 Generator Option: Installed
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

8.000 ohm

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

W(watts) (Input Power):

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00



Port D (hex): 00

• Clocks

Output Rate: Track Output SR

Sync Out Level: 3.300 V
Sync Out Polarity: Normal
Timebase Reference: Internal
Jitter: Disabled

• Triggers

Source: Off

Input Logic Level: 3.300 V Edge: Rising

Line Gain -10 200kTermination: Verify Connections

Waveform: Sine

Generator Level: 0.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/15/2023 11:18:16.959 AM)

Ch1 290.9 mVrms

Gain (5/15/2023 11:18:16.959 AM)

Ch1 -8.506 dB

THD+N Ratio (5/15/2023 11:18:16.959 AM)

Ch1 ---- %

Frequency (5/15/2023 11:18:16.959 AM)

Ch1 ---- Hz

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Line Gain -10 200kTermination: Stepped Frequency Sweep -10

Generator Level: 0.000 dBu

DC Offset: 0.000 V

EQ: None

Start Frequency: 20.0000 kHz
Stop Frequency: 20.0000 Hz
Step Type: Logarithmic

Number of Points: 15

Weighting Filter: Signal Path
High-pass Filter: 20 Hz
Phase Ref Channel: Ch1

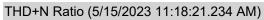
Measured 1 5/15/2023 11:18:21 AM

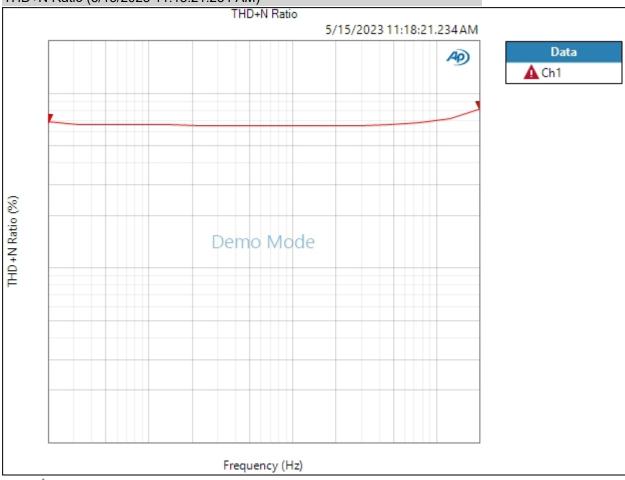
# RMS Level (5/15/2023 11:18:21.234 AM) RMS Level 5/15/2023 11:18:21.234 AM Data Ch1 Prequency (Hz)

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Result: A FAILED





Ch1 A Failed Upper Limit

Result: A FAILED

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Line Gain -10 600 Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
AG52 Generator Option: Installed
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 600 ohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm W(watts) (Input Power): 8.000 ohm

• DCX

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00



Port D (hex):

• Clocks

Output Rate: Track Output SR

Sync Out Level: 3.300 V
Sync Out Polarity: Normal
Timebase Reference: Internal
Jitter: Disabled

• Triggers

Source: Off

Input Logic Level: 3.300 V Edge: Rising

Line Gain -10 600 Termination: Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/15/2023 11:18:26.959 AM)

Ch1 291.9 mVrms

Gain (5/15/2023 11:18:26.959 AM)

Ch1 1.523 dB

THD+N Ratio (5/15/2023 11:18:26.959 AM)

Ch1 ---- %

Frequency (5/15/2023 11:18:26.959 AM)

Ch1 ---- Hz

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Line Gain -10 600 Termination: Level and Gain -10

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/15/2023 11:18:29.573 AM)

Channel Lower Limit Value Upper Limit
Ch1 -11.500 dBu -8.598 dBu -8.500 dBu

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**3** 



Line Gain +5 200kTermination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
AG52 Generator Option: Installed
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm W(watts) (Input Power): 8.000 ohm

• DCX

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00



Port D (hex): 00

• Clocks

Output Rate: Track Output SR

Sync Out Level: 3.300 V
Sync Out Polarity: Normal
Timebase Reference: Internal
Jitter: Disabled

• Triggers

Source: Off

Input Logic Level: 3.300 V Edge: Rising

Line Gain +5 200kTermination : Verify Connections

Waveform: Sine

Generator Level: 0.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/15/2023 11:18:35.445 AM)

Ch1 294.6 mVrms

Gain (5/15/2023 11:18:35.445 AM)

Ch1 -8.396 dB

THD+N Ratio (5/15/2023 11:18:35.445 AM)

Ch1 ---- %

Frequency (5/15/2023 11:18:35.445 AM)

Ch1 ---- Hz

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Line Gain +5 200kTermination : Stepped Frequency Sweep +5

Generator Level: 0.000 dBu

DC Offset: 0.000 V

EQ: None

Start Frequency: 20.0000 kHz
Stop Frequency: 20.0000 Hz
Step Type: Logarithmic

Number of Points: 15

Weighting Filter: Signal Path

High-pass Filter: 20 Hz Phase Ref Channel: Ch1

Measured 1 5/15/2023 11:18:39 AM

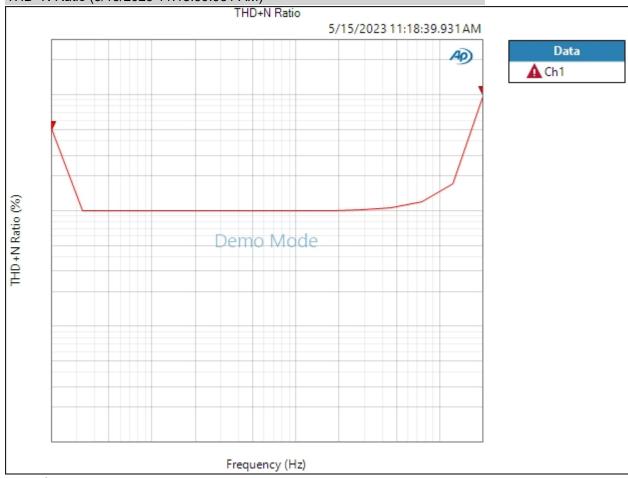
# RMS Level (5/15/2023 11:18:39.931 AM) RMS Level 5/15/2023 11:18:39.931 AM Data Ch1 Demo Mode Frequency (Hz)

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Result: A FAILED





Ch1 A Failed Upper Limit

Result: A FAILED

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Line Gain +5 600 Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
AG52 Generator Option: Installed
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 600 ohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm W(watts) (Input Power): 8.000 ohm

• DCX

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00



Port D (hex): 00

• Clocks

Output Rate: Track Output SR

Sync Out Level: 3.300 V
Sync Out Polarity: Normal
Timebase Reference: Internal
Jitter: Disabled

• Triggers

Source: Off

Input Logic Level: 3.300 V Edge: Rising

Line Gain +5 600 Termination: Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/15/2023 11:18:45.168 AM)

Ch1 276.1 mVrms

Gain (5/15/2023 11:18:45.168 AM)

Ch1 1.041 dB

THD+N Ratio (5/15/2023 11:18:45.168 AM)

Ch1 ---- %

Frequency (5/15/2023 11:18:45.168 AM)

Ch1 ---- Hz

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Line Gain +5 600 Termination: Level and Gain +5

Waveform: Sine

Generator Level: -10.000 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/15/2023 11:18:47.795 AM)

Channel Lower Limit Value Upper Limit
Ch1 +3.500 dBu -8.787 dBu +6.500 dBu

Result: A FAILED

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Line Gain -5 600 Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
AG52 Generator Option: Installed
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 600 ohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

8.000 ohm

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

W(watts) (Input Power):

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00



Port D (hex): 00

• Clocks

Output Rate: Track Output SR

Sync Out Level: 3.300 V
Sync Out Polarity: Normal
Timebase Reference: Internal
Jitter: Disabled

• Triggers

Source: Off

Input Logic Level: 3.300 V Edge: Rising

Line Gain -5 600 Termination: Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/15/2023 11:18:53.042 AM)

Ch1 280.3 mVrms

Gain (5/15/2023 11:18:53.042 AM)

Ch1 1.172 dB

THD+N Ratio (5/15/2023 11:18:53.042 AM)

Ch1 ---- %

Frequency (5/15/2023 11:18:53.042 AM)

Ch1 ---- Hz

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Line Gain -5 600 Termination: Level and Gain -5

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/15/2023 11:18:55.652 AM)

Channel Lower Limit Value Upper Limit
Ch1 -6.500 dBu -8.593 dBu -3.500 dBu

Result: A FAILED

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Line Gain 0 600 Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
AG52 Generator Option: Installed
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 600 ohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm W(watts) (Input Power): 8.000 ohm

• DCX

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00



Port D (hex): 00

• Clocks

Output Rate: Track Output SR

Sync Out Level: 3.300 V
Sync Out Polarity: Normal
Timebase Reference: Internal
Jitter: Disabled

• Triggers

Source: Off

Input Logic Level: 3.300 V Edge: Rising

Line Gain 0 600 Termination: Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/15/2023 11:19:00.975 AM)

Ch1 284.5 mVrms

Gain (5/15/2023 11:19:00.975 AM)

Ch1 1.300 dB

THD+N Ratio (5/15/2023 11:19:00.975 AM)

Ch1 ---- %

Frequency (5/15/2023 11:19:00.975 AM)

Ch1 ---- Hz

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Line Gain 0 600 Termination: Level and Gain 0

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/15/2023 11:19:03.732 AM)

Channel Lower Limit Value Upper Limit
Ch1 -1.500 dBu -8.849 dBu +1.500 dBu

Result: A FAILED

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Line Gain +10 600 Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
AG52 Generator Option: Installed
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 600 ohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm W(watts) (Input Power): 8.000 ohm

• DCX

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00



Port D (hex): 00

• Clocks

Output Rate: Track Output SR

Sync Out Level: 3.300 V
Sync Out Polarity: Normal
Timebase Reference: Internal
Jitter: Disabled

• Triggers

Source: Off

Input Logic Level: 3.300 V Edge: Rising

Line Gain +10 600 Termination: Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/15/2023 11:19:08.951 AM)

Ch1 289.1 mVrms

Gain (5/15/2023 11:19:08.951 AM)

Ch1 1.438 dB

THD+N Ratio (5/15/2023 11:19:08.951 AM)

Ch1 ---- %

Frequency (5/15/2023 11:19:08.951 AM)

Ch1 ---- Hz

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Line Gain +10 600 Termination: Level and Gain +10

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/15/2023 11:19:11.747 AM)

Channel Lower Limit Value Upper Limit

Ch1 +8.500 dBu -8.661 dBu +11.500 dBu

Result: A FAILED

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Line Gain +10 200k Termination Level Hi : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
AG52 Generator Option: Installed
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

8.000 ohm

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00

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W(watts) (Input Power):



Port D (hex): 00

• Clocks

Output Rate: Track Output SR

Sync Out Level: 3.300 V
Sync Out Polarity: Normal
Timebase Reference: Internal
Jitter: Disabled

• Triggers

Source: Off

Input Logic Level: 3.300 V
Edge: Rising

Line Gain +10 200k Termination Level Hi: Verify Connections

Waveform: Sine

Generator Level: -20.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/15/2023 11:19:17.960 AM)

Ch1 299.3 mVrms

Gain (5/15/2023 11:19:17.960 AM)

Ch1 11.741 dB

THD+N Ratio (5/15/2023 11:19:17.960 AM)

Ch1 ---- %

Frequency (5/15/2023 11:19:17.960 AM)

Ch1 ---- Hz

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#### Line Gain +10 200k Termination Level Hi: Noise Recorder (RMS) CW

Waveform: None
Low-pass Filter: 20 kHz
Weighting Filter: Signal Path

High-pass Filter: 20 Hz

Sweep Time: 0.00:00:03.000

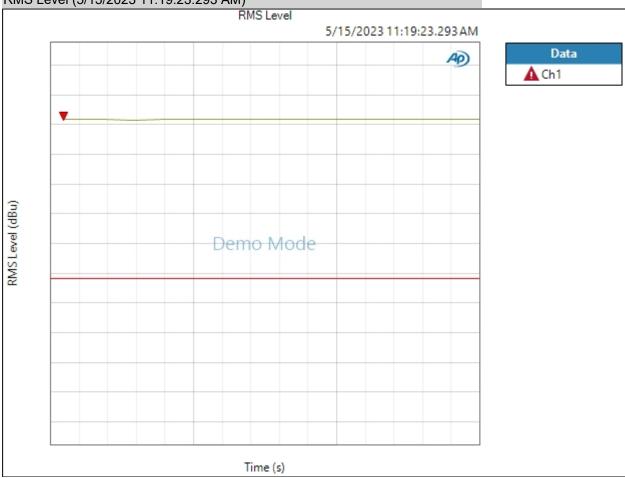
Reading Rate: 10/sec

Input Bandwidth: Use Signal Path

Record Acquisition: False

Measured 1 5/15/2023 11:19:23 AM

#### RMS Level (5/15/2023 11:19:23.293 AM)



Result: A FAILED

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Line Gain +10 200k Termination Level Low: Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
AG52 Generator Option: Installed
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm W(watts) (Input Power): 8.000 ohm

• DCX

DC Output 1: 0.000 V
DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00



Port D (hex): 00

• Clocks

Output Rate: Track Output SR

Sync Out Level: 3.300 V
Sync Out Polarity: Normal
Timebase Reference: Internal
Jitter: Disabled

• Triggers

Source: Off

Input Logic Level: 3.300 V
Edge: Rising

Line Gain +10 200k Termination Level Low: Verify Connections

Waveform: Sine

Generator Level: -20.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/15/2023 11:19:29.456 AM)

Ch1 292.7 mVrms

Gain (5/15/2023 11:19:29.456 AM)

Ch1 11.546 dB

THD+N Ratio (5/15/2023 11:19:29.456 AM)

Ch1 ---- %

Frequency (5/15/2023 11:19:29.456 AM)

Ch1 ---- Hz

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Line Gain +10 200k Termination Level Low: Noise Recorder (RMS) CCW

Waveform: None
Low-pass Filter: 20 kHz
Weighting Filter: Signal Path

High-pass Filter: 20 Hz

Sweep Time: 0.00:00:03.000

Reading Rate: 10/sec

Input Bandwidth: Use Signal Path

Record Acquisition: False

Measured 1 5/15/2023 11:19:35 AM

#### RMS Level (5/15/2023 11:19:35.406 AM)



Result: A FAILED

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Hi Z Gain -10 2.2M 200k Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 2

Source Impedance: 100 ohm
AG52 Generator Option: Installed
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm W(watts) (Input Power): 8.000 ohm

• DCX

DC Output 1: 0.000 V
DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00



Port D (hex): 00

• Clocks

Output Rate: Track Output SR

Sync Out Level: 3.300 V
Sync Out Polarity: Normal
Timebase Reference: Internal
Jitter: Disabled

• Triggers

Source: Off

Input Logic Level: 3.300 V Edge: Rising

Hi Z Gain -10 2.2M 200k Termination: Verify Connections

Waveform: Sine

Generator Level: -22.300 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/15/2023 11:19:40.474 AM)

Ch1 295.5 mVrms

Gain (5/15/2023 11:19:40.474 AM)

Ch1 13.931 dB

THD+N Ratio (5/15/2023 11:19:40.474 AM)

Ch1 ---- %

Frequency (5/15/2023 11:19:40.474 AM)

Ch1 ---- Hz

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Hi Z Gain -10 2.2M 200k Termination: Level and Gain 2.2M

Waveform: Sine

Generator Level: -22.300 dBu
DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/15/2023 11:19:43.770 AM)

Channel Lower Limit Value Upper Limit
Ch1 -2.000 dBu -8.590 dBu +2.000 dBu

Result: A FAILED

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Hi Z Gain -10 47k 200k Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 2

Source Impedance: 100 ohm
AG52 Generator Option: Installed
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

8.000 ohm

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

W(watts) (Input Power):

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00



Port D (hex): 00

• Clocks

Output Rate: Track Output SR

Sync Out Level: 3.300 V
Sync Out Polarity: Normal
Timebase Reference: Internal
Jitter: Disabled

• Triggers

Source: Off

Input Logic Level: 3.300 V Edge: Rising

Hi Z Gain -10 47k 200k Termination: Verify Connections

Waveform: Sine

Generator Level: -22.300 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/15/2023 11:19:48.877 AM)

Ch1 297.5 mVrms

Gain (5/15/2023 11:19:48.877 AM)

Ch1 13.987 dB

THD+N Ratio (5/15/2023 11:19:48.877 AM)

Ch1 ---- %

Frequency (5/15/2023 11:19:48.877 AM)

Ch1 ---- Hz

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Hi Z Gain -10 47k 200k Termination: Level and Gain 47K

Waveform: Sine

Generator Level: -22.300 dBu
DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/15/2023 11:19:51.580 AM)

Channel Lower Limit Value Upper Limit
Ch1 -8.000 dBu -8.637 dBu -4.000 dBu

Result: A FAILED

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Dummy Signal Path For Report : Signal Path Setup

Output Connector: Analog Unbalanced

Channels: 2

Source Impedance: 50 ohm
AG52 Generator Option: Installed
Output EQ: None

Input Connector: Analog Unbalanced

Channels: 2

Termination: 100 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL 600.0 ohm dBm (Input Power): W(watts) (Input Power): 8.000 ohm

• DCX

DC Output 1: 0.000 V DC Output 1: Off 0.000 V DC Output 2: DC Output 2: Off Port A (hex): 00 Port B (hex): 00 00 Port C (hex): 00 Port D (hex):



• Clocks

Output Rate: Track Output SR

Sync Out Level: 3.300 V
Sync Out Polarity: Normal
Timebase Reference: Internal
Jitter: Disabled

• Triggers

Source: Off

Input Logic Level: 3.300 V Edge: Rising

Dummy Signal Path For Report : Verify Connections

Waveform: Sine

Generator Level: 100.0 mVrms

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/15/2023 11:19:55.455 AM)

Ch1 290.0 mVrms Ch2 284.0 mVrms

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