

Sequence Report



Summary

Mic500 200k Termination

Signal Path Setup PASSED

Stepped Frequency Sweep MIC 500 FAILED

Mic 2k 200k termination

Signal Path Setup PASSED

Stepped Frequency Sweep MIC 2K FAILED

Mic 2k 15dB PAD 200k termination

Signal Path Setup PASSED

Stepped Frequency Sweep 15dB PAD FAILED

Line Gain -10 200kTermination

Signal Path Setup PASSED

Stepped Frequency Sweep -10 FAILED

Line Gain -10 600 Termination

Signal Path Setup PASSED

Level and Gain -10 PASSED

Line Gain +5 200kTermination

Signal Path Setup PASSED

Stepped Frequency Sweep +5 FAILED

Line Gain +5 600 Termination

Signal Path Setup PASSED

Level and Gain +5 FAILED

Line Gain -5 600 Termination

Signal Path Setup PASSED

Level and Gain -5 FAILED

Line Gain 0 600 Termination

Signal Path Setup PASSED

Level and Gain 0 FAILED

Line Gain +10 600 Termination

Signal Path Setup PASSED

Level and Gain +10 FAILED

Line Gain +10 200k Termination Level Hi

Signal Path Setup PASSED

Noise Recorder (RMS) CW FAILED

Line Gain +10 200k Termination Level Low

Signal Path Setup PASSED

Noise Recorder (RMS) CCW FAILED

Hi Z Gain -10 2.2M 200k Termination

Signal Path Setup	✓ PASSED
Level and Gain 2.2M	⚠ FAILED
Hi Z Gain -10 47k 200k Termination	
Signal Path Setup	✓ PASSED
Level and Gain 47K	⚠ FAILED
Dummy Signal Path For Report	
Signal Path Setup	✓ PASSED
Sequence Result:	
Sequence Result:	⚠ FAILED

Sequence Report



Mic500 200k Termination : Signal Path Setup

Output Connector:	Analog Balanced
Channels:	1
Source Impedance:	100 ohm
AG52 Generator Option:	Installed
Output EQ:	None
Input Connector:	Analog Balanced
Channels:	1
Channel:	Ch1
Termination:	200 kohm
Input Bandwidth:	AC (<10 Hz) - 90k (192 kHz SR)
Device Delay:	0.000 s
Input EQ:	None

• References

dBr G:	100.0 mVrms
dBm (Output Power):	600.0 ohm
W(watts) (Output Power):	8.000 ohm
Shared Frequency Reference:	1.00000 kHz
dBrA:	1.000 Vrms
dBrB:	1.000 Vrms
dBrA Offset:	0.000 dB
dBrB Offset:	0.000 dB
dB SPL1:	10.00 mVrms
dB SPL2:	10.00 mVrms
dB SPL1 Calibrator Level:	94.000 dB SPL
dB SPL2 Calibrator Level:	94.000 dB SPL
dBm (Input Power):	600.0 ohm
W(watts) (Input Power):	8.000 ohm

• DCX

DC Output 1:	0.000 V
DC Output 1:	Off
DC Output 2:	0.000 V
DC Output 2:	Off
Port A (hex):	00
Port B (hex):	00

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Port C (hex): 00
Port D (hex): 00
• Clocks
Output Rate: Track Output SR
Sync Out Level: 3.300 V
Sync Out Polarity: Normal
Timebase Reference: Internal
Jitter: Disabled
• Triggers
Source: Off
Input Logic Level: 3.300 V
Edge: Rising

Mic500 200k Termination : Verify Connections

Waveform: Sine
Generator Level: -42.300 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/15/2023 12:45:03.706 PM)

Ch1 282.0 mVrms

Gain (5/15/2023 12:45:03.706 PM)

Ch1 33.523 dB

THD+N Ratio (5/15/2023 12:45:03.706 PM)

Ch1 ---- %

Frequency (5/15/2023 12:45:03.706 PM)

Ch1 ---- Hz

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Mic500 200k Termination : Stepped Frequency Sweep MIC 500

Generator Level: -42.300 dBu

DC Offset: 0.000 V

EQ: None

Start Frequency: 20.0000 kHz

Stop Frequency: 20.0000 Hz

Step Type: Logarithmic

Number of Points: 10

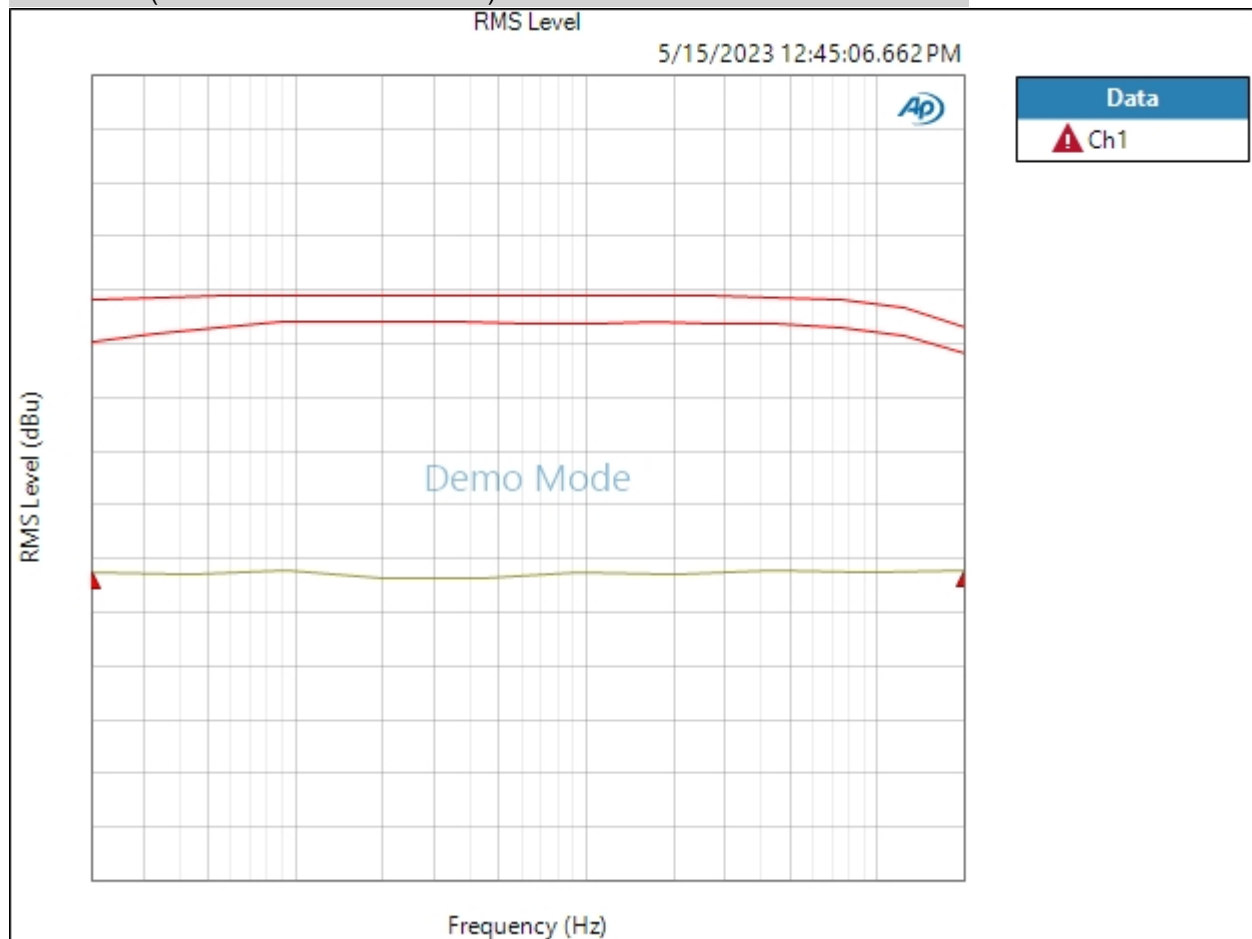
Weighting Filter: Signal Path

High-pass Filter: 20 Hz

Phase Ref Channel: Ch1

Measured 1 5/15/2023 12:45:06 PM

RMS Level (5/15/2023 12:45:06.662 PM)



Ch1 ▲ Failed Lower Limit

5/15/2023 12:47 PM

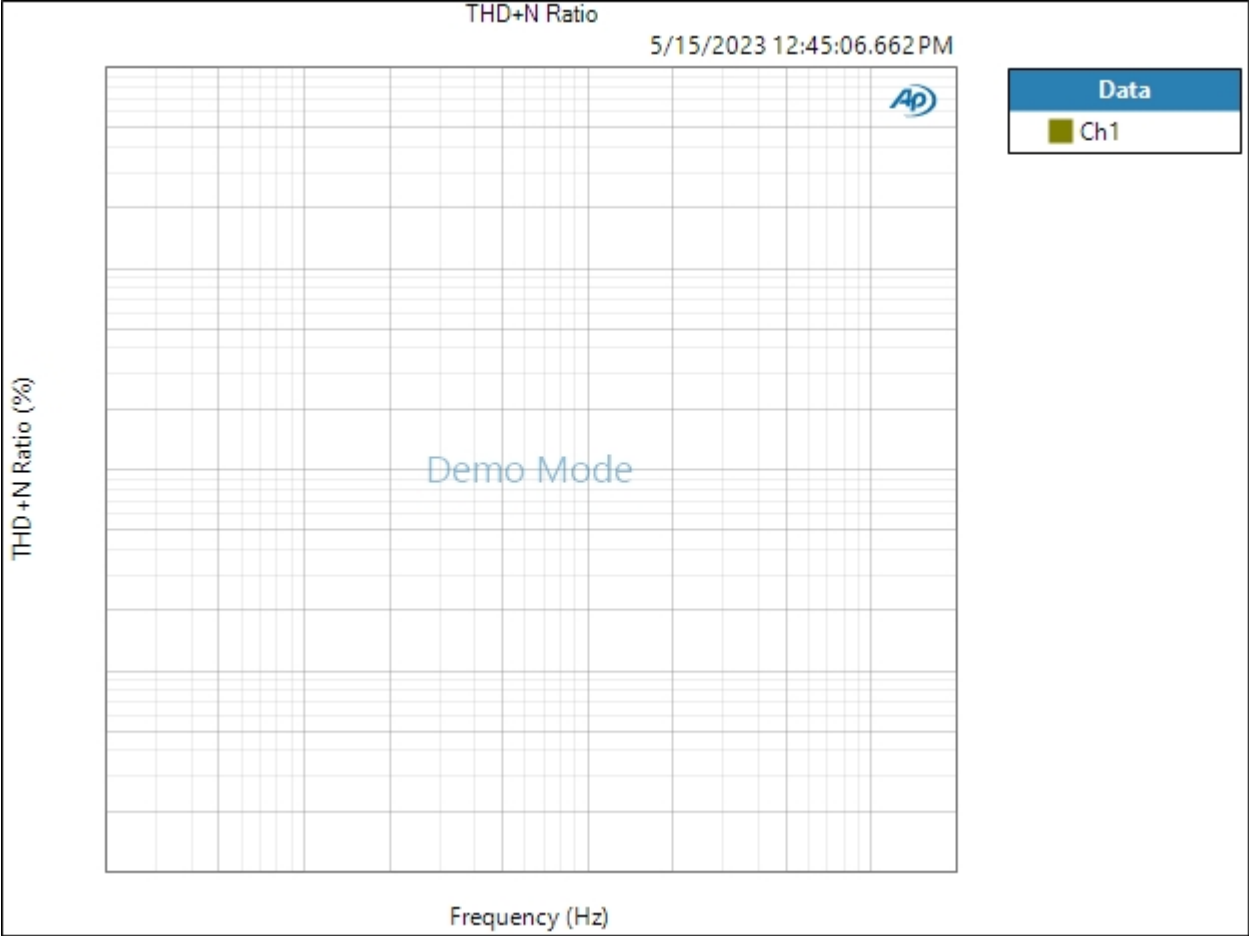
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Result: FAILED

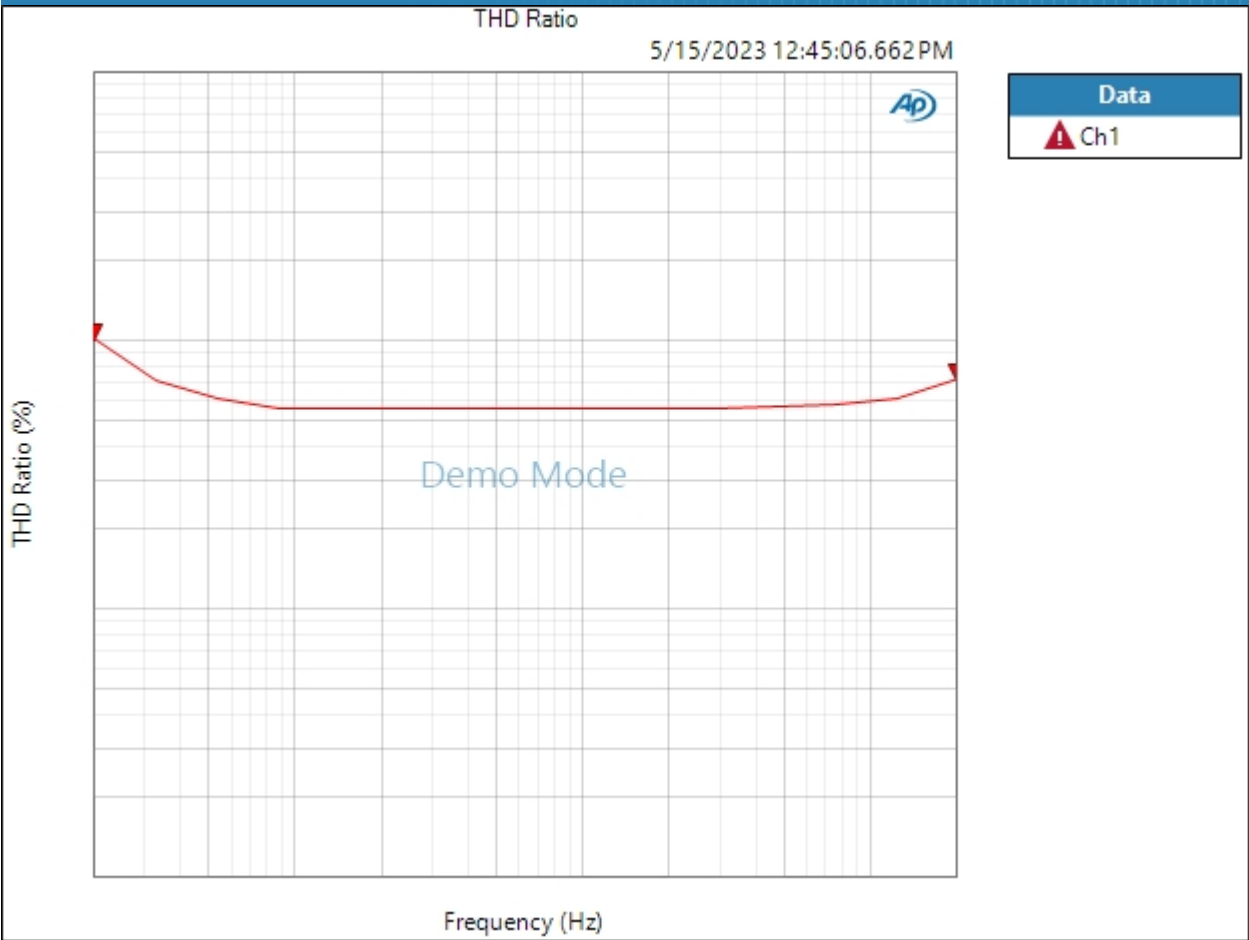
THD+N Ratio (5/15/2023 12:45:06.662 PM)



Result: PASSED

THD Ratio (5/15/2023 12:45:06.662 PM)

Sequence Report



Ch1 Failed Upper Limit

Result: FAILED

Sequence Report



Mic 2k 200k termination : Signal Path Setup

Output Connector:	Analog Balanced
Channels:	1
Source Impedance:	100 ohm
AG52 Generator Option:	Installed
Output EQ:	None
Input Connector:	Analog Balanced
Channels:	1
Channel:	Ch1
Termination:	200 kohm
Input Bandwidth:	AC (<10 Hz) - 90k (192 kHz SR)
Device Delay:	0.000 s
Input EQ:	None

• References

dBr G:	100.0 mVrms
dBm (Output Power):	600.0 ohm
W(watts) (Output Power):	8.000 ohm
Shared Frequency Reference:	1.00000 kHz
dBrA:	1.000 Vrms
dBrB:	1.000 Vrms
dBrA Offset:	0.000 dB
dBrB Offset:	0.000 dB
dB SPL1:	10.00 mVrms
dB SPL2:	10.00 mVrms
dB SPL1 Calibrator Level:	94.000 dB SPL
dB SPL2 Calibrator Level:	94.000 dB SPL
dBm (Input Power):	600.0 ohm
W(watts) (Input Power):	8.000 ohm

• DCX

DC Output 1:	0.000 V
DC Output 1:	Off
DC Output 2:	0.000 V
DC Output 2:	Off
Port A (hex):	00
Port B (hex):	00
Port C (hex):	00

5/15/2023 12:47 PM

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Port D (hex): 00

- Clocks

Output Rate: Track Output SR

Sync Out Level: 3.300 V

Sync Out Polarity: Normal

Timebase Reference: Internal

Jitter: Disabled

- Triggers

Source: Off

Input Logic Level: 3.300 V

Edge: Rising

Mic 2k 200k termination : Verify Connections

Waveform: Sine

Generator Level: -42.300 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/15/2023 12:45:11.412 PM)

Ch1 282.8 mVrms

Gain (5/15/2023 12:45:11.412 PM)

Ch1 33.547 dB

THD+N Ratio (5/15/2023 12:45:11.412 PM)

Ch1 ---- %

Frequency (5/15/2023 12:45:11.412 PM)

Ch1 ---- Hz

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Mic 2k 200k termination : Stepped Frequency Sweep MIC 2K

Generator Level: -42.300 dBu

DC Offset: 0.000 V

EQ: None

Start Frequency: 20.0000 kHz

Stop Frequency: 20.0000 Hz

Step Type: Logarithmic

Number of Points: 10

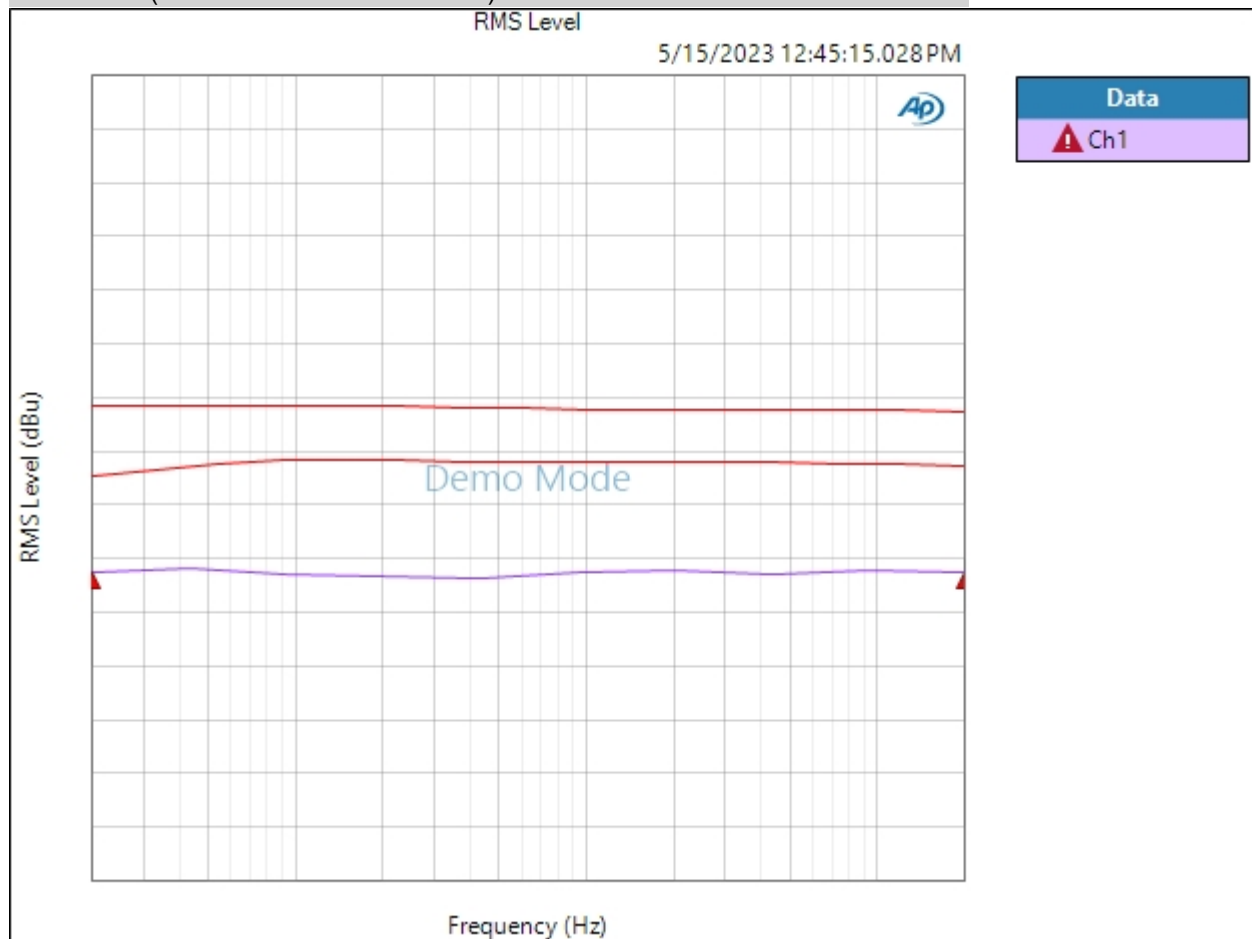
Weighting Filter: Signal Path

High-pass Filter: 20 Hz

Phase Ref Channel: Ch1

Measured 1 5/15/2023 12:45:15 PM

RMS Level (5/15/2023 12:45:15.028 PM)



Ch1 Failed Lower Limit

5/15/2023 12:47 PM

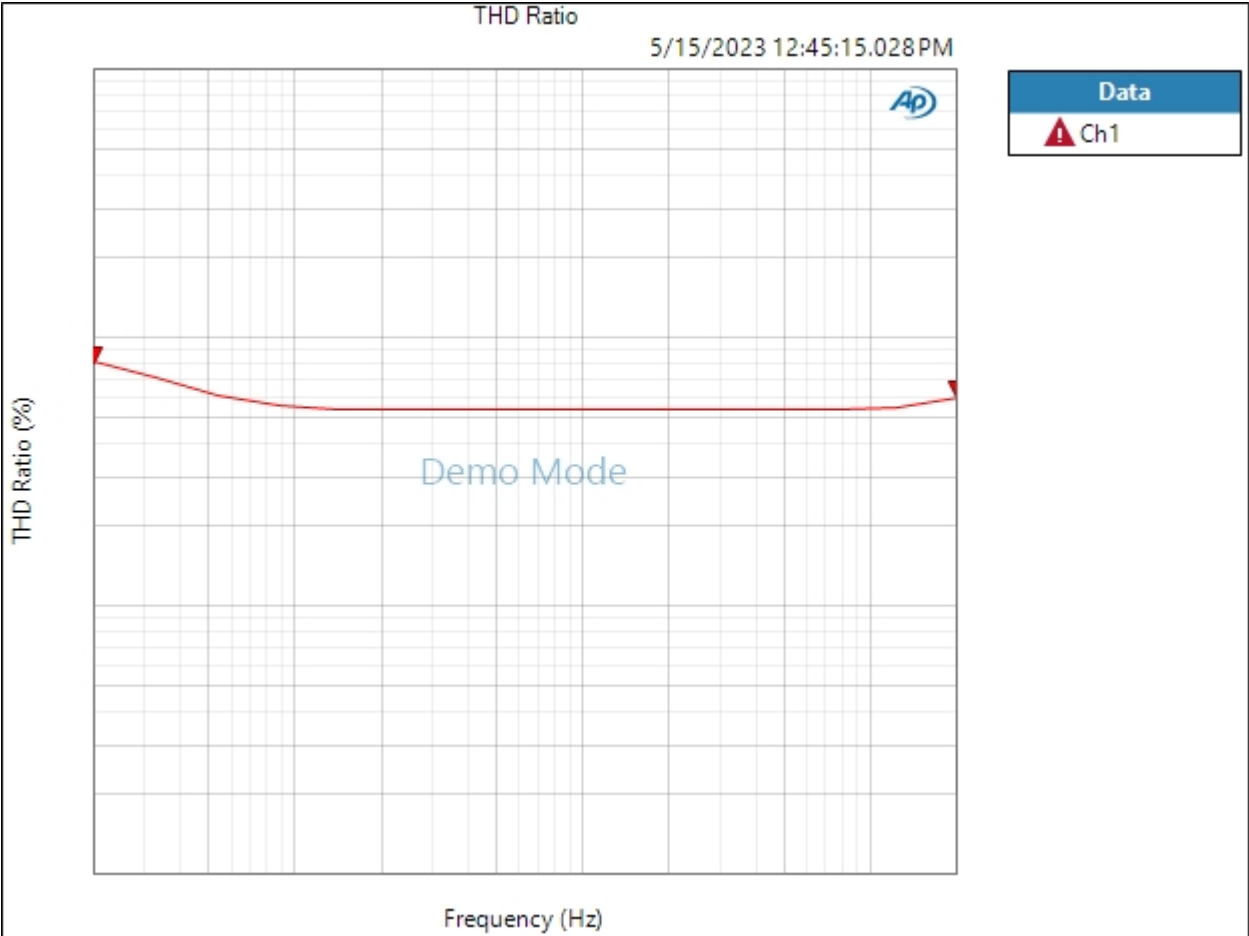
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Result: FAILED

THD Ratio (5/15/2023 12:45:15.028 PM)



Ch1 Failed Upper Limit

Result: FAILED

Sequence Report



Mic 2k 15dB PAD 200k termination : Signal Path Setup

Output Connector:	Analog Balanced
Channels:	1
Source Impedance:	100 ohm
AG52 Generator Option:	Installed
Output EQ:	None
Input Connector:	Analog Balanced
Channels:	1
Channel:	Ch1
Termination:	200 kohm
Input Bandwidth:	AC (<10 Hz) - 90k (192 kHz SR)
Device Delay:	0.000 s
Input EQ:	None

• References

dBr G:	100.0 mVrms
dBm (Output Power):	600.0 ohm
W(watts) (Output Power):	8.000 ohm
Shared Frequency Reference:	1.00000 kHz
dBra:	1.000 Vrms
dBrB:	1.000 Vrms
dBra Offset:	0.000 dB
dBrB Offset:	0.000 dB
dB SPL1:	10.00 mVrms
dB SPL2:	10.00 mVrms
dB SPL1 Calibrator Level:	94.000 dB SPL
dB SPL2 Calibrator Level:	94.000 dB SPL
dBm (Input Power):	600.0 ohm
W(watts) (Input Power):	8.000 ohm

• DCX

DC Output 1:	0.000 V
DC Output 1:	Off
DC Output 2:	0.000 V
DC Output 2:	Off
Port A (hex):	00
Port B (hex):	00
Port C (hex):	00

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Port D (hex): 00

- Clocks

Output Rate: Track Output SR

Sync Out Level: 3.300 V

Sync Out Polarity: Normal

Timebase Reference: Internal

Jitter: Disabled

- Triggers

Source: Off

Input Logic Level: 3.300 V

Edge: Rising

Mic 2k 15dB PAD 200k termination : Verify Connections

Waveform: Sine

Generator Level: -42.300 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/15/2023 12:45:19.964 PM)

Ch1 288.7 mVrms

Gain (5/15/2023 12:45:19.964 PM)

Ch1 33.728 dB

THD+N Ratio (5/15/2023 12:45:19.964 PM)

Ch1 ---- %

Frequency (5/15/2023 12:45:19.964 PM)

Ch1 ---- Hz

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Mic 2k 15dB PAD 200k termination : Stepped Frequency Sweep 15dB PAD

Generator Level: -42.000 dBu

DC Offset: 0.000 V

EQ: None

Start Frequency: 20.0000 kHz

Stop Frequency: 20.0000 Hz

Step Type: Logarithmic

Number of Points: 10

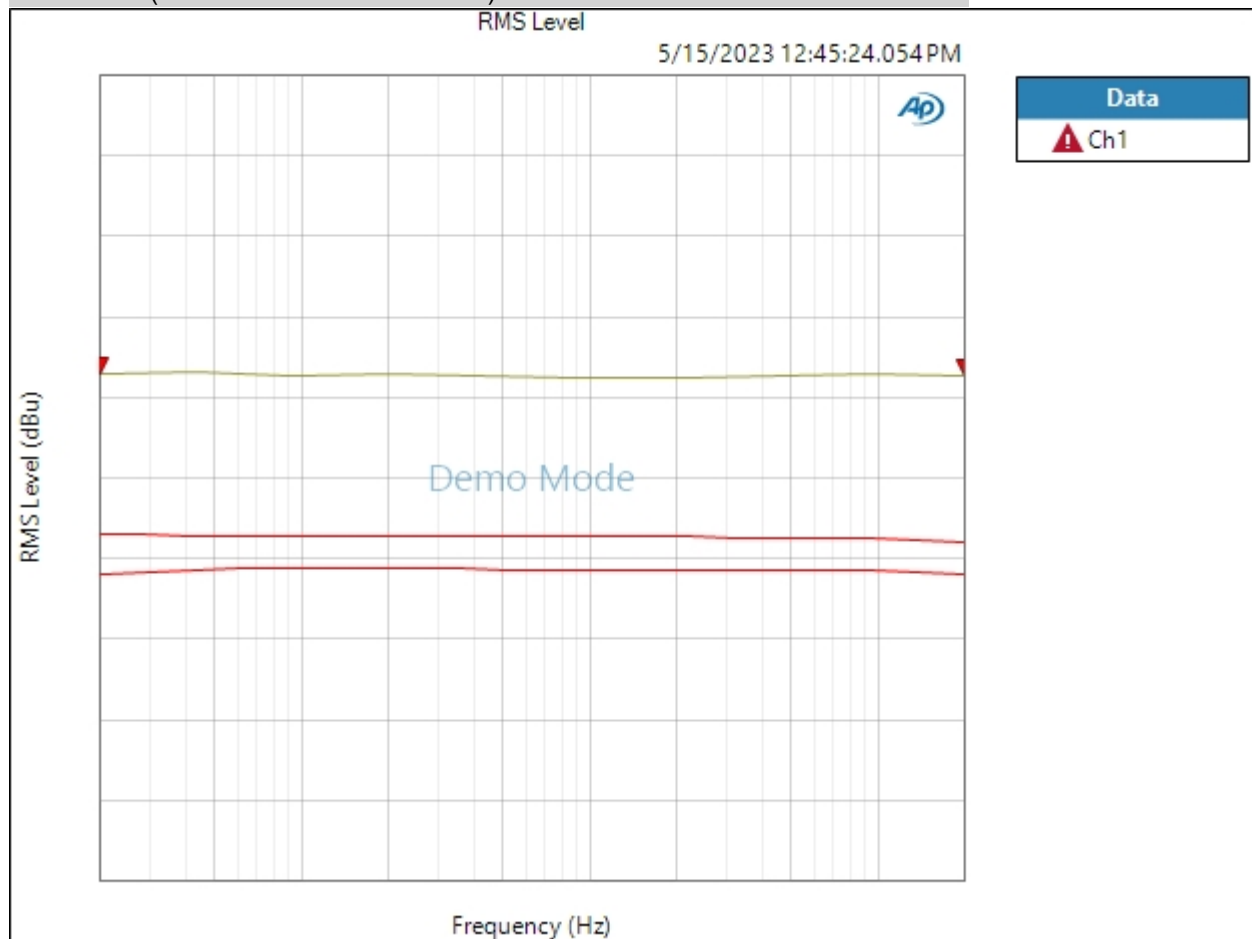
Weighting Filter: Signal Path

High-pass Filter: 20 Hz

Phase Ref Channel: Ch1

Measured 1 5/15/2023 12:45:24 PM

RMS Level (5/15/2023 12:45:24.054 PM)



Ch1 ▲ Failed Upper Limit

5/15/2023 12:47 PM

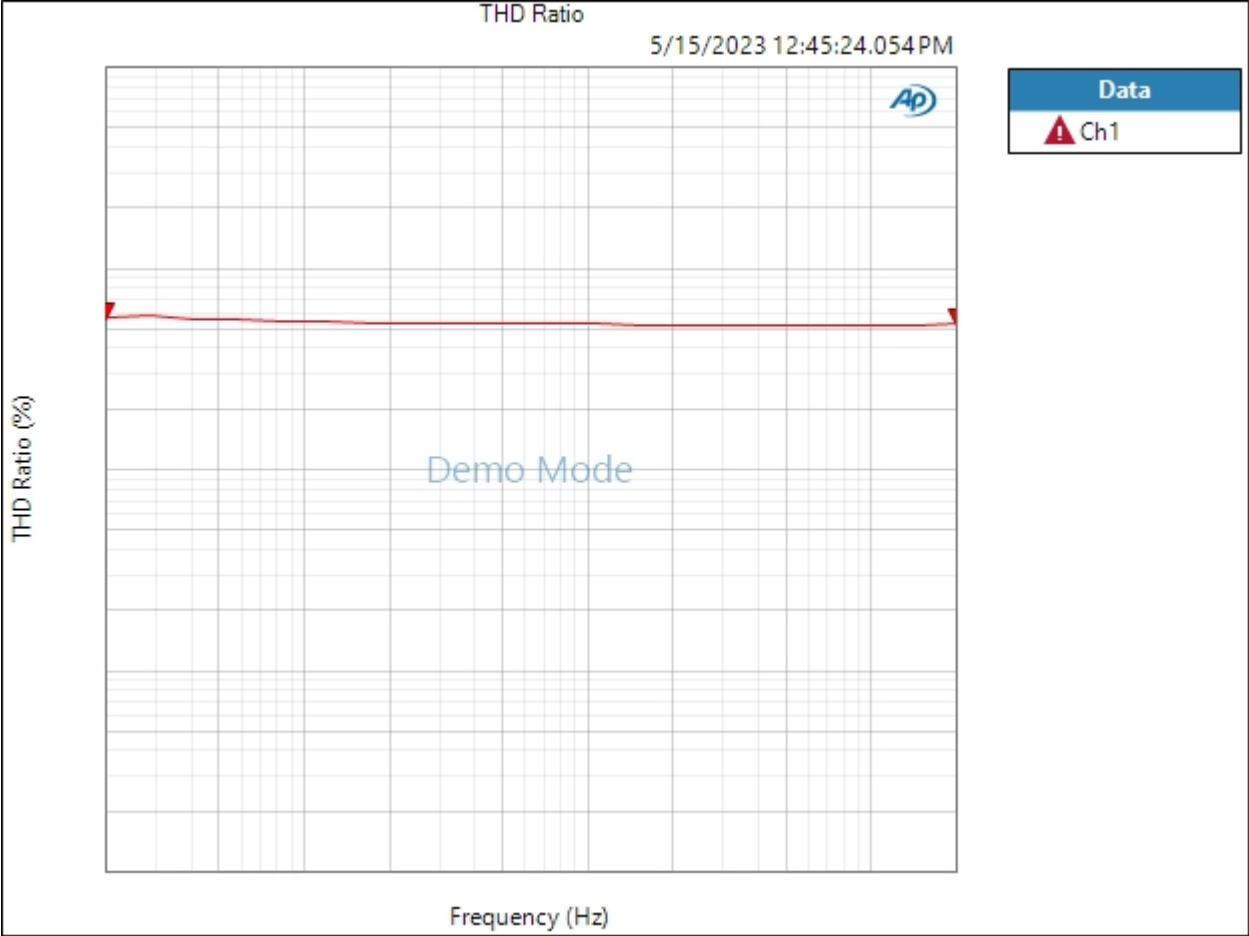
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Result: FAILED

THD Ratio (5/15/2023 12:45:24.054 PM)



Ch1 Failed Upper Limit

Result: FAILED

Sequence Report



Line Gain -10 200kTermination : Signal Path Setup

Output Connector:	Analog Balanced
Channels:	1
Source Impedance:	100 ohm
AG52 Generator Option:	Installed
Output EQ:	None
Input Connector:	Analog Balanced
Channels:	1
Channel:	Ch1
Termination:	200 kohm
Input Bandwidth:	AC (<10 Hz) - 90k (192 kHz SR)
Device Delay:	0.000 s
Input EQ:	None

• References

dBr G:	100.0 mVrms
dBm (Output Power):	600.0 ohm
W(watts) (Output Power):	8.000 ohm
Shared Frequency Reference:	1.00000 kHz
dBra:	1.000 Vrms
dBrB:	1.000 Vrms
dBrA Offset:	0.000 dB
dBrB Offset:	0.000 dB
dB SPL1:	10.00 mVrms
dB SPL2:	10.00 mVrms
dB SPL1 Calibrator Level:	94.000 dB SPL
dB SPL2 Calibrator Level:	94.000 dB SPL
dBm (Input Power):	600.0 ohm
W(watts) (Input Power):	8.000 ohm

• DCX

DC Output 1:	0.000 V
DC Output 1:	Off
DC Output 2:	0.000 V
DC Output 2:	Off
Port A (hex):	00
Port B (hex):	00
Port C (hex):	00

5/15/2023 12:47 PM

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Port D (hex): 00

- Clocks

Output Rate: Track Output SR

Sync Out Level: 3.300 V

Sync Out Polarity: Normal

Timebase Reference: Internal

Jitter: Disabled

- Triggers

Source: Off

Input Logic Level: 3.300 V

Edge: Rising

Line Gain -10 200kTermination : Verify Connections

Waveform: Sine

Generator Level: 0.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/15/2023 12:45:29.025 PM)

Ch1 294.0 mVrms

Gain (5/15/2023 12:45:29.025 PM)

Ch1 -8.416 dB

THD+N Ratio (5/15/2023 12:45:29.025 PM)

Ch1 ---- %

Frequency (5/15/2023 12:45:29.025 PM)

Ch1 ---- Hz

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Line Gain -10 200kTermination : Stepped Frequency Sweep -10

Generator Level: 0.000 dBu

DC Offset: 0.000 V

EQ: None

Start Frequency: 20.0000 kHz

Stop Frequency: 20.0000 Hz

Step Type: Logarithmic

Number of Points: 15

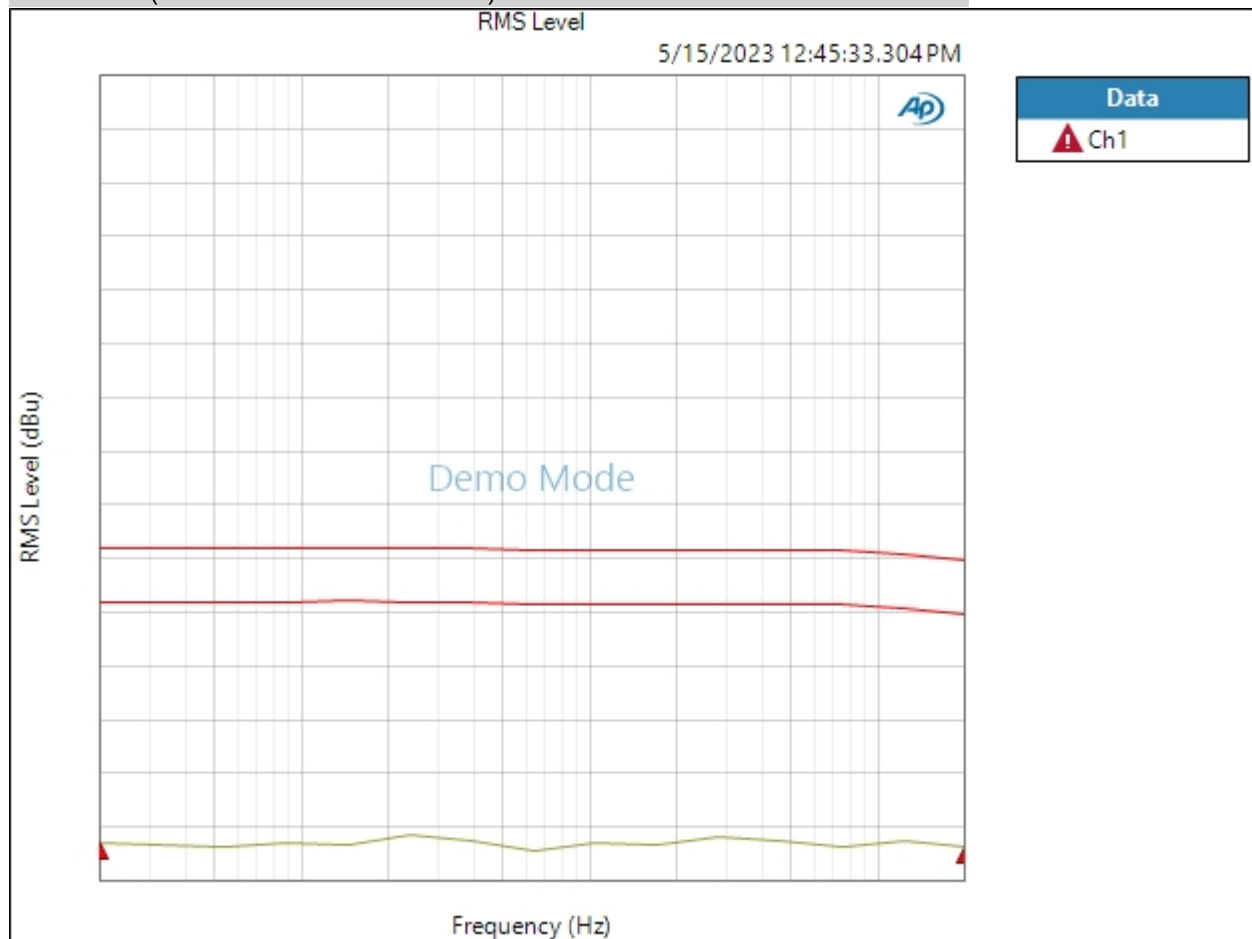
Weighting Filter: Signal Path

High-pass Filter: 20 Hz

Phase Ref Channel: Ch1

Measured 1 5/15/2023 12:45:33 PM

RMS Level (5/15/2023 12:45:33.304 PM)



Ch1 ▲ Failed Lower Limit

5/15/2023 12:47 PM

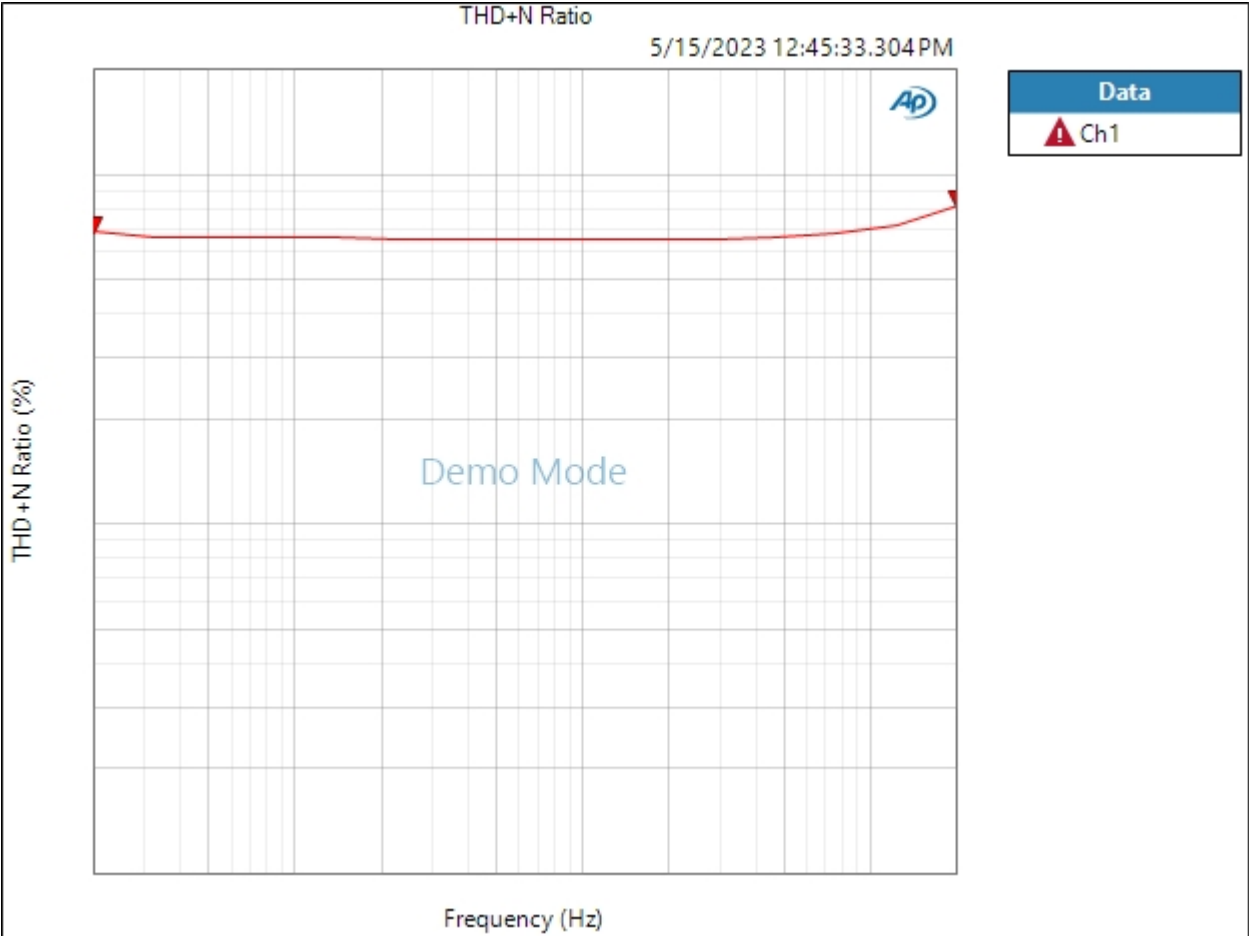
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Result: FAILED

THD+N Ratio (5/15/2023 12:45:33.304 PM)



Ch1 Failed Upper Limit

Result: FAILED

Sequence Report



Line Gain -10 600 Termination : Signal Path Setup

Output Connector:	Analog Balanced
Channels:	1
Source Impedance:	100 ohm
AG52 Generator Option:	Installed
Output EQ:	None
Input Connector:	Analog Balanced
Channels:	1
Channel:	Ch1
Termination:	600 ohm
Input Bandwidth:	AC (<10 Hz) - 90k (192 kHz SR)
Device Delay:	0.000 s
Input EQ:	None

• References

dBr G:	100.0 mVrms
dBm (Output Power):	600.0 ohm
W(watts) (Output Power):	8.000 ohm
Shared Frequency Reference:	1.00000 kHz
dBrA:	1.000 Vrms
dBrB:	1.000 Vrms
dBrA Offset:	0.000 dB
dBrB Offset:	0.000 dB
dB SPL1:	10.00 mVrms
dB SPL2:	10.00 mVrms
dB SPL1 Calibrator Level:	94.000 dB SPL
dB SPL2 Calibrator Level:	94.000 dB SPL
dBm (Input Power):	600.0 ohm
W(watts) (Input Power):	8.000 ohm

• DCX

DC Output 1:	0.000 V
DC Output 1:	Off
DC Output 2:	0.000 V
DC Output 2:	Off
Port A (hex):	00
Port B (hex):	00
Port C (hex):	00

5/15/2023 12:47 PM

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Port D (hex): 00

- Clocks

Output Rate: Track Output SR

Sync Out Level: 3.300 V

Sync Out Polarity: Normal

Timebase Reference: Internal

Jitter: Disabled

- Triggers

Source: Off

Input Logic Level: 3.300 V

Edge: Rising

Line Gain -10 600 Termination : Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/15/2023 12:45:38.318 PM)

Ch1 283.2 mVrms

Gain (5/15/2023 12:45:38.318 PM)

Ch1 1.261 dB

THD+N Ratio (5/15/2023 12:45:38.318 PM)

Ch1 ---- %

Frequency (5/15/2023 12:45:38.318 PM)

Ch1 ---- Hz

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Line Gain -10 600 Termination : Level and Gain -10

Waveform: Sine

Generator Level: -10.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/15/2023 12:45:40.932 PM)

Channel	Lower Limit	Value	Upper Limit	
Ch1	-11.500 dBu	-8.695 dBu	-8.500 dBu	✓

Result: ✓ PASSED

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Line Gain +5 200kTermination : Signal Path Setup

Output Connector: Analog Balanced
Channels: 1
Source Impedance: 100 ohm
AG52 Generator Option: Installed
Output EQ: None
Input Connector: Analog Balanced
Channels: 1
Channel: Ch1
Termination: 200 kohm
Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)
Device Delay: 0.000 s
Input EQ: None

• References

dBr G: 100.0 mVrms
dBm (Output Power): 600.0 ohm
W(watts) (Output Power): 8.000 ohm
Shared Frequency Reference: 1.00000 kHz
dBrA: 1.000 Vrms
dBrB: 1.000 Vrms
dBrA Offset: 0.000 dB
dBrB Offset: 0.000 dB
dBSPL1: 10.00 mVrms
dBSPL2: 10.00 mVrms
dBSPL1 Calibrator Level: 94.000 dB SPL
dBSPL2 Calibrator Level: 94.000 dB SPL
dBm (Input Power): 600.0 ohm
W(watts) (Input Power): 8.000 ohm

• DCX

DC Output 1: 0.000 V
DC Output 1: Off
DC Output 2: 0.000 V
DC Output 2: Off
Port A (hex): 00
Port B (hex): 00
Port C (hex): 00

5/15/2023 12:47 PM

Sequence Report



Port D (hex): 00

- Clocks

Output Rate: Track Output SR

Sync Out Level: 3.300 V

Sync Out Polarity: Normal

Timebase Reference: Internal

Jitter: Disabled

- Triggers

Source: Off

Input Logic Level: 3.300 V

Edge: Rising

Line Gain +5 200kTermination : Verify Connections

Waveform: Sine

Generator Level: 0.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/15/2023 12:45:46.039 PM)

Ch1 280.2 mVrms

Gain (5/15/2023 12:45:46.039 PM)

Ch1 -8.831 dB

THD+N Ratio (5/15/2023 12:45:46.039 PM)

Ch1 ---- %

Frequency (5/15/2023 12:45:46.039 PM)

Ch1 ---- Hz

Sequence Report



Line Gain +5 200kTermination : Stepped Frequency Sweep +5

Generator Level: 0.000 dBu

DC Offset: 0.000 V

EQ: None

Start Frequency: 20.0000 kHz

Stop Frequency: 20.0000 Hz

Step Type: Logarithmic

Number of Points: 15

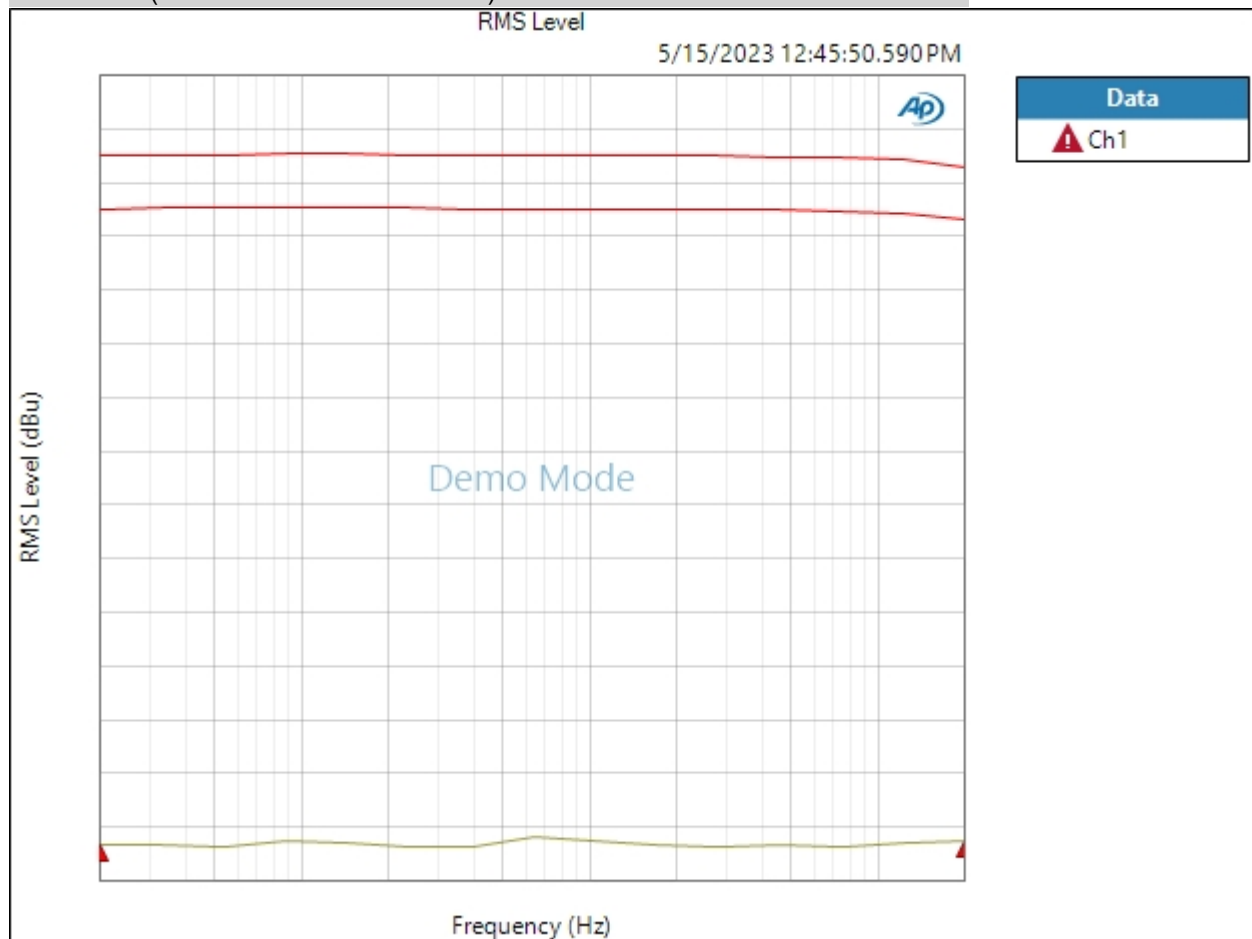
Weighting Filter: Signal Path

High-pass Filter: 20 Hz

Phase Ref Channel: Ch1

Measured 1 5/15/2023 12:45:50 PM

RMS Level (5/15/2023 12:45:50.590 PM)



Ch1 ▲ Failed Lower Limit

5/15/2023 12:47 PM

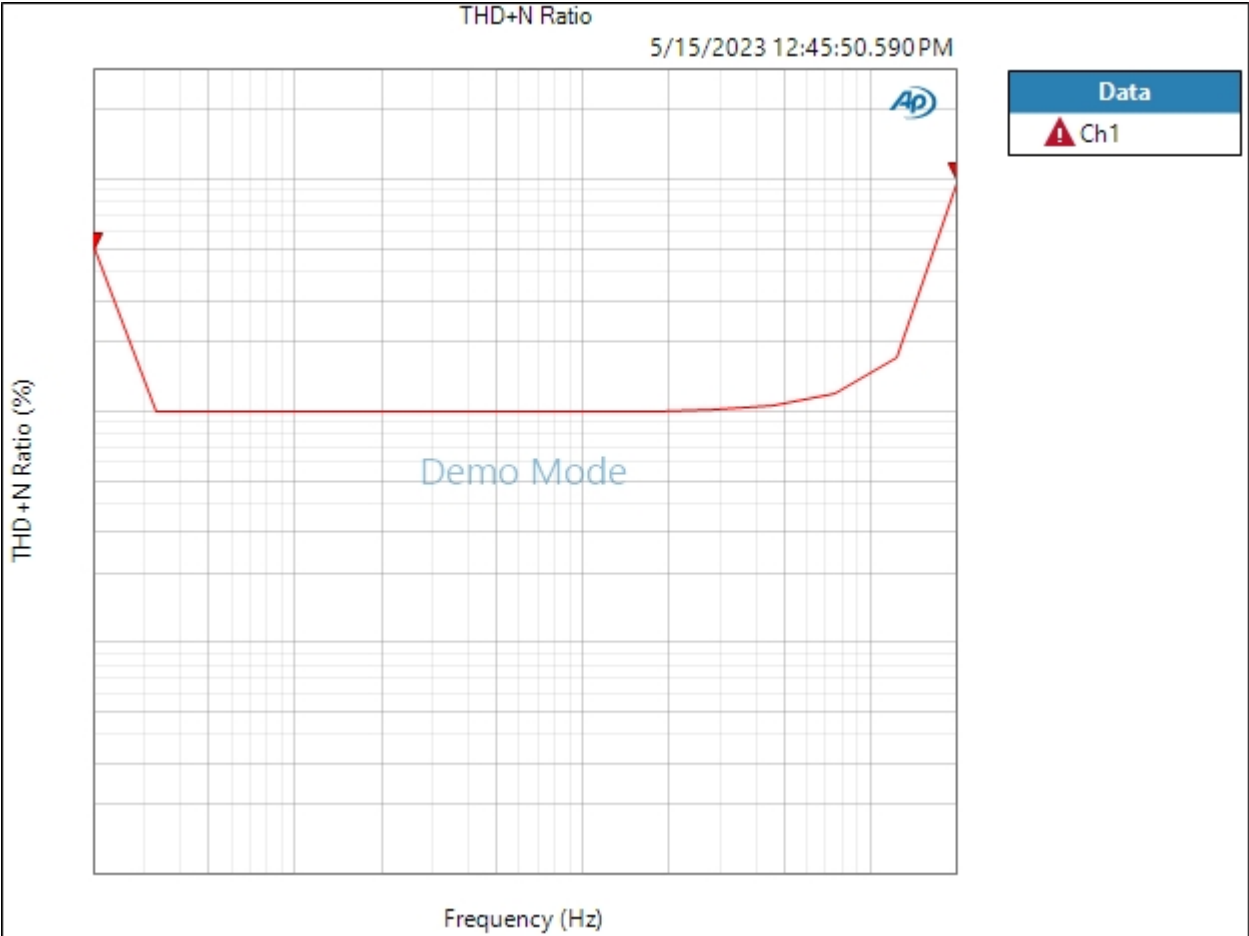
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Result: ▲ FAILED

THD+N Ratio (5/15/2023 12:45:50.590 PM)



Ch1 ▲ Failed Upper Limit

Result: ▲ FAILED

Sequence Report



Line Gain +5 600 Termination : Signal Path Setup

Output Connector:	Analog Balanced
Channels:	1
Source Impedance:	100 ohm
AG52 Generator Option:	Installed
Output EQ:	None
Input Connector:	Analog Balanced
Channels:	1
Channel:	Ch1
Termination:	600 ohm
Input Bandwidth:	AC (<10 Hz) - 90k (192 kHz SR)
Device Delay:	0.000 s
Input EQ:	None

• References

dBr G:	100.0 mVrms
dBm (Output Power):	600.0 ohm
W(watts) (Output Power):	8.000 ohm
Shared Frequency Reference:	1.00000 kHz
dBrA:	1.000 Vrms
dBrB:	1.000 Vrms
dBrA Offset:	0.000 dB
dBrB Offset:	0.000 dB
dB SPL1:	10.00 mVrms
dB SPL2:	10.00 mVrms
dB SPL1 Calibrator Level:	94.000 dB SPL
dB SPL2 Calibrator Level:	94.000 dB SPL
dBm (Input Power):	600.0 ohm
W(watts) (Input Power):	8.000 ohm

• DCX

DC Output 1:	0.000 V
DC Output 1:	Off
DC Output 2:	0.000 V
DC Output 2:	Off
Port A (hex):	00
Port B (hex):	00
Port C (hex):	00

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Port D (hex): 00

- Clocks

Output Rate: Track Output SR

Sync Out Level: 3.300 V

Sync Out Polarity: Normal

Timebase Reference: Internal

Jitter: Disabled

- Triggers

Source: Off

Input Logic Level: 3.300 V

Edge: Rising

Line Gain +5 600 Termination : Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/15/2023 12:45:55.704 PM)

Ch1 284.3 mVrms

Gain (5/15/2023 12:45:55.704 PM)

Ch1 1.295 dB

THD+N Ratio (5/15/2023 12:45:55.704 PM)

Ch1 ---- %

Frequency (5/15/2023 12:45:55.704 PM)

Ch1 ---- Hz

Sequence Report



Line Gain +5 600 Termination : Level and Gain +5


Waveform: Sine

Generator Level: -10.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/15/2023 12:45:58.303 PM)

Channel	Lower Limit	Value	Upper Limit	
Ch1	+3.500 dBu	-8.583 dBu	+6.500 dBu	

Result:  FAILED

Sequence Report



Line Gain -5 600 Termination : Signal Path Setup

Output Connector:	Analog Balanced
Channels:	1
Source Impedance:	100 ohm
AG52 Generator Option:	Installed
Output EQ:	None
Input Connector:	Analog Balanced
Channels:	1
Channel:	Ch1
Termination:	600 ohm
Input Bandwidth:	AC (<10 Hz) - 90k (192 kHz SR)
Device Delay:	0.000 s
Input EQ:	None

• References

dBr G:	100.0 mVrms
dBm (Output Power):	600.0 ohm
W(watts) (Output Power):	8.000 ohm
Shared Frequency Reference:	1.00000 kHz
dBrA:	1.000 Vrms
dBrB:	1.000 Vrms
dBrA Offset:	0.000 dB
dBrB Offset:	0.000 dB
dB SPL1:	10.00 mVrms
dB SPL2:	10.00 mVrms
dB SPL1 Calibrator Level:	94.000 dB SPL
dB SPL2 Calibrator Level:	94.000 dB SPL
dBm (Input Power):	600.0 ohm
W(watts) (Input Power):	8.000 ohm

• DCX

DC Output 1:	0.000 V
DC Output 1:	Off
DC Output 2:	0.000 V
DC Output 2:	Off
Port A (hex):	00
Port B (hex):	00
Port C (hex):	00

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Port D (hex): 00

- Clocks

Output Rate: Track Output SR

Sync Out Level: 3.300 V

Sync Out Polarity: Normal

Timebase Reference: Internal

Jitter: Disabled

- Triggers

Source: Off

Input Logic Level: 3.300 V

Edge: Rising

Line Gain -5 600 Termination : Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/15/2023 12:46:03.382 PM)

Ch1 288.3 mVrms

Gain (5/15/2023 12:46:03.382 PM)

Ch1 1.416 dB

THD+N Ratio (5/15/2023 12:46:03.382 PM)

Ch1 ---- %

Frequency (5/15/2023 12:46:03.382 PM)

Ch1 ---- Hz

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Line Gain -5 600 Termination : Level and Gain -5


Waveform: Sine

Generator Level: -10.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/15/2023 12:46:05.977 PM)

Channel	Lower Limit	Value	Upper Limit	
Ch1	-6.500 dBu	-8.616 dBu	-3.500 dBu	

Result:  FAILED

Sequence Report



Line Gain 0 600 Termination : Signal Path Setup

Output Connector: Analog Balanced
Channels: 1
Source Impedance: 100 ohm
AG52 Generator Option: Installed
Output EQ: None
Input Connector: Analog Balanced
Channels: 1
Channel: Ch1
Termination: 600 ohm
Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)
Device Delay: 0.000 s
Input EQ: None

• References

dBr G: 100.0 mVrms
dBm (Output Power): 600.0 ohm
W(watts) (Output Power): 8.000 ohm
Shared Frequency Reference: 1.00000 kHz
dBrA: 1.000 Vrms
dBrB: 1.000 Vrms
dBrA Offset: 0.000 dB
dBrB Offset: 0.000 dB
dBSPL1: 10.00 mVrms
dBSPL2: 10.00 mVrms
dBSPL1 Calibrator Level: 94.000 dB SPL
dBSPL2 Calibrator Level: 94.000 dB SPL
dBm (Input Power): 600.0 ohm
W(watts) (Input Power): 8.000 ohm

• DCX

DC Output 1: 0.000 V
DC Output 1: Off
DC Output 2: 0.000 V
DC Output 2: Off
Port A (hex): 00
Port B (hex): 00
Port C (hex): 00

5/15/2023 12:47 PM

Sequence Report



Port D (hex): 00

- Clocks

Output Rate: Track Output SR

Sync Out Level: 3.300 V

Sync Out Polarity: Normal

Timebase Reference: Internal

Jitter: Disabled

- Triggers

Source: Off

Input Logic Level: 3.300 V

Edge: Rising

Line Gain 0 600 Termination : Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/15/2023 12:46:11.295 PM)

Ch1 285.2 mVrms

Gain (5/15/2023 12:46:11.295 PM)

Ch1 1.322 dB

THD+N Ratio (5/15/2023 12:46:11.295 PM)

Ch1 ---- %

Frequency (5/15/2023 12:46:11.295 PM)

Ch1 ---- Hz

Sequence Report



Line Gain 0 600 Termination : Level and Gain 0


Waveform: Sine

Generator Level: -10.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/15/2023 12:46:13.894 PM)

Channel	Lower Limit	Value	Upper Limit	
Ch1	-1.500 dBu	-8.534 dBu	+1.500 dBu	

Result:  FAILED

Sequence Report



Line Gain +10 600 Termination : Signal Path Setup

Output Connector:	Analog Balanced
Channels:	1
Source Impedance:	100 ohm
AG52 Generator Option:	Installed
Output EQ:	None
Input Connector:	Analog Balanced
Channels:	1
Channel:	Ch1
Termination:	600 ohm
Input Bandwidth:	AC (<10 Hz) - 90k (192 kHz SR)
Device Delay:	0.000 s
Input EQ:	None

• References

dBr G:	100.0 mVrms
dBm (Output Power):	600.0 ohm
W(watts) (Output Power):	8.000 ohm
Shared Frequency Reference:	1.00000 kHz
dBrA:	1.000 Vrms
dBrB:	1.000 Vrms
dBrA Offset:	0.000 dB
dBrB Offset:	0.000 dB
dB SPL1:	10.00 mVrms
dB SPL2:	10.00 mVrms
dB SPL1 Calibrator Level:	94.000 dB SPL
dB SPL2 Calibrator Level:	94.000 dB SPL
dBm (Input Power):	600.0 ohm
W(watts) (Input Power):	8.000 ohm

• DCX

DC Output 1:	0.000 V
DC Output 1:	Off
DC Output 2:	0.000 V
DC Output 2:	Off
Port A (hex):	00
Port B (hex):	00
Port C (hex):	00

5/15/2023 12:47 PM

Sequence Report



Port D (hex): 00

- Clocks

Output Rate: Track Output SR

Sync Out Level: 3.300 V

Sync Out Polarity: Normal

Timebase Reference: Internal

Jitter: Disabled

- Triggers

Source: Off

Input Logic Level: 3.300 V

Edge: Rising

Line Gain +10 600 Termination : Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/15/2023 12:46:19.162 PM)

Ch1 297.5 mVrms

Gain (5/15/2023 12:46:19.162 PM)

Ch1 1.687 dB

THD+N Ratio (5/15/2023 12:46:19.162 PM)

Ch1 ---- %

Frequency (5/15/2023 12:46:19.162 PM)

Ch1 ---- Hz

Sequence Report



Line Gain +10 600 Termination : Level and Gain +10


Waveform: Sine

Generator Level: -10.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/15/2023 12:46:21.749 PM)

Channel	Lower Limit	Value	Upper Limit	
Ch1	+8.500 dBu	-8.506 dBu	+11.500 dBu	

Result:  FAILED

Sequence Report



Line Gain +10 200k Termination Level Hi : Signal Path Setup

Output Connector:	Analog Balanced
Channels:	1
Source Impedance:	100 ohm
AG52 Generator Option:	Installed
Output EQ:	None
Input Connector:	Analog Balanced
Channels:	1
Channel:	Ch1
Termination:	200 kohm
Input Bandwidth:	AC (<10 Hz) - 90k (192 kHz SR)
Device Delay:	0.000 s
Input EQ:	None

• References

dBr G:	100.0 mVrms
dBm (Output Power):	600.0 ohm
W(watts) (Output Power):	8.000 ohm
Shared Frequency Reference:	1.00000 kHz
dBrA:	1.000 Vrms
dBrB:	1.000 Vrms
dBrA Offset:	0.000 dB
dBrB Offset:	0.000 dB
dB SPL1:	10.00 mVrms
dB SPL2:	10.00 mVrms
dB SPL1 Calibrator Level:	94.000 dB SPL
dB SPL2 Calibrator Level:	94.000 dB SPL
dBm (Input Power):	600.0 ohm
W(watts) (Input Power):	8.000 ohm

• DCX

DC Output 1:	0.000 V
DC Output 1:	Off
DC Output 2:	0.000 V
DC Output 2:	Off
Port A (hex):	00
Port B (hex):	00
Port C (hex):	00

5/15/2023 12:47 PM

Sequence Report



Port D (hex): 00

- Clocks

Output Rate: Track Output SR

Sync Out Level: 3.300 V

Sync Out Polarity: Normal

Timebase Reference: Internal

Jitter: Disabled

- Triggers

Source: Off

Input Logic Level: 3.300 V

Edge: Rising

Line Gain +10 200k Termination Level Hi : Verify Connections

Waveform: Sine

Generator Level: -20.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/15/2023 12:46:26.992 PM)

Ch1 281.4 mVrms

Gain (5/15/2023 12:46:26.992 PM)

Ch1 11.204 dB

THD+N Ratio (5/15/2023 12:46:26.992 PM)

Ch1 ---- %

Frequency (5/15/2023 12:46:26.992 PM)

Ch1 ---- Hz

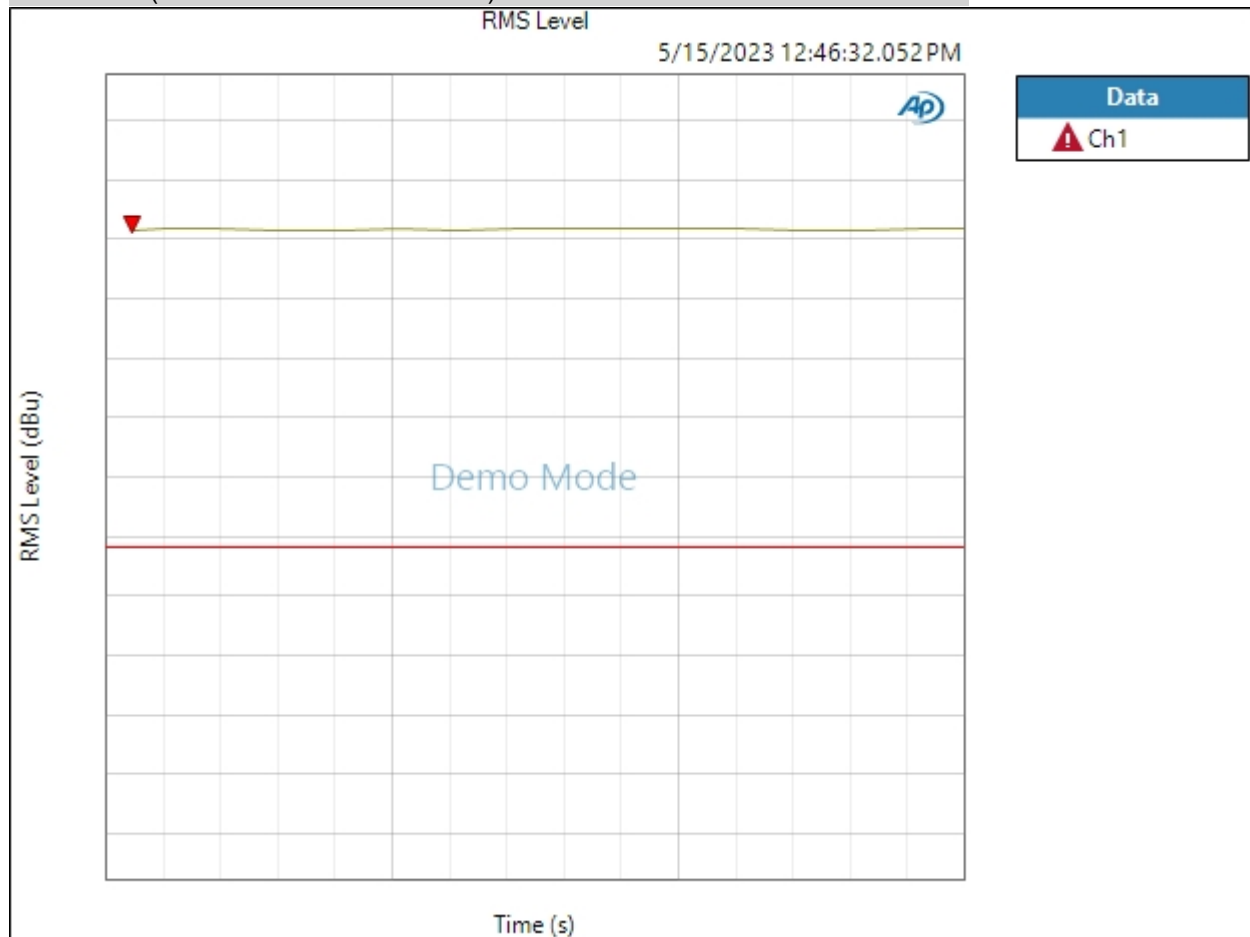
Sequence Report



Line Gain +10 200k Termination Level Hi : Noise Recorder (RMS) CW

Waveform: None
Low-pass Filter: 20 kHz
Weighting Filter: Signal Path
High-pass Filter: 20 Hz
Sweep Time: 0.00:00:03.000
Reading Rate: 10/sec
Input Bandwidth: Use Signal Path
Record Acquisition: False
Measured 1 5/15/2023 12:46:32 PM

RMS Level (5/15/2023 12:46:32.052 PM)



Ch1 Failed Upper Limit

Result: FAILED

5/15/2023 12:47 PM

Sequence Report



Line Gain +10 200k Termination Level Low : Signal Path Setup

Output Connector: Analog Balanced
Channels: 1
Source Impedance: 100 ohm
AG52 Generator Option: Installed
Output EQ: None
Input Connector: Analog Balanced
Channels: 1
Channel: Ch1
Termination: 200 kohm
Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)
Device Delay: 0.000 s
Input EQ: None

• References

dBr G: 100.0 mVrms
dBm (Output Power): 600.0 ohm
W(watts) (Output Power): 8.000 ohm
Shared Frequency Reference: 1.00000 kHz
dBrA: 1.000 Vrms
dBrB: 1.000 Vrms
dBrA Offset: 0.000 dB
dBrB Offset: 0.000 dB
dBSPL1: 10.00 mVrms
dBSPL2: 10.00 mVrms
dBSPL1 Calibrator Level: 94.000 dB SPL
dBSPL2 Calibrator Level: 94.000 dB SPL
dBm (Input Power): 600.0 ohm
W(watts) (Input Power): 8.000 ohm

• DCX

DC Output 1: 0.000 V
DC Output 1: Off
DC Output 2: 0.000 V
DC Output 2: Off
Port A (hex): 00
Port B (hex): 00
Port C (hex): 00

5/15/2023 12:47 PM

Sequence Report



Port D (hex): 00

- Clocks

Output Rate: Track Output SR

Sync Out Level: 3.300 V

Sync Out Polarity: Normal

Timebase Reference: Internal

Jitter: Disabled

- Triggers

Source: Off

Input Logic Level: 3.300 V

Edge: Rising

Line Gain +10 200k Termination Level Low : Verify Connections

Waveform: Sine

Generator Level: -20.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/15/2023 12:46:37.347 PM)

Ch1 289.5 mVrms

Gain (5/15/2023 12:46:37.347 PM)

Ch1 11.452 dB

THD+N Ratio (5/15/2023 12:46:37.347 PM)

Ch1 ---- %

Frequency (5/15/2023 12:46:37.347 PM)

Ch1 ---- Hz

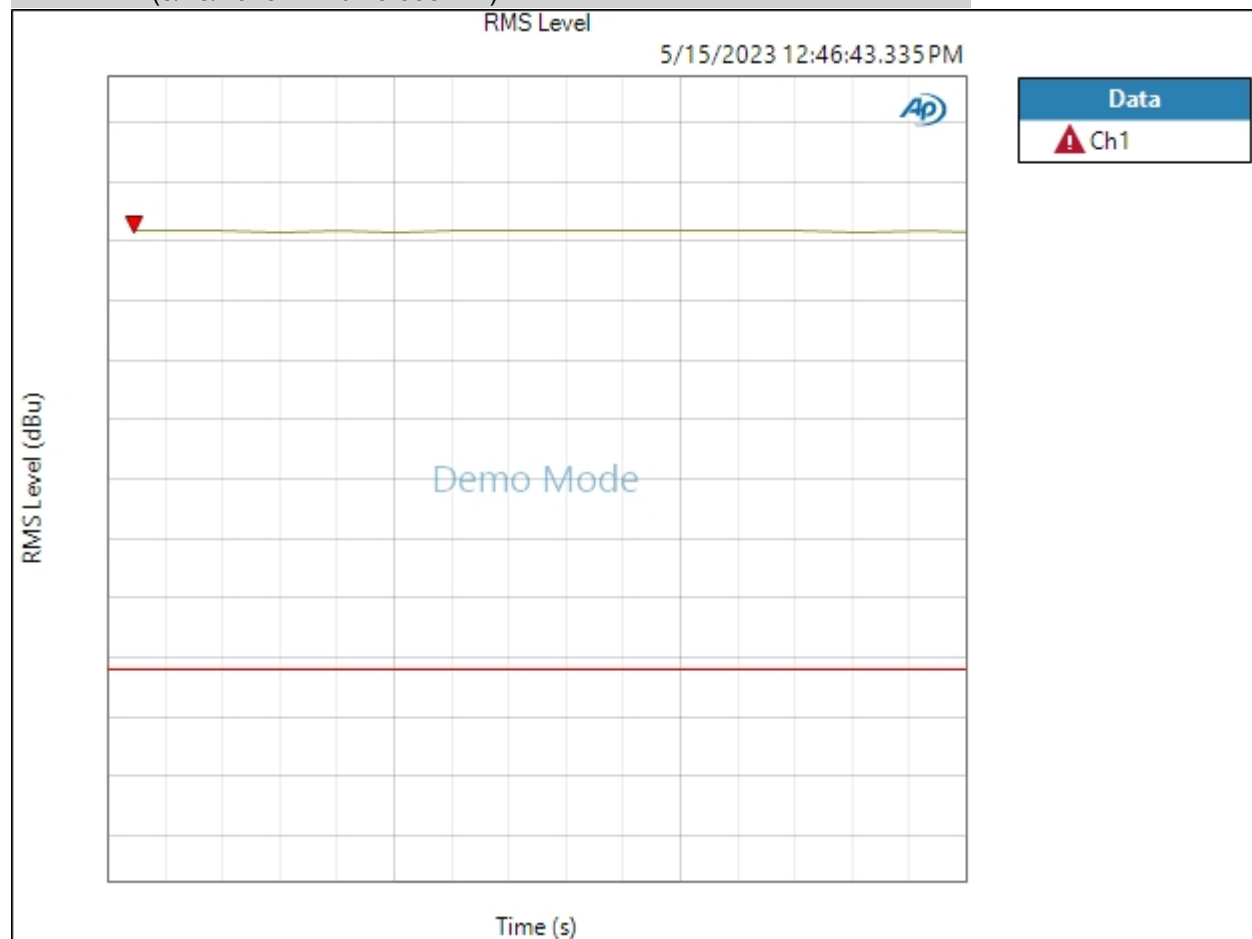
Sequence Report



Line Gain +10 200k Termination Level Low : Noise Recorder (RMS) CCW

Waveform: None
Low-pass Filter: 20 kHz
Weighting Filter: Signal Path
High-pass Filter: 20 Hz
Sweep Time: 0.00:00:03.000
Reading Rate: 10/sec
Input Bandwidth: Use Signal Path
Record Acquisition: False
Measured 1 5/15/2023 12:46:43 PM

RMS Level (5/15/2023 12:46:43.335 PM)



Ch1 Failed Upper Limit

Result: FAILED

5/15/2023 12:47 PM

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Sequence Report



Hi Z Gain -10 2.2M 200k Termination : Signal Path Setup

Output Connector:	Analog Balanced
Channels:	2
Source Impedance:	100 ohm
AG52 Generator Option:	Installed
Output EQ:	None
Input Connector:	Analog Balanced
Channels:	1
Channel:	Ch1
Termination:	200 kohm
Input Bandwidth:	AC (<10 Hz) - 90k (192 kHz SR)
Device Delay:	0.000 s
Input EQ:	None

• References

dBr G:	100.0 mVrms
dBm (Output Power):	600.0 ohm
W(watts) (Output Power):	8.000 ohm
Shared Frequency Reference:	1.00000 kHz
dBrA:	1.000 Vrms
dBrB:	1.000 Vrms
dBrA Offset:	0.000 dB
dBrB Offset:	0.000 dB
dB SPL1:	10.00 mVrms
dB SPL2:	10.00 mVrms
dB SPL1 Calibrator Level:	94.000 dB SPL
dB SPL2 Calibrator Level:	94.000 dB SPL
dBm (Input Power):	600.0 ohm
W(watts) (Input Power):	8.000 ohm

• DCX

DC Output 1:	0.000 V
DC Output 1:	Off
DC Output 2:	0.000 V
DC Output 2:	Off
Port A (hex):	00
Port B (hex):	00
Port C (hex):	00

5/15/2023 12:47 PM

Sequence Report



Port D (hex): 00

- Clocks

Output Rate: Track Output SR

Sync Out Level: 3.300 V

Sync Out Polarity: Normal

Timebase Reference: Internal

Jitter: Disabled

- Triggers

Source: Off

Input Logic Level: 3.300 V

Edge: Rising

Hi Z Gain -10 2.2M 200k Termination : Verify Connections

Waveform: Sine

Generator Level: -22.300 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/15/2023 12:46:48.371 PM)

Ch1 298.7 mVrms

Gain (5/15/2023 12:46:48.371 PM)

Ch1 14.025 dB

THD+N Ratio (5/15/2023 12:46:48.371 PM)

Ch1 ---- %

Frequency (5/15/2023 12:46:48.371 PM)

Ch1 ---- Hz

Sequence Report



Hi Z Gain -10 2.2M 200k Termination : Level and Gain 2.2M


Waveform: Sine

Generator Level: -22.300 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/15/2023 12:46:51.597 PM)

Channel	Lower Limit	Value	Upper Limit	
Ch1	-2.000 dBu	-8.657 dBu	+2.000 dBu	

Result:  FAILED

Sequence Report



Hi Z Gain -10 47k 200k Termination : Signal Path Setup

Output Connector: Analog Balanced
Channels: 2
Source Impedance: 100 ohm
AG52 Generator Option: Installed
Output EQ: None
Input Connector: Analog Balanced
Channels: 1
Channel: Ch1
Termination: 200 kohm
Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)
Device Delay: 0.000 s
Input EQ: None

• References

dBr G: 100.0 mVrms
dBm (Output Power): 600.0 ohm
W(watts) (Output Power): 8.000 ohm
Shared Frequency Reference: 1.00000 kHz
dBrA: 1.000 Vrms
dBrB: 1.000 Vrms
dBrA Offset: 0.000 dB
dBrB Offset: 0.000 dB
dBSPL1: 10.00 mVrms
dBSPL2: 10.00 mVrms
dBSPL1 Calibrator Level: 94.000 dB SPL
dBSPL2 Calibrator Level: 94.000 dB SPL
dBm (Input Power): 600.0 ohm
W(watts) (Input Power): 8.000 ohm

• DCX

DC Output 1: 0.000 V
DC Output 1: Off
DC Output 2: 0.000 V
DC Output 2: Off
Port A (hex): 00
Port B (hex): 00
Port C (hex): 00

5/15/2023 12:47 PM

Sequence Report



Port D (hex): 00

- Clocks

Output Rate: Track Output SR

Sync Out Level: 3.300 V

Sync Out Polarity: Normal

Timebase Reference: Internal

Jitter: Disabled

- Triggers

Source: Off

Input Logic Level: 3.300 V

Edge: Rising

Hi Z Gain -10 47k 200k Termination : Verify Connections

Waveform: Sine

Generator Level: -22.300 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/15/2023 12:46:56.892 PM)

Ch1 283.9 mVrms

Gain (5/15/2023 12:46:56.892 PM)

Ch1 13.581 dB

THD+N Ratio (5/15/2023 12:46:56.892 PM)

Ch1 ---- %

Frequency (5/15/2023 12:46:56.892 PM)

Ch1 ---- Hz

Sequence Report



Hi Z Gain -10 47k 200k Termination : Level and Gain 47K


Waveform: Sine

Generator Level: -22.300 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/15/2023 12:46:59.534 PM)

Channel	Lower Limit	Value	Upper Limit	
Ch1	-8.000 dBu	-8.799 dBu	-4.000 dBu	

Result:  FAILED

Sequence Report



Dummy Signal Path For Report : Signal Path Setup

Output Connector:	Analog Unbalanced
Channels:	2
Source Impedance:	50 ohm
AG52 Generator Option:	Installed
Output EQ:	None
Input Connector:	Analog Unbalanced
Channels:	2
Termination:	100 kohm
Input Bandwidth:	AC (<10 Hz) - 90k (192 kHz SR)
Device Delay:	0.000 s
Input EQ:	None

• References

dBr G:	100.0 mVrms
dBm (Output Power):	600.0 ohm
W(watts) (Output Power):	8.000 ohm
Shared Frequency Reference:	1.00000 kHz
dBrA:	1.000 Vrms
dBrB:	1.000 Vrms
dBrA Offset:	0.000 dB
dBrB Offset:	0.000 dB
dB SPL1:	10.00 mVrms
dB SPL2:	10.00 mVrms
dB SPL1 Calibrator Level:	94.000 dB SPL
dB SPL2 Calibrator Level:	94.000 dB SPL
dBm (Input Power):	600.0 ohm
W(watts) (Input Power):	8.000 ohm

• DCX

DC Output 1:	0.000 V
DC Output 1:	Off
DC Output 2:	0.000 V
DC Output 2:	Off
Port A (hex):	00
Port B (hex):	00
Port C (hex):	00
Port D (hex):	00

5/15/2023 12:47 PM

Sequence Report



- Clocks

Output Rate:	Track Output SR
Sync Out Level:	3.300 V
Sync Out Polarity:	Normal
Timebase Reference:	Internal
Jitter:	Disabled

- Triggers

Source:	Off
Input Logic Level:	3.300 V
Edge:	Rising

Dummy Signal Path For Report : Verify Connections

Waveform:	Sine
Generator Level:	100.0 mVrms
DC Offset:	0.000 V
Frequency:	1.00000 kHz

RMS Level (5/15/2023 12:47:03.148 PM)

Ch1	284.9 mVrms
Ch2	290.1 mVrms