

## Sequence Report



### Summary

#### Mic500 200k Termination

Signal Path Setup PASSED

Stepped Frequency Sweep MIC 500 FAILED

#### Mic 2k 200k termination

Signal Path Setup PASSED

Stepped Frequency Sweep MIC 2K FAILED

#### Mic 2k 15dB PAD 200k termination

Signal Path Setup PASSED

Stepped Frequency Sweep 15dB PAD FAILED

#### Line Gain -10 200kTermination

Signal Path Setup PASSED

Stepped Frequency Sweep -10 FAILED

#### Line Gain -10 600 Termination

Signal Path Setup PASSED

Level and Gain -10 PASSED

#### Line Gain +5 200kTermination

Signal Path Setup PASSED

Stepped Frequency Sweep +5 FAILED

#### Line Gain +5 600 Termination

Signal Path Setup PASSED

Level and Gain +5 FAILED

#### Line Gain -5 600 Termination

Signal Path Setup PASSED

Level and Gain -5 FAILED

#### Line Gain 0 600 Termination

Signal Path Setup PASSED

Level and Gain 0 FAILED

#### Line Gain +10 600 Termination

Signal Path Setup PASSED

Level and Gain +10 FAILED

#### Line Gain +10 200k Termination Level Hi

Signal Path Setup PASSED

Noise Recorder (RMS) CW FAILED

#### Line Gain +10 200k Termination Level Low

Signal Path Setup PASSED

Noise Recorder (RMS) CCW FAILED

#### Hi Z Gain -10 2.2M 200k Termination

Signal Path Setup	✓ PASSED
Level and Gain 2.2M	⚠ FAILED
Hi Z Gain -10 47k 200k Termination	
Signal Path Setup	✓ PASSED
Level and Gain 47K	⚠ FAILED
Dummy Signal Path For Report	
Signal Path Setup	✓ PASSED
Sequence Result:	
Sequence Result:	⚠ FAILED

## Sequence Report



### Mic500 200k Termination : Signal Path Setup

Output Connector:	Analog Balanced
Channels:	1
Source Impedance:	100 ohm
Output EQ:	None
Input Connector:	Analog Balanced
Channels:	1
Channel:	Ch1
Termination:	200 kohm
Input Bandwidth:	AC (<10 Hz) - 90k (192 kHz SR)
Device Delay:	0.000 s
Input EQ:	None

#### • References

dBr G:	100.0 mVrms
dBm (Output Power):	600.0 ohm
W(watts) (Output Power):	8.000 ohm
Shared Frequency Reference:	1.00000 kHz
dBrA:	1.000 Vrms
dBrB:	1.000 Vrms
dBrA Offset:	0.000 dB
dBrB Offset:	0.000 dB
dB SPL1:	10.00 mVrms
dB SPL2:	10.00 mVrms
dB SPL1 Calibrator Level:	94.000 dB SPL
dB SPL2 Calibrator Level:	94.000 dB SPL
dBm (Input Power):	600.0 ohm
W(watts) (Input Power):	8.000 ohm

#### • DCX

DC Output 1:	0.000 V
DC Output 1:	Off
DC Output 2:	0.000 V
DC Output 2:	Off
Port A (hex):	00
Port B (hex):	00
Port C (hex):	00

## Sequence Report



Port D (hex): 00

### Mic500 200k Termination : Verify Connections

Waveform: Sine

Generator Level: -42.300 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 1:48:54.509 PM)

Ch1 290.1 mVrms

Gain (5/2/2023 1:48:54.509 PM)

Ch1 33.769 dB

THD+N Ratio (5/2/2023 1:48:54.509 PM)

Ch1 ---- %

Frequency (5/2/2023 1:48:54.509 PM)

Ch1 ---- Hz

## Sequence Report



Mic500 200k Termination : Stepped Frequency Sweep MIC 500

Generator Level: -42.300 dBu

DC Offset: 0.000 V

EQ: None

Start Frequency: 20.0000 kHz

Stop Frequency: 20.0000 Hz

Step Type: Logarithmic

Number of Points: 10

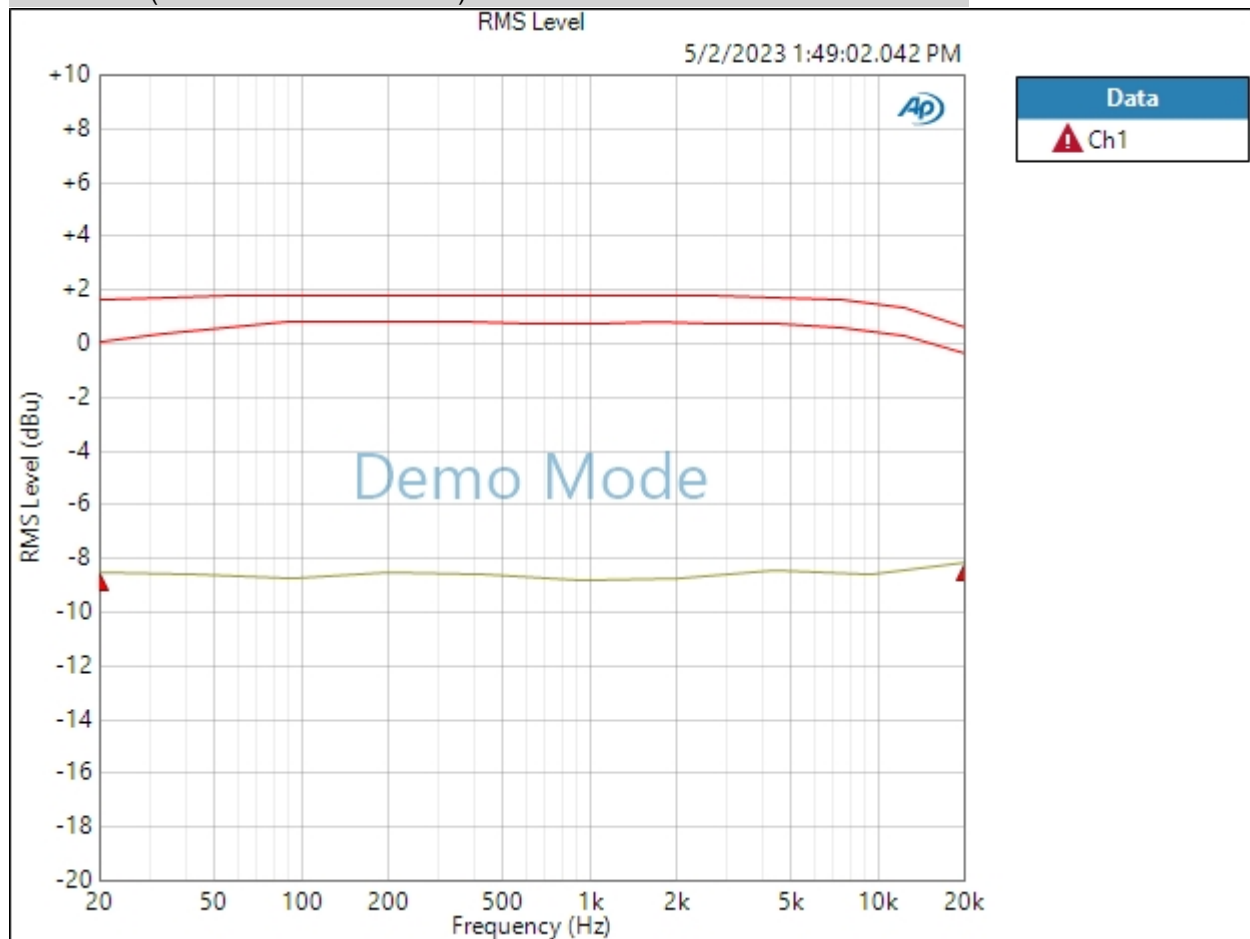
Weighting Filter: Signal Path

High-pass Filter: 20 Hz

Phase Ref Channel: Ch1

Measured 1 5/2/2023 1:49:02 PM

RMS Level (5/2/2023 1:49:02.042 PM)



Ch1 Failed Lower Limit

5/2/2023 1:51 PM

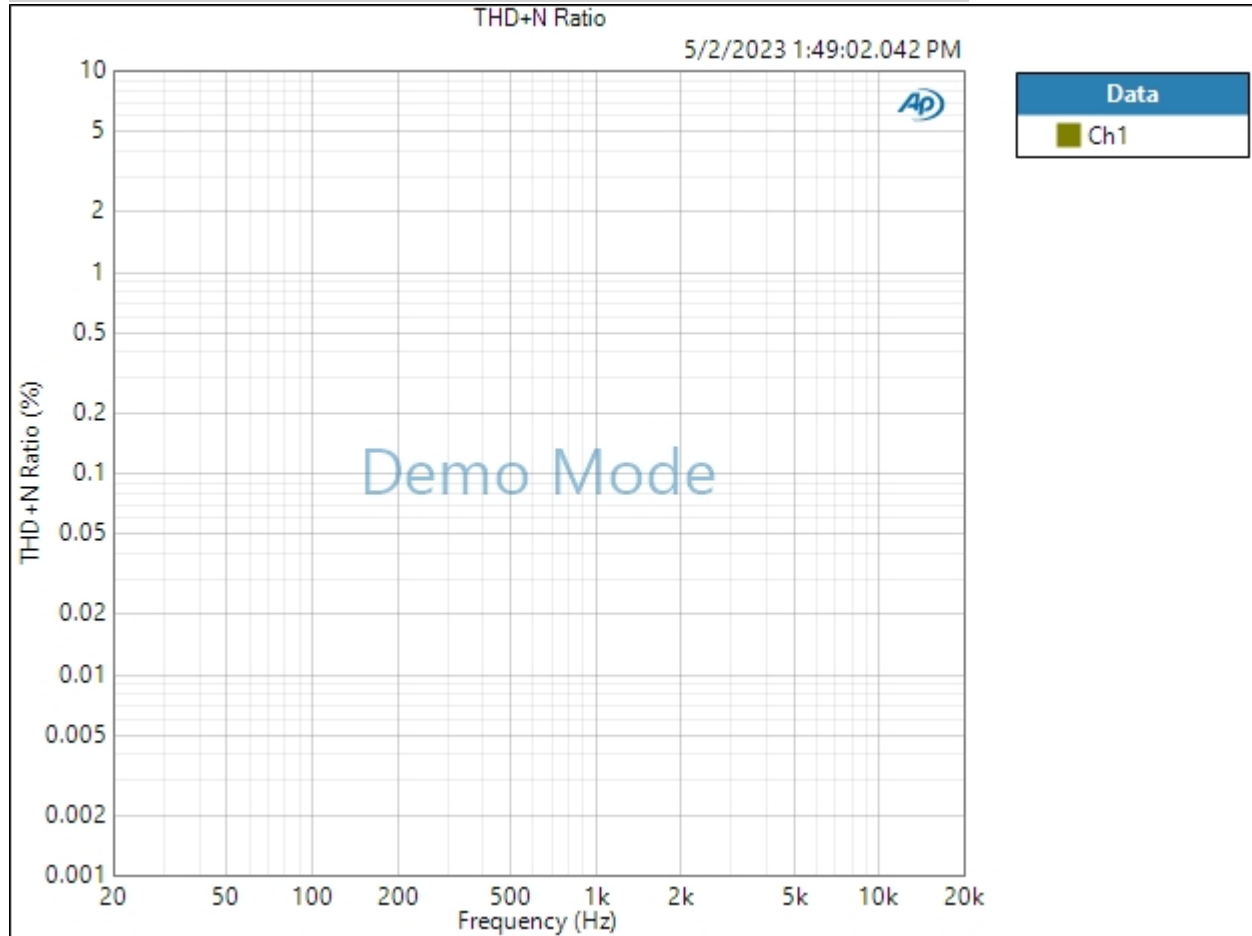
Page 5 of 47

## Sequence Report



Result: ▲ FAILED

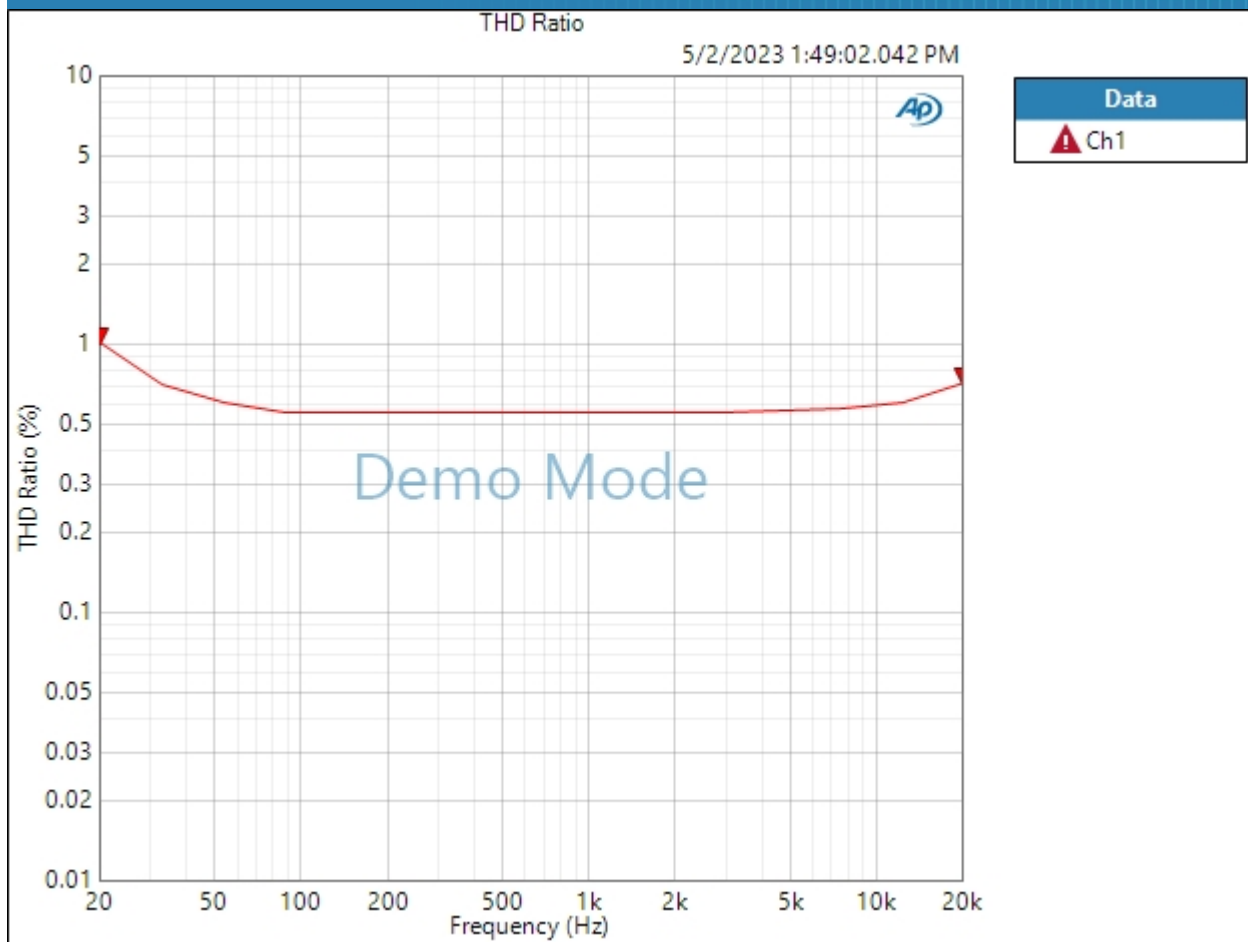
THD+N Ratio (5/2/2023 1:49:02.042 PM)



Result: ✔ PASSED

THD Ratio (5/2/2023 1:49:02.042 PM)

## Sequence Report



Ch1 Failed Upper Limit

Result: FAILED

## Sequence Report



### Mic 2k 200k termination : Signal Path Setup

Output Connector:	Analog Balanced
Channels:	1
Source Impedance:	100 ohm
Output EQ:	None
Input Connector:	Analog Balanced
Channels:	1
Channel:	Ch1
Termination:	200 kohm
Input Bandwidth:	AC (<10 Hz) - 90k (192 kHz SR)
Device Delay:	0.000 s
Input EQ:	None

#### • References

dBr G:	100.0 mVrms
dBm (Output Power):	600.0 ohm
W(watts) (Output Power):	8.000 ohm
Shared Frequency Reference:	1.00000 kHz
dBrA:	1.000 Vrms
dBrB:	1.000 Vrms
dBrA Offset:	0.000 dB
dBrB Offset:	0.000 dB
dB SPL1:	10.00 mVrms
dB SPL2:	10.00 mVrms
dB SPL1 Calibrator Level:	94.000 dB SPL
dB SPL2 Calibrator Level:	94.000 dB SPL
dBm (Input Power):	600.0 ohm
W(watts) (Input Power):	8.000 ohm

#### • DCX

DC Output 1:	0.000 V
DC Output 1:	Off
DC Output 2:	0.000 V
DC Output 2:	Off
Port A (hex):	00
Port B (hex):	00
Port C (hex):	00



## Sequence Report



Port D (hex): 00

### Mic 2k 200k termination : Verify Connections

Waveform: Sine

Generator Level: -42.300 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 1:49:07.277 PM)

Ch1 288.3 mVrms

Gain (5/2/2023 1:49:07.277 PM)

Ch1 33.715 dB

THD+N Ratio (5/2/2023 1:49:07.277 PM)

Ch1 ---- %

Frequency (5/2/2023 1:49:07.277 PM)

Ch1 ---- Hz

## Sequence Report



Mic 2k 200k termination : Stepped Frequency Sweep MIC 2K

Generator Level: -42.300 dBu

DC Offset: 0.000 V

EQ: None

Start Frequency: 20.0000 kHz

Stop Frequency: 20.0000 Hz

Step Type: Logarithmic

Number of Points: 10

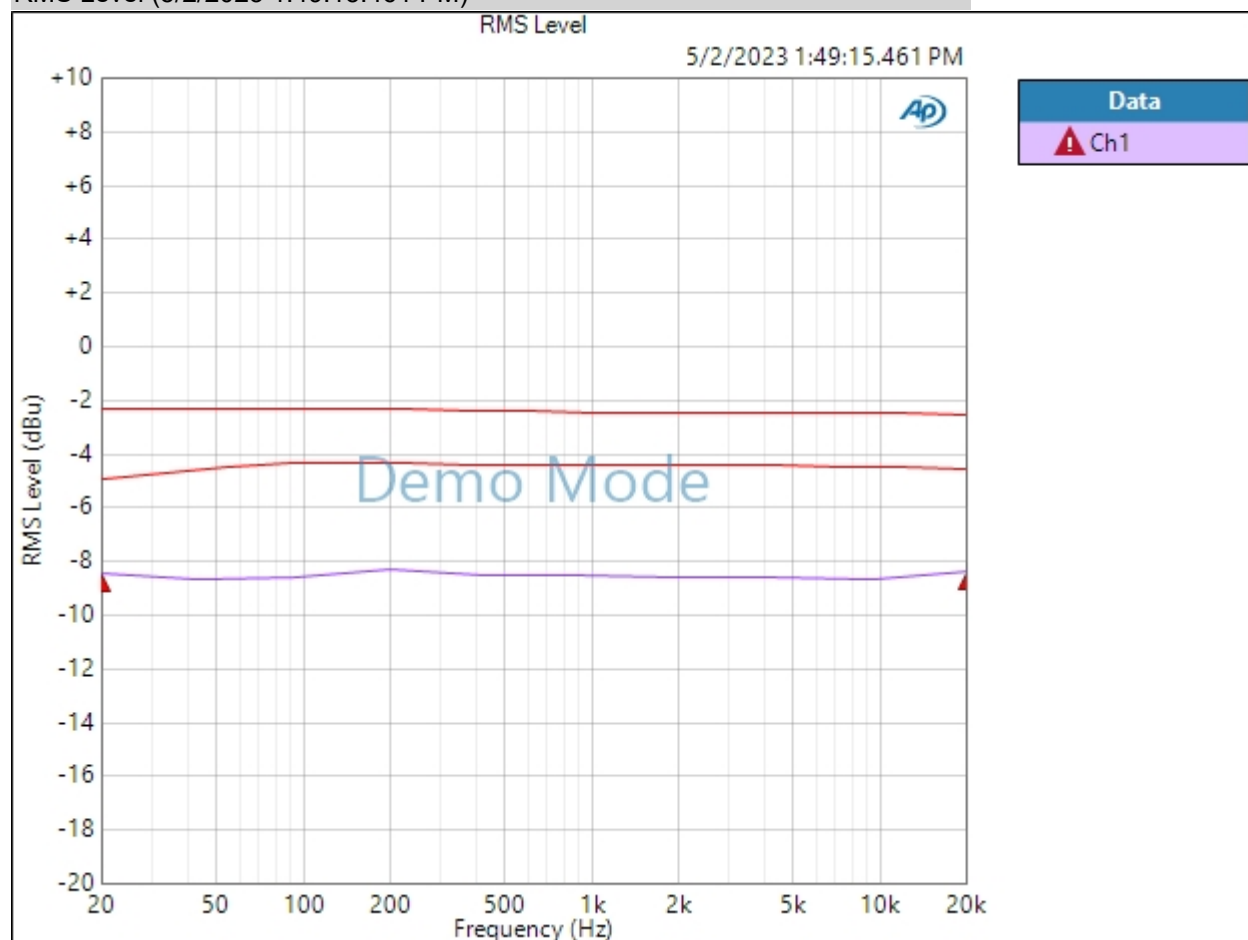
Weighting Filter: Signal Path

High-pass Filter: 20 Hz

Phase Ref Channel: Ch1

Measured 1 5/2/2023 1:49:15 PM

RMS Level (5/2/2023 1:49:15.461 PM)

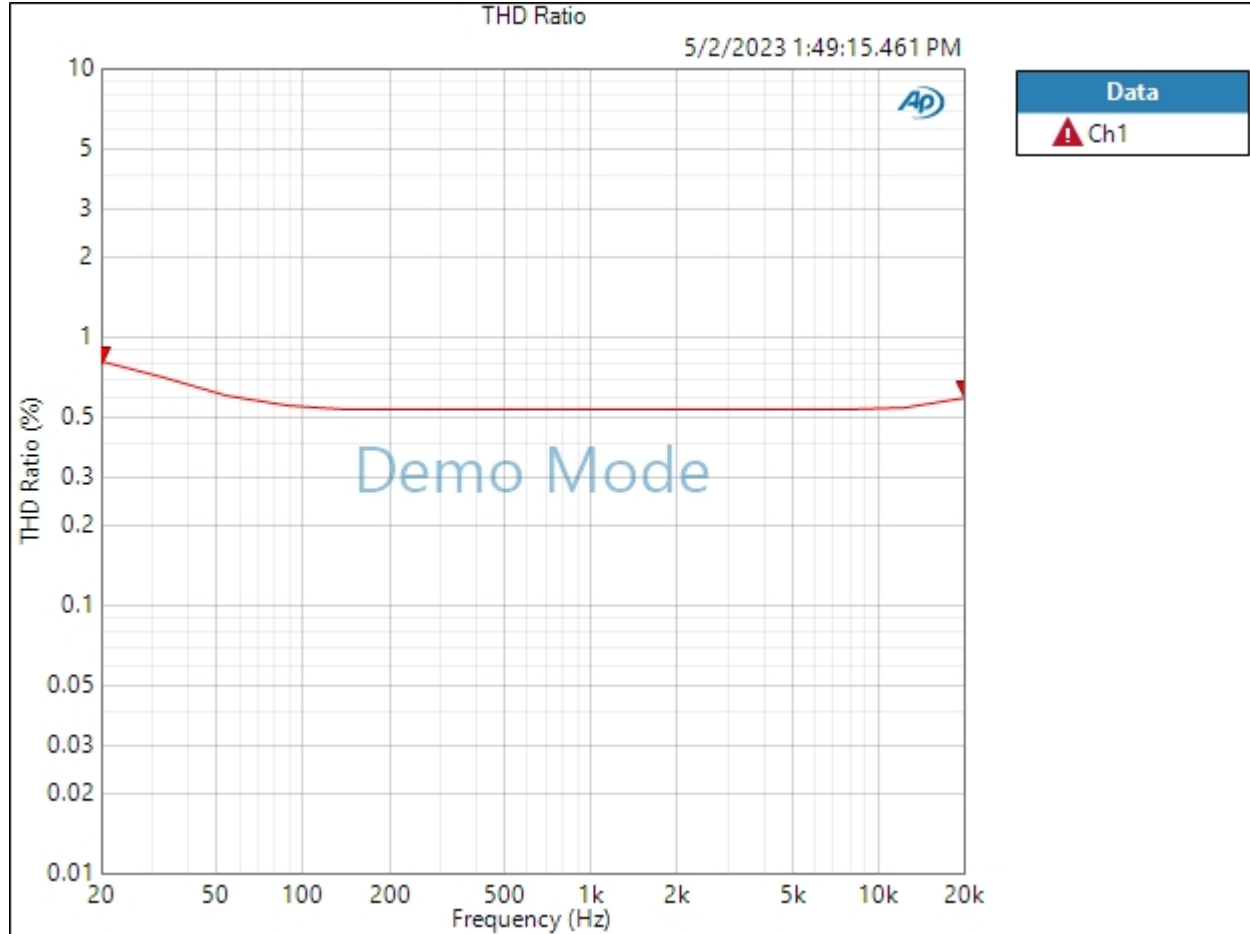


## Sequence Report



Result: ▲ FAILED

THD Ratio (5/2/2023 1:49:15.461 PM)



Ch1 ▲ Failed Upper Limit

Result: ▲ FAILED

## Sequence Report



Mic 2k 15dB PAD 200k termination : Signal Path Setup

Output Connector: Analog Balanced  
Channels: 1  
Source Impedance: 100 ohm  
Output EQ: None  
Input Connector: Analog Balanced  
Channels: 1  
Channel: Ch1  
Termination: 200 kohm  
Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)  
Device Delay: 0.000 s  
Input EQ: None

### • References

dBr G: 100.0 mVrms  
dBm (Output Power): 600.0 ohm  
W(watts) (Output Power): 8.000 ohm  
Shared Frequency Reference: 1.00000 kHz  
dBrA: 1.000 Vrms  
dBrB: 1.000 Vrms  
dBrA Offset: 0.000 dB  
dBrB Offset: 0.000 dB  
dBSPL1: 10.00 mVrms  
dBSPL2: 10.00 mVrms  
dBSPL1 Calibrator Level: 94.000 dB SPL  
dBSPL2 Calibrator Level: 94.000 dB SPL  
dBm (Input Power): 600.0 ohm  
W(watts) (Input Power): 8.000 ohm

### • DCX

DC Output 1: 0.000 V  
DC Output 1: Off  
DC Output 2: 0.000 V  
DC Output 2: Off  
Port A (hex): 00  
Port B (hex): 00  
Port C (hex): 00

5/2/2023 1:51 PM

Page 12 of 47

## Sequence Report



Port D (hex): 00

Mic 2k 15dB PAD 200k termination : Verify Connections

Waveform: Sine

Generator Level: -42.300 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 1:49:20.803 PM)

Ch1 288.8 mVrms

Gain (5/2/2023 1:49:20.803 PM)

Ch1 33.731 dB

THD+N Ratio (5/2/2023 1:49:20.803 PM)

Ch1 ---- %

Frequency (5/2/2023 1:49:20.803 PM)

Ch1 ---- Hz

## Sequence Report



Mic 2k 15dB PAD 200k termination : Stepped Frequency Sweep 15dB PAD

Generator Level: -42.000 dBu

DC Offset: 0.000 V

EQ: None

Start Frequency: 20.0000 kHz

Stop Frequency: 20.0000 Hz

Step Type: Logarithmic

Number of Points: 10

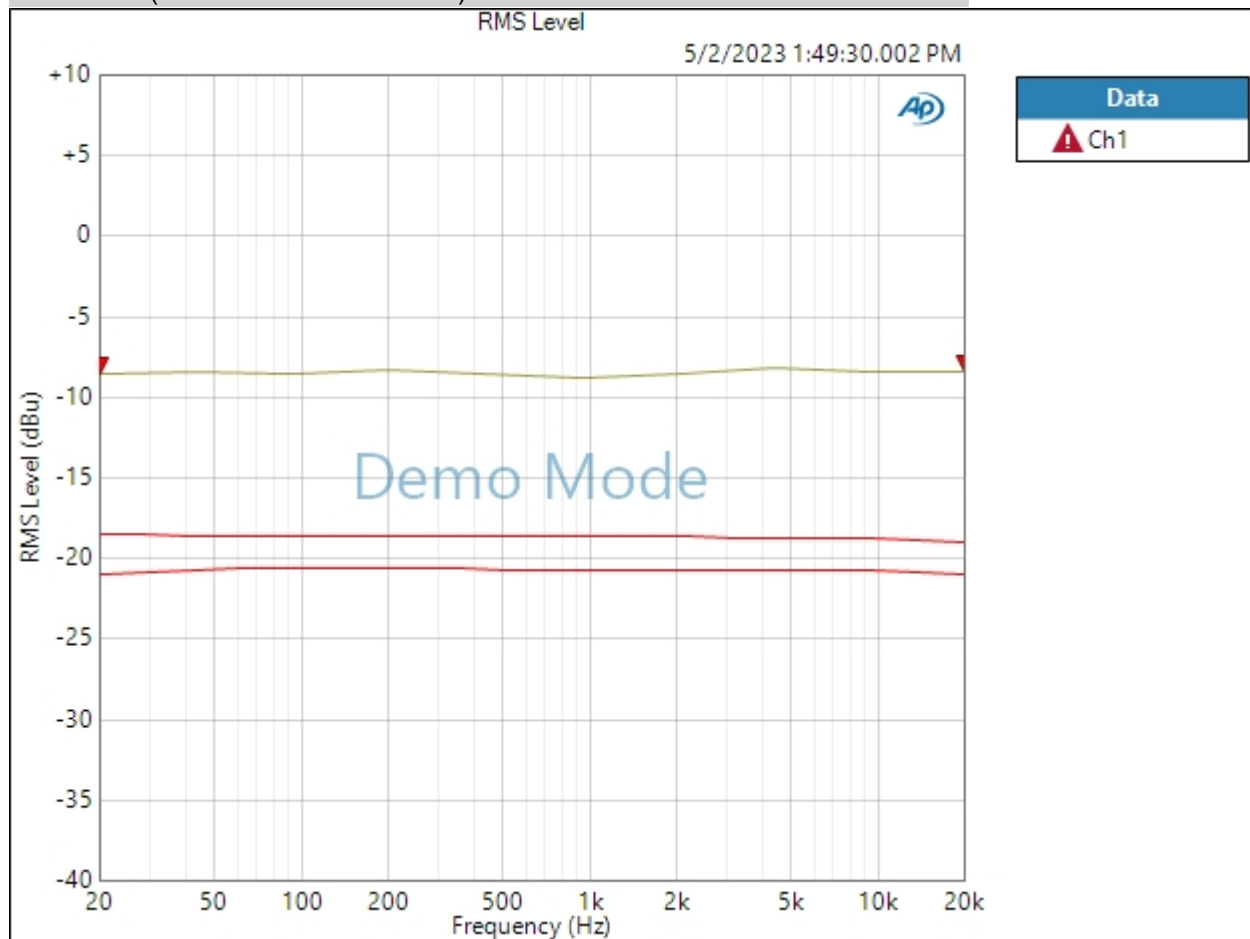
Weighting Filter: Signal Path

High-pass Filter: 20 Hz

Phase Ref Channel: Ch1

Measured 1 5/2/2023 1:49:30 PM

RMS Level (5/2/2023 1:49:30.002 PM)



Ch1 Failed Upper Limit

5/2/2023 1:51 PM

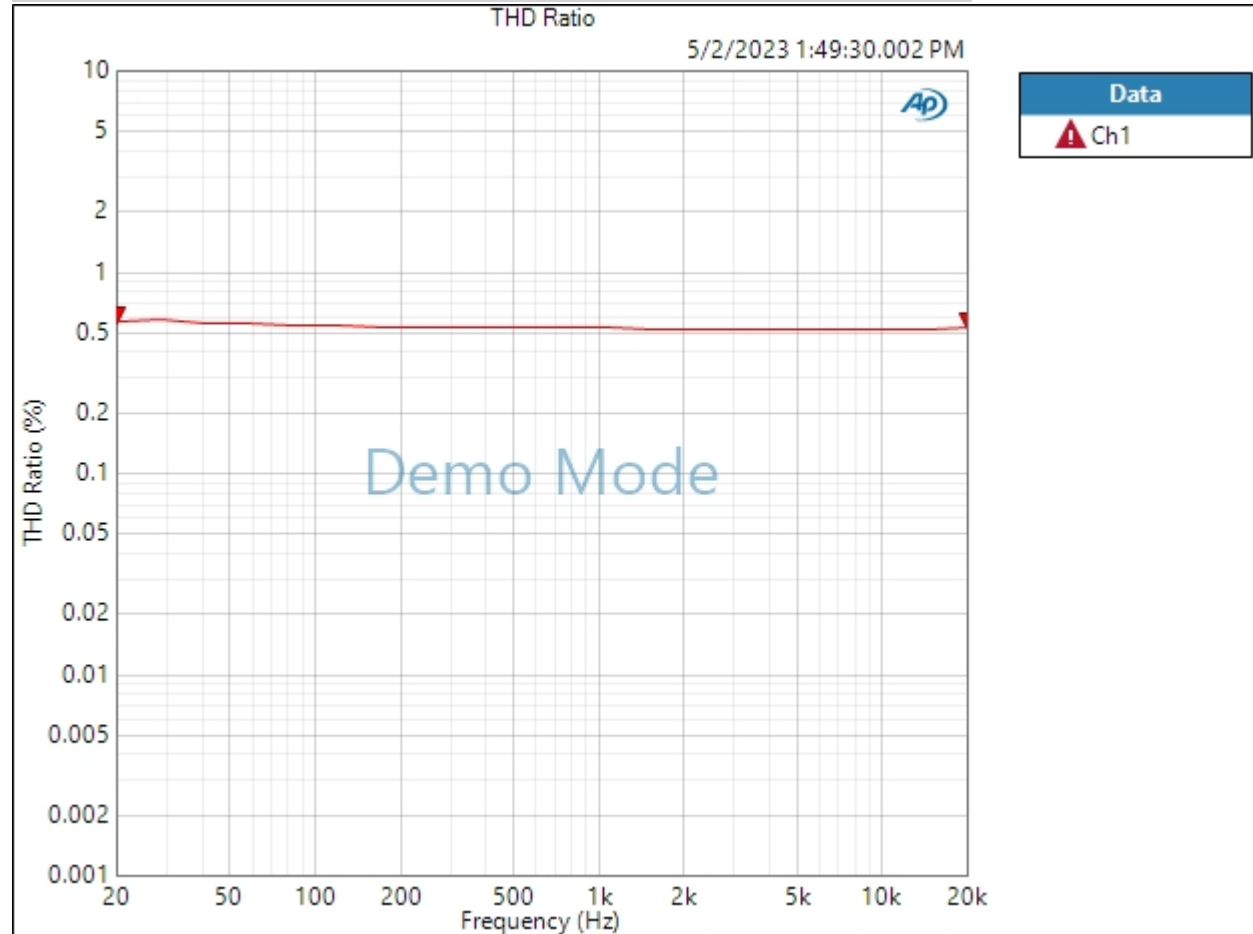
Page 14 of 47

## Sequence Report



Result: ▲ FAILED

THD Ratio (5/2/2023 1:49:30.002 PM)



Ch1 ▲ Failed Upper Limit

Result: ▲ FAILED

## Sequence Report



### Line Gain -10 200kTermination : Signal Path Setup

Output Connector: Analog Balanced  
Channels: 1  
Source Impedance: 100 ohm  
Output EQ: None  
Input Connector: Analog Balanced  
Channels: 1  
Channel: Ch1  
Termination: 200 kohm  
Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)  
Device Delay: 0.000 s  
Input EQ: None

#### • References

dBr G: 100.0 mVrms  
dBm (Output Power): 600.0 ohm  
W(watts) (Output Power): 8.000 ohm  
Shared Frequency Reference: 1.00000 kHz  
dBrA: 1.000 Vrms  
dBrB: 1.000 Vrms  
dBrA Offset: 0.000 dB  
dBrB Offset: 0.000 dB  
dBSPL1: 10.00 mVrms  
dBSPL2: 10.00 mVrms  
dBSPL1 Calibrator Level: 94.000 dB SPL  
dBSPL2 Calibrator Level: 94.000 dB SPL  
dBm (Input Power): 600.0 ohm  
W(watts) (Input Power): 8.000 ohm

#### • DCX

DC Output 1: 0.000 V  
DC Output 1: Off  
DC Output 2: 0.000 V  
DC Output 2: Off  
Port A (hex): 00  
Port B (hex): 00  
Port C (hex): 00



## Sequence Report



Port D (hex): 00

### Line Gain -10 200kTermination : Verify Connections

Waveform: Sine

Generator Level: 0.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 1:49:35.262 PM)

Ch1 291.9 mVrms

Gain (5/2/2023 1:49:35.262 PM)

Ch1 -8.478 dB

THD+N Ratio (5/2/2023 1:49:35.262 PM)

Ch1 ---- %

Frequency (5/2/2023 1:49:35.262 PM)

Ch1 ---- Hz

## Sequence Report



Line Gain -10 200kTermination : Stepped Frequency Sweep -10

Generator Level: 0.000 dBu

DC Offset: 0.000 V

EQ: None

Start Frequency: 20.0000 kHz

Stop Frequency: 20.0000 Hz

Step Type: Logarithmic

Number of Points: 15

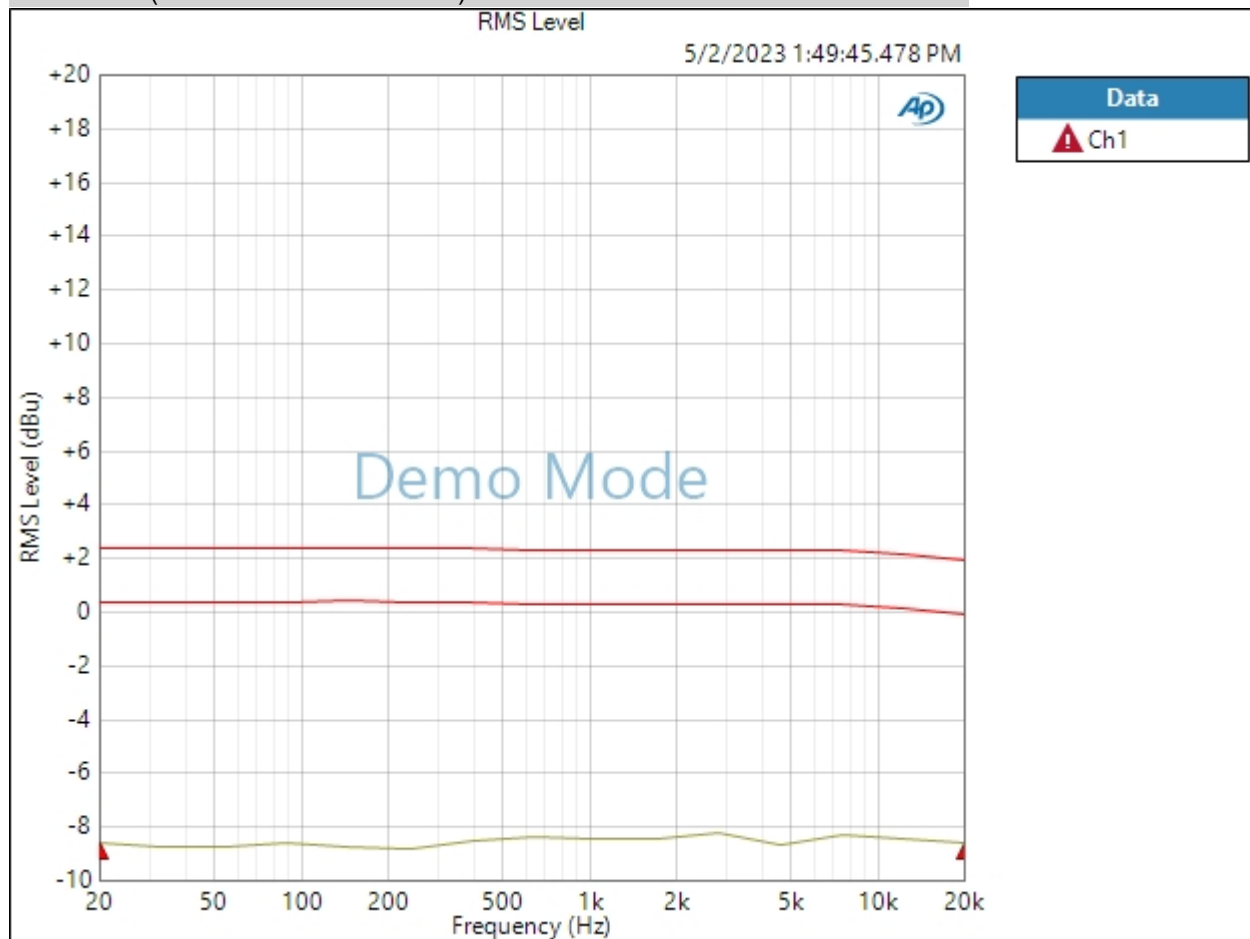
Weighting Filter: Signal Path

High-pass Filter: 20 Hz

Phase Ref Channel: Ch1

Measured 1 5/2/2023 1:49:45 PM

RMS Level (5/2/2023 1:49:45.478 PM)



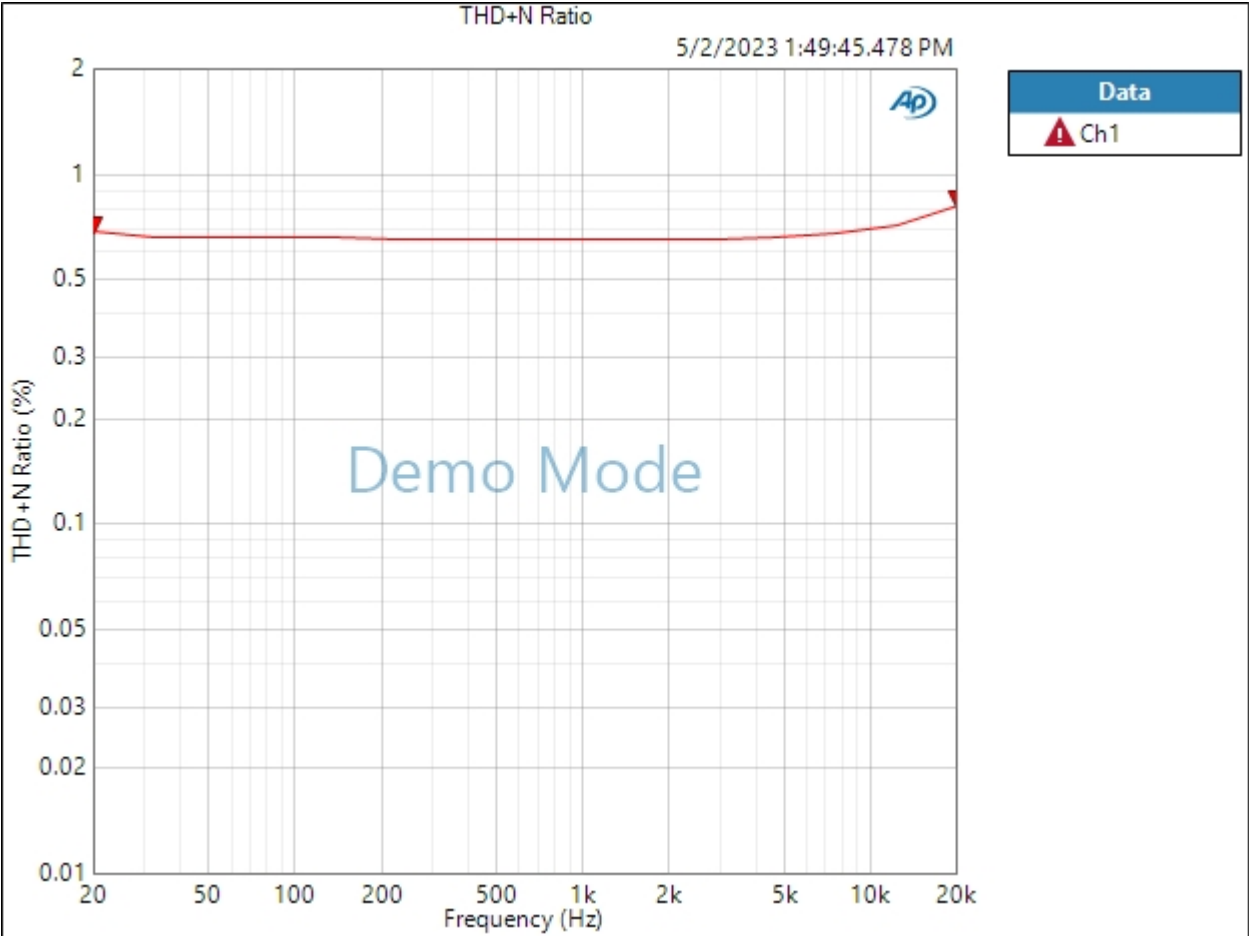
Ch1 ▲ Failed Lower Limit

# Sequence Report



Result: ▲ FAILED

THD+N Ratio (5/2/2023 1:49:45.478 PM)



Ch1 ▲ Failed Upper Limit

Result: ▲ FAILED

## Sequence Report



### Line Gain -10 600 Termination : Signal Path Setup

Output Connector:	Analog Balanced
Channels:	1
Source Impedance:	100 ohm
Output EQ:	None
Input Connector:	Analog Balanced
Channels:	1
Channel:	Ch1
Termination:	600 ohm
Input Bandwidth:	AC (<10 Hz) - 90k (192 kHz SR)
Device Delay:	0.000 s
Input EQ:	None

#### • References

dBr G:	100.0 mVrms
dBm (Output Power):	600.0 ohm
W(watts) (Output Power):	8.000 ohm
Shared Frequency Reference:	1.00000 kHz
dBrA:	1.000 Vrms
dBrB:	1.000 Vrms
dBrA Offset:	0.000 dB
dBrB Offset:	0.000 dB
dB SPL1:	10.00 mVrms
dB SPL2:	10.00 mVrms
dB SPL1 Calibrator Level:	94.000 dB SPL
dB SPL2 Calibrator Level:	94.000 dB SPL
dBm (Input Power):	600.0 ohm
W(watts) (Input Power):	8.000 ohm

#### • DCX

DC Output 1:	0.000 V
DC Output 1:	Off
DC Output 2:	0.000 V
DC Output 2:	Off
Port A (hex):	00
Port B (hex):	00
Port C (hex):	00

## Sequence Report



Port D (hex): 00

### Line Gain -10 600 Termination : Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 1:49:51.051 PM)

Ch1 283.0 mVrms

Gain (5/2/2023 1:49:51.051 PM)

Ch1 1.255 dB

THD+N Ratio (5/2/2023 1:49:51.051 PM)

Ch1 ---- %

Frequency (5/2/2023 1:49:51.051 PM)

Ch1 ---- Hz

### Line Gain -10 600 Termination : Level and Gain -10

Waveform: Sine

Generator Level: -10.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 1:49:54.335 PM)

Channel	Lower Limit	Value	Upper Limit	
Ch1	-11.500 dBu	-8.606 dBu	-8.500 dBu	✓

Result: ✓ PASSED

## Sequence Report



### Line Gain +5 200kTermination : Signal Path Setup

Output Connector: Analog Balanced  
Channels: 1  
Source Impedance: 100 ohm  
Output EQ: None  
Input Connector: Analog Balanced  
Channels: 1  
Channel: Ch1  
Termination: 200 kohm  
Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)  
Device Delay: 0.000 s  
Input EQ: None

#### • References

dBr G: 100.0 mVrms  
dBm (Output Power): 600.0 ohm  
W(watts) (Output Power): 8.000 ohm  
Shared Frequency Reference: 1.00000 kHz  
dBrA: 1.000 Vrms  
dBrB: 1.000 Vrms  
dBrA Offset: 0.000 dB  
dBrB Offset: 0.000 dB  
dBSPL1: 10.00 mVrms  
dBSPL2: 10.00 mVrms  
dBSPL1 Calibrator Level: 94.000 dB SPL  
dBSPL2 Calibrator Level: 94.000 dB SPL  
dBm (Input Power): 600.0 ohm  
W(watts) (Input Power): 8.000 ohm

#### • DCX

DC Output 1: 0.000 V  
DC Output 1: Off  
DC Output 2: 0.000 V  
DC Output 2: Off  
Port A (hex): 00  
Port B (hex): 00  
Port C (hex): 00

## Sequence Report



Port D (hex): 00

### Line Gain +5 200kTermination : Verify Connections

Waveform: Sine

Generator Level: 0.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 1:49:59.832 PM)

Ch1 280.7 mVrms

Gain (5/2/2023 1:49:59.832 PM)

Ch1 -8.818 dB

THD+N Ratio (5/2/2023 1:49:59.832 PM)

Ch1 ---- %

Frequency (5/2/2023 1:49:59.832 PM)

Ch1 ---- Hz

## Sequence Report



Line Gain +5 200kTermination : Stepped Frequency Sweep +5

Generator Level: 0.000 dBu

DC Offset: 0.000 V

EQ: None

Start Frequency: 20.0000 kHz

Stop Frequency: 20.0000 Hz

Step Type: Logarithmic

Number of Points: 15

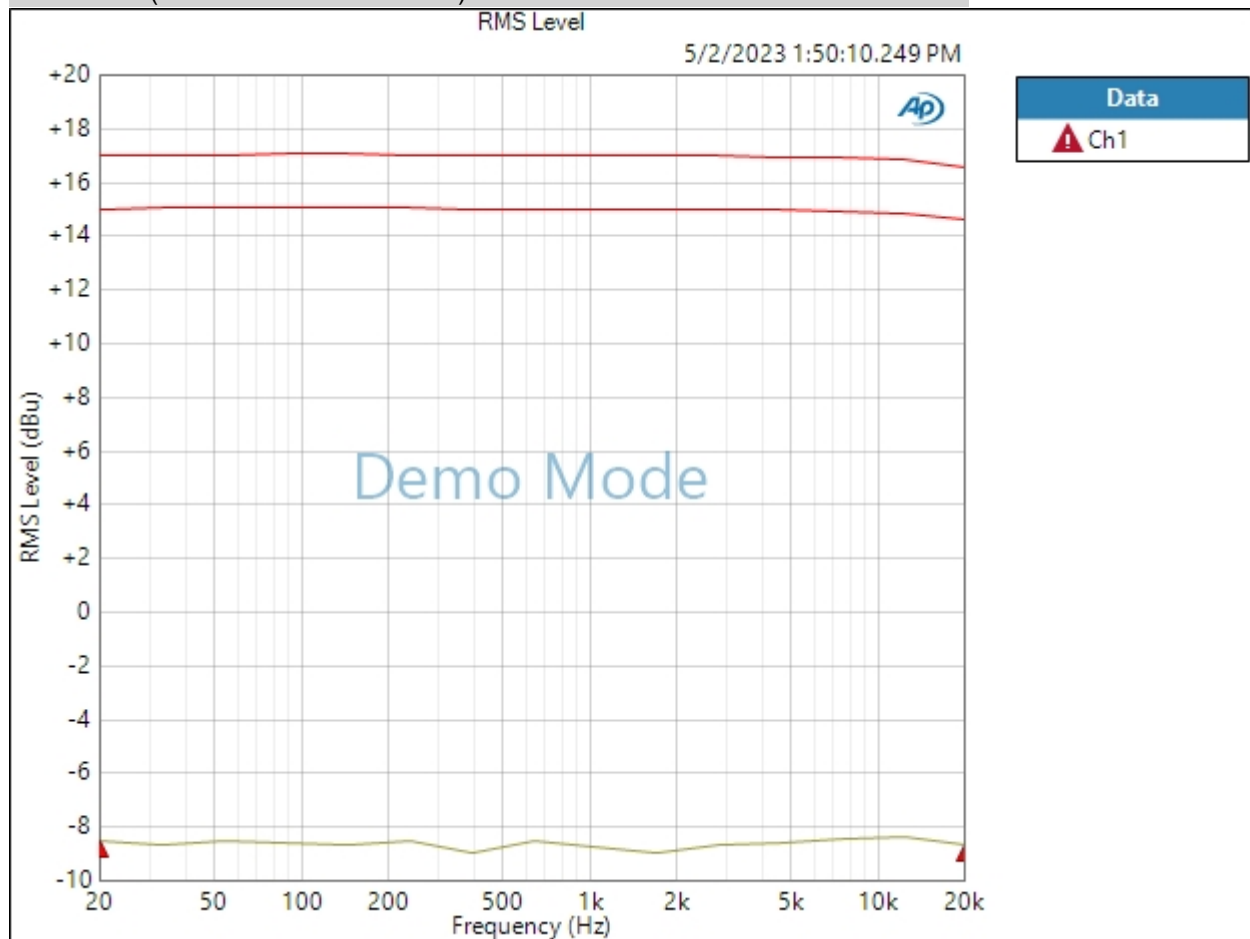
Weighting Filter: Signal Path

High-pass Filter: 20 Hz

Phase Ref Channel: Ch1

Measured 1 5/2/2023 1:50:10 PM

RMS Level (5/2/2023 1:50:10.249 PM)



Ch1 Failed Lower Limit

5/2/2023 1:51 PM

Page 24 of 47

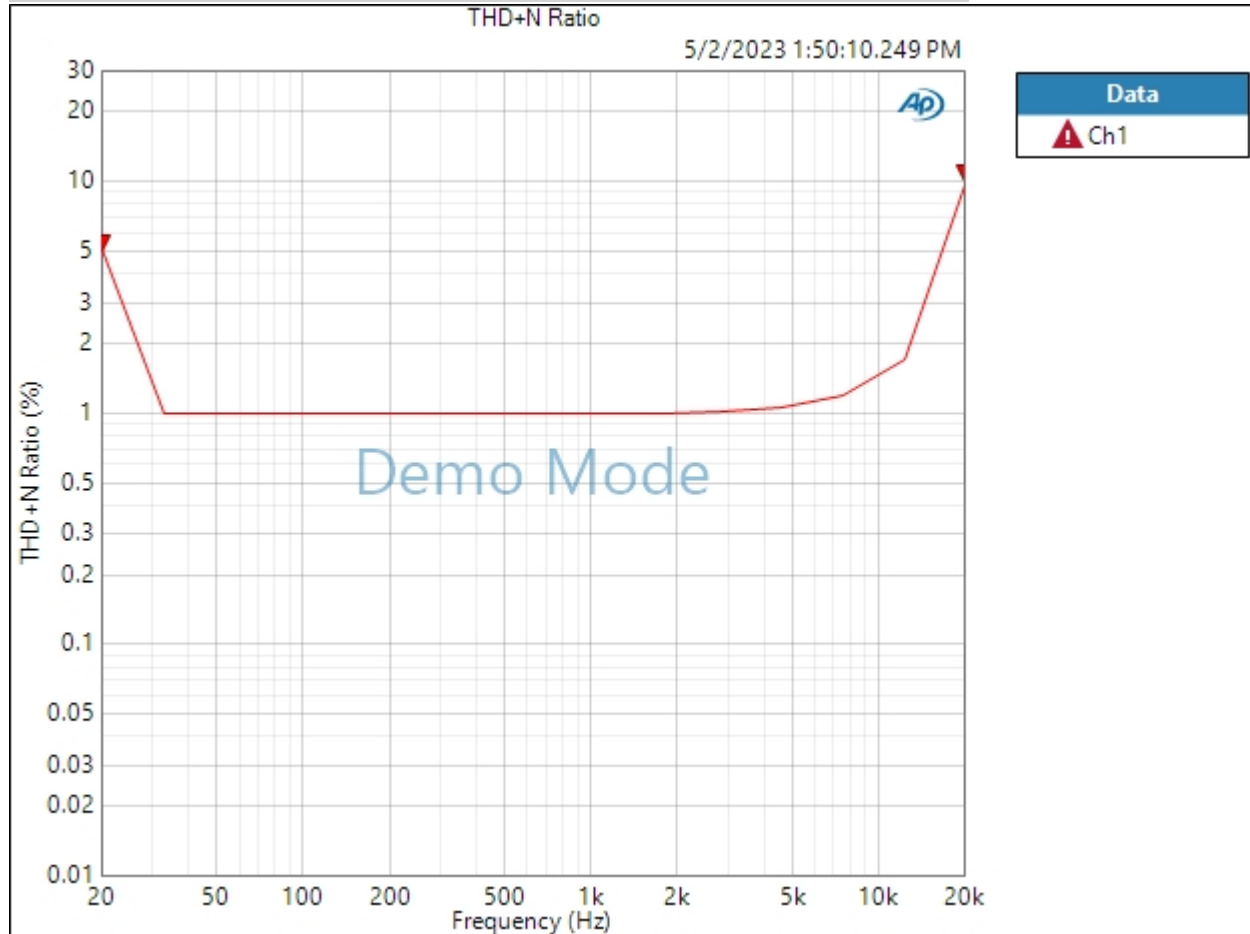


## Sequence Report



Result: ▲ FAILED

THD+N Ratio (5/2/2023 1:50:10.249 PM)



Ch1 ▲ Failed Upper Limit

Result: ▲ FAILED

## Sequence Report



### Line Gain +5 600 Termination : Signal Path Setup

Output Connector:	Analog Balanced
Channels:	1
Source Impedance:	100 ohm
Output EQ:	None
Input Connector:	Analog Balanced
Channels:	1
Channel:	Ch1
Termination:	600 ohm
Input Bandwidth:	AC (<10 Hz) - 90k (192 kHz SR)
Device Delay:	0.000 s
Input EQ:	None

#### • References

dBr G:	100.0 mVrms
dBm (Output Power):	600.0 ohm
W(watts) (Output Power):	8.000 ohm
Shared Frequency Reference:	1.00000 kHz
dBrA:	1.000 Vrms
dBrB:	1.000 Vrms
dBrA Offset:	0.000 dB
dBrB Offset:	0.000 dB
dB SPL1:	10.00 mVrms
dB SPL2:	10.00 mVrms
dB SPL1 Calibrator Level:	94.000 dB SPL
dB SPL2 Calibrator Level:	94.000 dB SPL
dBm (Input Power):	600.0 ohm
W(watts) (Input Power):	8.000 ohm

#### • DCX

DC Output 1:	0.000 V
DC Output 1:	Off
DC Output 2:	0.000 V
DC Output 2:	Off
Port A (hex):	00
Port B (hex):	00
Port C (hex):	00

## Sequence Report



Port D (hex): 00

### Line Gain +5 600 Termination : Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 1:50:16.553 PM)

Ch1 296.3 mVrms

Gain (5/2/2023 1:50:16.553 PM)

Ch1 1.654 dB

THD+N Ratio (5/2/2023 1:50:16.553 PM)

Ch1 ---- %

Frequency (5/2/2023 1:50:16.553 PM)

Ch1 ---- Hz

### Line Gain +5 600 Termination : Level and Gain +5

Waveform: Sine

Generator Level: -10.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 1:50:20.330 PM)

Channel	Lower Limit	Value	Upper Limit	
Ch1	+3.500 dBu	-8.711 dBu	+6.500 dBu	

Result:  FAILED

## Sequence Report



### Line Gain -5 600 Termination : Signal Path Setup

Output Connector: Analog Balanced  
Channels: 1  
Source Impedance: 100 ohm  
Output EQ: None  
Input Connector: Analog Balanced  
Channels: 1  
Channel: Ch1  
Termination: 600 ohm  
Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)  
Device Delay: 0.000 s  
Input EQ: None

#### • References

dBr G: 100.0 mVrms  
dBm (Output Power): 600.0 ohm  
W(watts) (Output Power): 8.000 ohm  
Shared Frequency Reference: 1.00000 kHz  
dBrA: 1.000 Vrms  
dBrB: 1.000 Vrms  
dBrA Offset: 0.000 dB  
dBrB Offset: 0.000 dB  
dBSPL1: 10.00 mVrms  
dBSPL2: 10.00 mVrms  
dBSPL1 Calibrator Level: 94.000 dB SPL  
dBSPL2 Calibrator Level: 94.000 dB SPL  
dBm (Input Power): 600.0 ohm  
W(watts) (Input Power): 8.000 ohm

#### • DCX

DC Output 1: 0.000 V  
DC Output 1: Off  
DC Output 2: 0.000 V  
DC Output 2: Off  
Port A (hex): 00  
Port B (hex): 00  
Port C (hex): 00

## Sequence Report



Port D (hex): 00

### Line Gain -5 600 Termination : Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 1:50:25.580 PM)

Ch1 293.0 mVrms

Gain (5/2/2023 1:50:25.580 PM)

Ch1 1.555 dB

THD+N Ratio (5/2/2023 1:50:25.580 PM)

Ch1 ---- %

Frequency (5/2/2023 1:50:25.580 PM)

Ch1 ---- Hz

### Line Gain -5 600 Termination : Level and Gain -5


Waveform: Sine

Generator Level: -10.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 1:50:29.309 PM)

Channel	Lower Limit	Value	Upper Limit	
Ch1	-6.500 dBu	-8.573 dBu	-3.500 dBu	

Result:  FAILED

## Sequence Report



### Line Gain 0 600 Termination : Signal Path Setup

Output Connector: Analog Balanced  
Channels: 1  
Source Impedance: 100 ohm  
Output EQ: None  
Input Connector: Analog Balanced  
Channels: 1  
Channel: Ch1  
Termination: 600 ohm  
Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)  
Device Delay: 0.000 s  
Input EQ: None

#### • References

dBr G: 100.0 mVrms  
dBm (Output Power): 600.0 ohm  
W(watts) (Output Power): 8.000 ohm  
Shared Frequency Reference: 1.00000 kHz  
dBrA: 1.000 Vrms  
dBrB: 1.000 Vrms  
dBrA Offset: 0.000 dB  
dBrB Offset: 0.000 dB  
dBSPL1: 10.00 mVrms  
dBSPL2: 10.00 mVrms  
dBSPL1 Calibrator Level: 94.000 dB SPL  
dBSPL2 Calibrator Level: 94.000 dB SPL  
dBm (Input Power): 600.0 ohm  
W(watts) (Input Power): 8.000 ohm

#### • DCX

DC Output 1: 0.000 V  
DC Output 1: Off  
DC Output 2: 0.000 V  
DC Output 2: Off  
Port A (hex): 00  
Port B (hex): 00  
Port C (hex): 00

## Sequence Report



Port D (hex): 00

### Line Gain 0 600 Termination : Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 1:50:34.561 PM)

Ch1 290.9 mVrms

Gain (5/2/2023 1:50:34.561 PM)

Ch1 1.493 dB

THD+N Ratio (5/2/2023 1:50:34.561 PM)

Ch1 ---- %

Frequency (5/2/2023 1:50:34.561 PM)

Ch1 ---- Hz

### Line Gain 0 600 Termination : Level and Gain 0

Waveform: Sine

Generator Level: -10.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 1:50:38.282 PM)

Channel	Lower Limit	Value	Upper Limit	
Ch1	-1.500 dBu	-8.648 dBu	+1.500 dBu	

Result:  FAILED

## Sequence Report



### Line Gain +10 600 Termination : Signal Path Setup

Output Connector: Analog Balanced  
Channels: 1  
Source Impedance: 100 ohm  
Output EQ: None  
Input Connector: Analog Balanced  
Channels: 1  
Channel: Ch1  
Termination: 600 ohm  
Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)  
Device Delay: 0.000 s  
Input EQ: None

#### • References

dBr G: 100.0 mVrms  
dBm (Output Power): 600.0 ohm  
W(watts) (Output Power): 8.000 ohm  
Shared Frequency Reference: 1.00000 kHz  
dBrA: 1.000 Vrms  
dBrB: 1.000 Vrms  
dBrA Offset: 0.000 dB  
dBrB Offset: 0.000 dB  
dBSPL1: 10.00 mVrms  
dBSPL2: 10.00 mVrms  
dBSPL1 Calibrator Level: 94.000 dB SPL  
dBSPL2 Calibrator Level: 94.000 dB SPL  
dBm (Input Power): 600.0 ohm  
W(watts) (Input Power): 8.000 ohm

#### • DCX

DC Output 1: 0.000 V  
DC Output 1: Off  
DC Output 2: 0.000 V  
DC Output 2: Off  
Port A (hex): 00  
Port B (hex): 00  
Port C (hex): 00



## Sequence Report



Port D (hex): 00

### Line Gain +10 600 Termination : Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 1:50:43.526 PM)

Ch1 291.1 mVrms

Gain (5/2/2023 1:50:43.526 PM)

Ch1 1.498 dB

THD+N Ratio (5/2/2023 1:50:43.526 PM)

Ch1 ---- %

Frequency (5/2/2023 1:50:43.526 PM)

Ch1 ---- Hz

### Line Gain +10 600 Termination : Level and Gain +10

Waveform: Sine

Generator Level: -10.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 1:50:47.230 PM)

Channel	Lower Limit	Value	Upper Limit
Ch1	+8.500 dBu	-8.638 dBu	+11.500 dBu



Result: FAILED

## Sequence Report



Line Gain +10 200k Termination Level Hi : Signal Path Setup

Output Connector: Analog Balanced  
Channels: 1  
Source Impedance: 100 ohm  
Output EQ: None  
Input Connector: Analog Balanced  
Channels: 1  
Channel: Ch1  
Termination: 200 kohm  
Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)  
Device Delay: 0.000 s  
Input EQ: None

### • References

dBr G: 100.0 mVrms  
dBm (Output Power): 600.0 ohm  
W(watts) (Output Power): 8.000 ohm  
Shared Frequency Reference: 1.00000 kHz  
dBrA: 1.000 Vrms  
dBrB: 1.000 Vrms  
dBrA Offset: 0.000 dB  
dBrB Offset: 0.000 dB  
dBSPL1: 10.00 mVrms  
dBSPL2: 10.00 mVrms  
dBSPL1 Calibrator Level: 94.000 dB SPL  
dBSPL2 Calibrator Level: 94.000 dB SPL  
dBm (Input Power): 600.0 ohm  
W(watts) (Input Power): 8.000 ohm

### • DCX

DC Output 1: 0.000 V  
DC Output 1: Off  
DC Output 2: 0.000 V  
DC Output 2: Off  
Port A (hex): 00  
Port B (hex): 00  
Port C (hex): 00

5/2/2023 1:51 PM

Page 34 of 47

## Sequence Report



Port D (hex): 00

Line Gain +10 200k Termination Level Hi : Verify Connections

Waveform: Sine

Generator Level: -20.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 1:50:52.532 PM)

Ch1 291.0 mVrms

Gain (5/2/2023 1:50:52.532 PM)

Ch1 11.498 dB

THD+N Ratio (5/2/2023 1:50:52.532 PM)

Ch1 ---- %

Frequency (5/2/2023 1:50:52.532 PM)

Ch1 ---- Hz

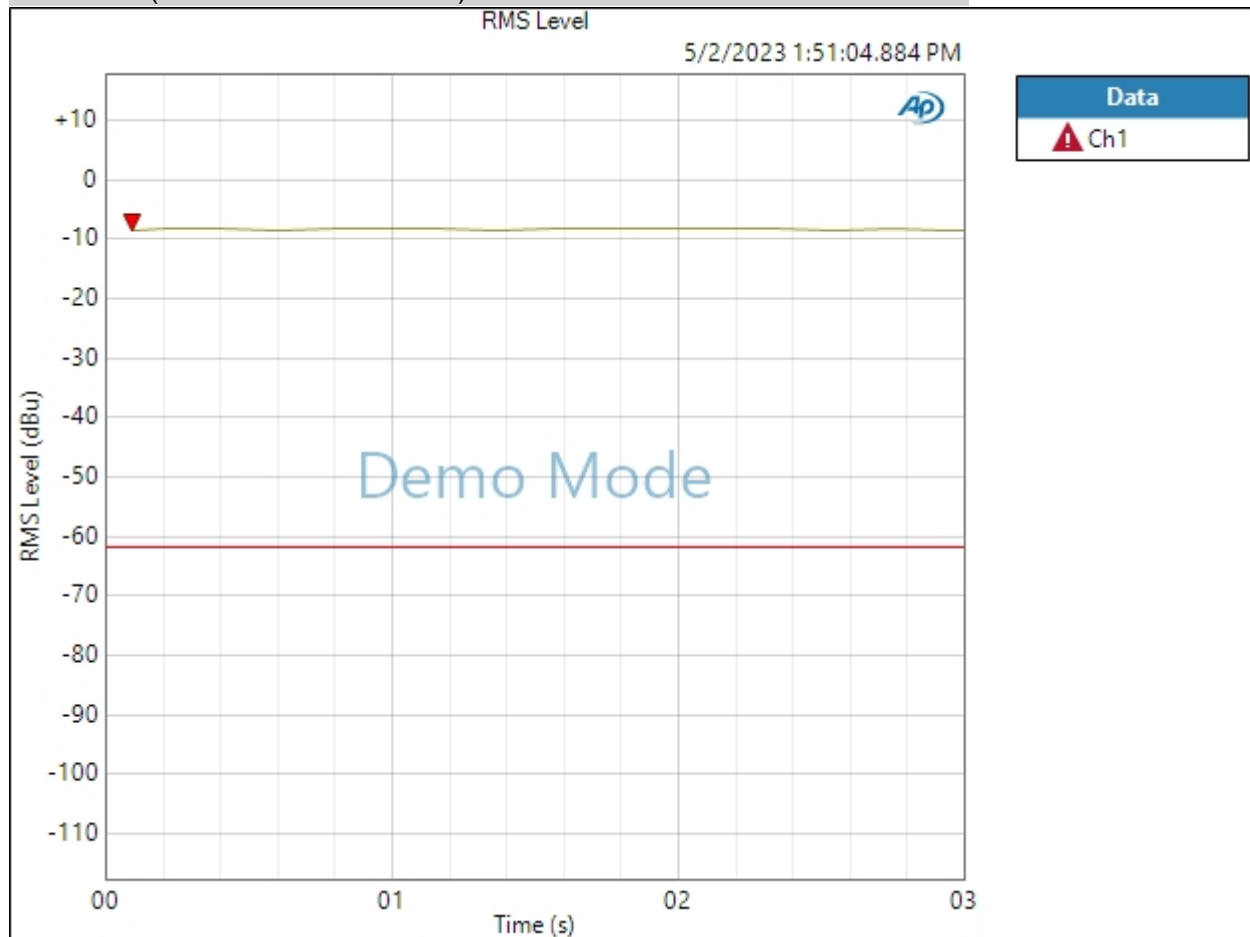
## Sequence Report



Line Gain +10 200k Termination Level Hi : Noise Recorder (RMS) CW

Waveform: None  
Low-pass Filter: 20 kHz  
Weighting Filter: Signal Path  
High-pass Filter: 20 Hz  
Sweep Time: 0.00:00:03.000  
Reading Rate: 10/sec  
Input Bandwidth: Use Signal Path  
Record Acquisition: False  
Measured 1 5/2/2023 1:51:04 PM

RMS Level (5/2/2023 1:51:04.884 PM)



Ch1 Failed Upper Limit

Result: FAILED

5/2/2023 1:51 PM

Page 36 of 47



## Sequence Report



Line Gain +10 200k Termination Level Low : Signal Path Setup

Output Connector: Analog Balanced  
Channels: 1  
Source Impedance: 100 ohm  
Output EQ: None  
Input Connector: Analog Balanced  
Channels: 1  
Channel: Ch1  
Termination: 200 kohm  
Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)  
Device Delay: 0.000 s  
Input EQ: None

### • References

dBr G: 100.0 mVrms  
dBm (Output Power): 600.0 ohm  
W(watts) (Output Power): 8.000 ohm  
Shared Frequency Reference: 1.00000 kHz  
dBrA: 1.000 Vrms  
dBrB: 1.000 Vrms  
dBrA Offset: 0.000 dB  
dBrB Offset: 0.000 dB  
dBSPL1: 10.00 mVrms  
dBSPL2: 10.00 mVrms  
dBSPL1 Calibrator Level: 94.000 dB SPL  
dBSPL2 Calibrator Level: 94.000 dB SPL  
dBm (Input Power): 600.0 ohm  
W(watts) (Input Power): 8.000 ohm

### • DCX

DC Output 1: 0.000 V  
DC Output 1: Off  
DC Output 2: 0.000 V  
DC Output 2: Off  
Port A (hex): 00  
Port B (hex): 00  
Port C (hex): 00

5/2/2023 1:51 PM

Page 38 of 47

## Sequence Report



Port D (hex): 00

### Line Gain +10 200k Termination Level Low : Verify Connections

Waveform: Sine

Generator Level: -20.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 1:51:10.441 PM)

Ch1 295.1 mVrms

Gain (5/2/2023 1:51:10.441 PM)

Ch1 11.618 dB

THD+N Ratio (5/2/2023 1:51:10.441 PM)

Ch1 ---- %

Frequency (5/2/2023 1:51:10.441 PM)

Ch1 ---- Hz

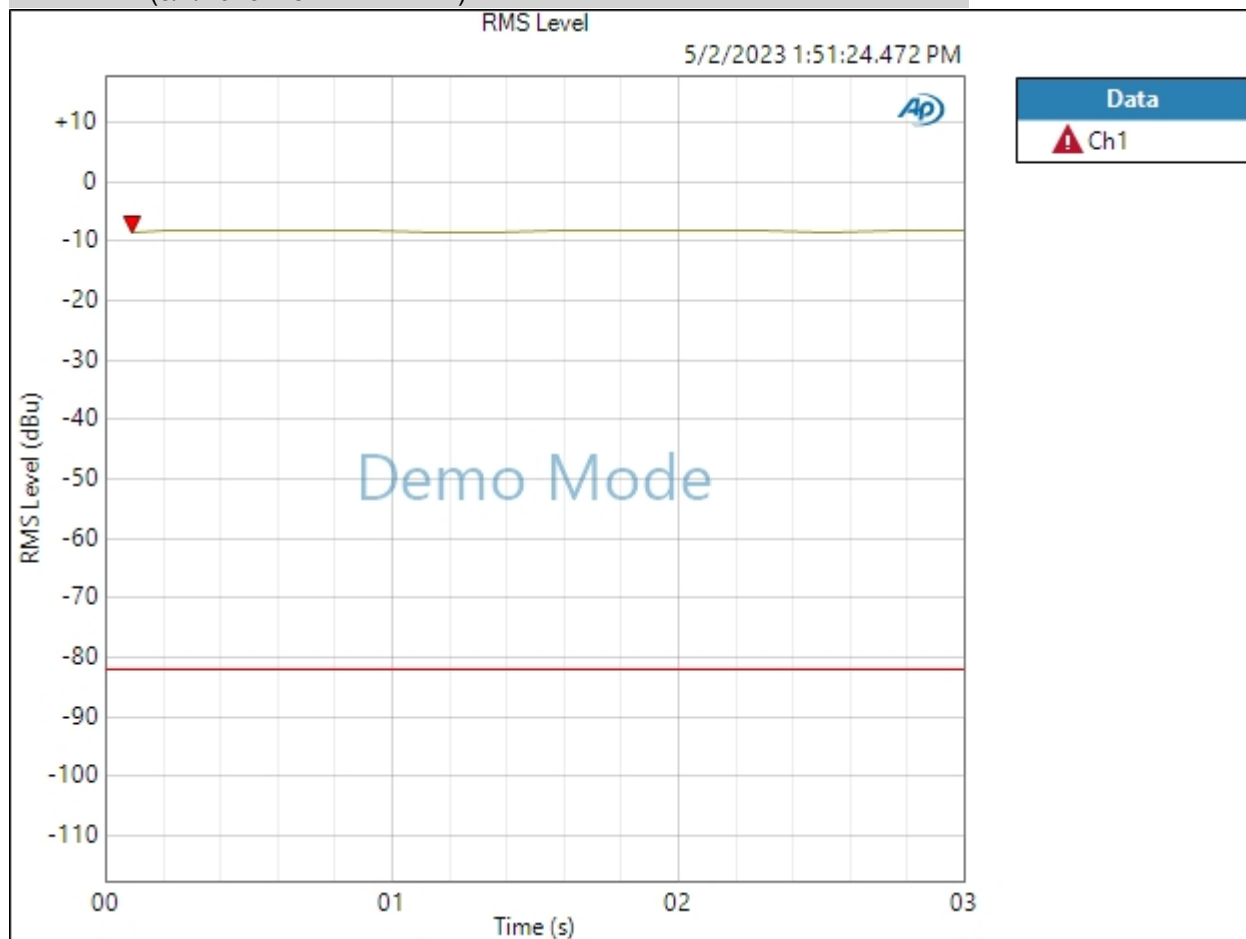
## Sequence Report



Line Gain +10 200k Termination Level Low : Noise Recorder (RMS) CCW

Waveform: None  
Low-pass Filter: 20 kHz  
Weighting Filter: Signal Path  
High-pass Filter: 20 Hz  
Sweep Time: 0.00:00:03.000  
Reading Rate: 10/sec  
Input Bandwidth: Use Signal Path  
Record Acquisition: False  
Measured 1 5/2/2023 1:51:24 PM

RMS Level (5/2/2023 1:51:24.472 PM)



Ch1 Failed Upper Limit

Result: FAILED

5/2/2023 1:51 PM

Page 40 of 47





## Sequence Report



### Hi Z Gain -10 2.2M 200k Termination : Signal Path Setup

Output Connector: Analog Balanced  
Channels: 2  
Source Impedance: 100 ohm  
Output EQ: None  
Input Connector: Analog Balanced  
Channels: 1  
Channel: Ch1  
Termination: 200 kohm  
Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)  
Device Delay: 0.000 s  
Input EQ: None

#### • References

dBr G: 100.0 mVrms  
dBm (Output Power): 600.0 ohm  
W(watts) (Output Power): 8.000 ohm  
Shared Frequency Reference: 1.00000 kHz  
dBrA: 1.000 Vrms  
dBrB: 1.000 Vrms  
dBrA Offset: 0.000 dB  
dBrB Offset: 0.000 dB  
dBSPL1: 10.00 mVrms  
dBSPL2: 10.00 mVrms  
dBSPL1 Calibrator Level: 94.000 dB SPL  
dBSPL2 Calibrator Level: 94.000 dB SPL  
dBm (Input Power): 600.0 ohm  
W(watts) (Input Power): 8.000 ohm

#### • DCX

DC Output 1: 0.000 V  
DC Output 1: Off  
DC Output 2: 0.000 V  
DC Output 2: Off  
Port A (hex): 00  
Port B (hex): 00  
Port C (hex): 00

5/2/2023 1:51 PM

Page 42 of 47

## Sequence Report



Port D (hex): 00

### Hi Z Gain -10 2.2M 200k Termination : Verify Connections

Waveform: Sine

Generator Level: -22.300 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 1:51:29.670 PM)

Ch1 286.7 mVrms

Gain (5/2/2023 1:51:29.670 PM)

Ch1 13.668 dB

THD+N Ratio (5/2/2023 1:51:29.670 PM)

Ch1 ---- %

Frequency (5/2/2023 1:51:29.670 PM)

Ch1 ---- Hz

### Hi Z Gain -10 2.2M 200k Termination : Level and Gain 2.2M

Waveform: Sine

Generator Level: -22.300 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 1:51:34.841 PM)

Channel	Lower Limit	Value	Upper Limit	
Ch1	-2.000 dBu	-8.609 dBu	+2.000 dBu	

Result:  FAILED

## Sequence Report



Hi Z Gain -10 47k 200k Termination : Signal Path Setup

Output Connector: Analog Unbalanced  
Channels: 2  
Source Impedance: 50 ohm  
Output EQ: None  
Input Connector: Analog Balanced  
Channels: 1  
Channel: Ch1  
Termination: 200 kohm  
Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)  
Device Delay: 0.000 s  
Input EQ: None

### • References

dBr G: 100.0 mVrms  
dBm (Output Power): 600.0 ohm  
W(watts) (Output Power): 8.000 ohm  
Shared Frequency Reference: 1.00000 kHz  
dBrA: 1.000 Vrms  
dBrB: 1.000 Vrms  
dBrA Offset: 0.000 dB  
dBrB Offset: 0.000 dB  
dBSPL1: 10.00 mVrms  
dBSPL2: 10.00 mVrms  
dBSPL1 Calibrator Level: 94.000 dB SPL  
dBSPL2 Calibrator Level: 94.000 dB SPL  
dBm (Input Power): 600.0 ohm  
W(watts) (Input Power): 8.000 ohm

### • DCX

DC Output 1: 0.000 V  
DC Output 1: Off  
DC Output 2: 0.000 V  
DC Output 2: Off  
Port A (hex): 00  
Port B (hex): 00  
Port C (hex): 00

5/2/2023 1:51 PM

Page 44 of 47

## Sequence Report



Port D (hex): 00

### Hi Z Gain -10 47k 200k Termination : Verify Connections

Waveform: Sine

Generator Level: -22.300 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 1:51:40.208 PM)

Ch1 292.8 mVrms

Gain (5/2/2023 1:51:40.208 PM)

Ch1 13.849 dB

THD+N Ratio (5/2/2023 1:51:40.208 PM)

Ch1 ---- %

Frequency (5/2/2023 1:51:40.208 PM)

Ch1 ---- Hz

### Hi Z Gain -10 47k 200k Termination : Level and Gain 47K

Waveform: Sine

Generator Level: -22.300 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 1:51:44.024 PM)

Channel	Lower Limit	Value	Upper Limit	
Ch1	-8.000 dBu	-8.531 dBu	-4.000 dBu	

Result:  FAILED

## Sequence Report



### Dummy Signal Path For Report : Signal Path Setup

Output Connector:	Analog Unbalanced
Channels:	2
Source Impedance:	50 ohm
Output EQ:	None
Input Connector:	Analog Unbalanced
Channels:	2
Termination:	100 kohm
Input Bandwidth:	AC (<10 Hz) - 90k (192 kHz SR)
Device Delay:	0.000 s
Input EQ:	None

#### • References

dBr G:	100.0 mVrms
dBm (Output Power):	600.0 ohm
W(watts) (Output Power):	8.000 ohm
Shared Frequency Reference:	1.00000 kHz
dBrA:	1.000 Vrms
dBrB:	1.000 Vrms
dBrA Offset:	0.000 dB
dBrB Offset:	0.000 dB
dB SPL1:	10.00 mVrms
dB SPL2:	10.00 mVrms
dB SPL1 Calibrator Level:	94.000 dB SPL
dB SPL2 Calibrator Level:	94.000 dB SPL
dBm (Input Power):	600.0 ohm
W(watts) (Input Power):	8.000 ohm

#### • DCX

DC Output 1:	0.000 V
DC Output 1:	Off
DC Output 2:	0.000 V
DC Output 2:	Off
Port A (hex):	00
Port B (hex):	00
Port C (hex):	00
Port D (hex):	00

## Sequence Report



### Dummy Signal Path For Report : Verify Connections

Waveform: Sine  
Generator Level: 100.0 mVrms  
DC Offset: 0.000 V  
Frequency: 1.00000 kHz

### RMS Level (5/2/2023 1:51:47.605 PM)

Ch1 290.0 mVrms  
Ch2 291.4 mVrms