

## Summary

Mi - 500 0001- T	
Mic500 200k Termination	PAGGED.
Signal Path Setup	♥ PASSED
Stepped Frequency Sweep MIC 500	<b>♥</b> PASSED
Mic 2k 200k termination	<b>* D 1 0 0 T D</b>
Signal Path Setup	♥ PASSED
Stepped Frequency Sweep MIC 2K	▼ PASSED
Mic 2k 15dB PAD 200k termination	
Signal Path Setup	♥ PASSED
Stepped Frequency Sweep 15dB PAD	▲ FAILED
Line Gain -10 200kTermination	
Signal Path Setup	PASSED
Stepped Frequency Sweep -10	<b>ॐ</b> PASSED
Line Gain -10 600 Termination	
Signal Path Setup	PASSED
Level and Gain -10	<b>ॐ</b> PASSED
Line Gain +5 200kTermination	
Signal Path Setup	PASSED
Stepped Frequency Sweep +5	PASSED
Line Gain +5 600 Termination	
Signal Path Setup	PASSED
Level and Gain +5	PASSED
Line Gain -5 600 Termination	
Signal Path Setup	PASSED
Level and Gain -5	PASSED
Line Gain 0 600 Termination	
Signal Path Setup	PASSED
Level and Gain 0	PASSED
Line Gain +10 600 Termination	
Signal Path Setup	PASSED
Level and Gain +10	PASSED
Line Gain +10 200k Termination Level Hi	
Signal Path Setup	PASSED
Noise Recorder (RMS) CW	🛕 FAILED
Line Gain +10 200k Termination Level Low	
Signal Path Setup	PASSED
Noise Recorder (RMS) CCW	PASSED
Hi Z Gain -10 2.2M 200k Termination	

Signal Path Setup Level and Gain 2.2M	<ul><li>PASSED</li><li>PASSED</li></ul>
Hi Z Gain -10 47k 200k Termination	
Signal Path Setup	PASSED
Level and Gain 47K	PASSED
Dummy Signal Path For Report	
Signal Path Setup	PASSED
Sequence Result:	
Sequence Result: 🛕 FAILED	

5/25/2023 4:56 PM Page 2 of 45



Mic500 200k Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms 10.00 mVrms dBSPL2: dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

DCX is not detected.

W(watts) (Input Power):

5/25/2023 4:56 PM Page 3 of 45



Mic500 200k Termination : Verify Connections

Waveform: Sine

Generator Level: -42.300 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/25/2023 4:53:36.382 PM)

Ch1 879.8 mVrms

Gain (5/25/2023 4:53:36.382 PM)

Ch1 43.407 dB

THD+N Ratio (5/25/2023 4:53:36.382 PM)

Ch1 0.202216 %

Frequency (5/25/2023 4:53:36.382 PM)

Ch1 1.00000 kHz

5/25/2023 4:56 PM Page 4 of 45



Mic500 200k Termination: Stepped Frequency Sweep MIC 500

Generator Level: -42.300 dBu
DC Offset: 0.000 V
EQ: None

Start Frequency: 20.0000 kHz
Stop Frequency: 20.0000 Hz
Step Type: Logarithmic

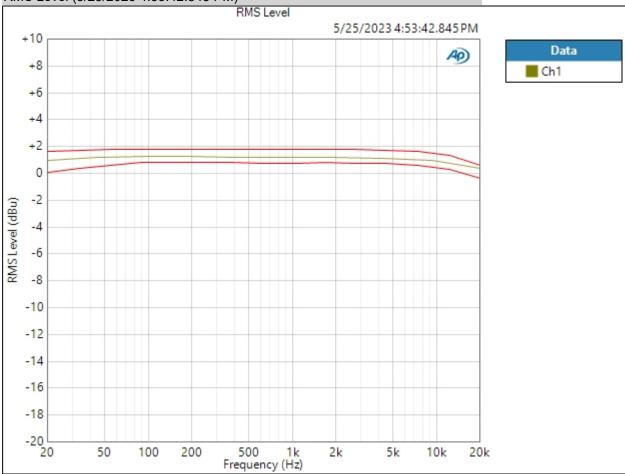
Number of Points: 10

Weighting Filter: Signal Path

High-pass Filter: 20 Hz Phase Ref Channel: Ch1

Measured 1 5/25/2023 4:53:42 PM

#### RMS Level (5/25/2023 4:53:42.845 PM)



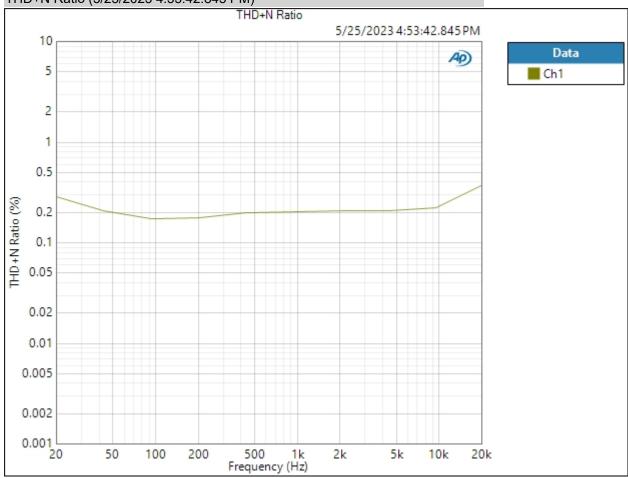
Ch1 🔮 PASSED

5/25/2023 4:56 PM Page 5 of 45



Result: V PASSED

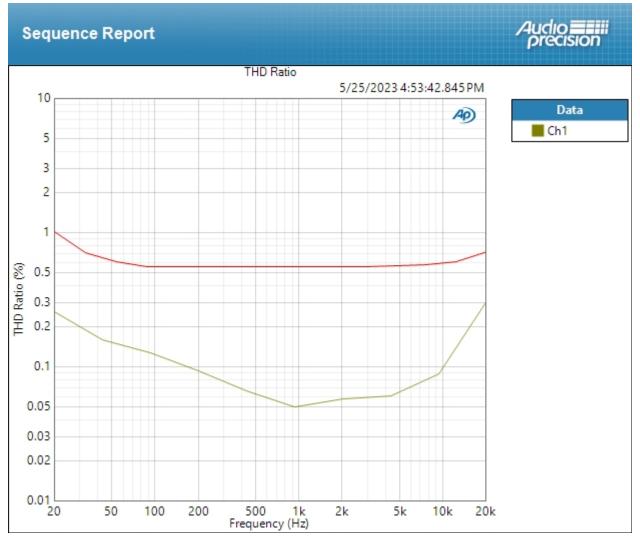
THD+N Ratio (5/25/2023 4:53:42.845 PM)



Result: V PASSED

THD Ratio (5/25/2023 4:53:42.845 PM)

5/25/2023 4:56 PM Page 6 of 45



Ch1 🔮 PASSED

Result: V PASSED

5/25/2023 4:56 PM Page 7 of 45



Mic 2k 200k termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms 10.00 mVrms dBSPL2: dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL

• DCX

DCX is not detected.

dBm (Input Power):

W(watts) (Input Power):

5/25/2023 4:56 PM Page 8 of 45

600.0 ohm



Mic 2k 200k termination : Verify Connections

Waveform: Sine

Generator Level: -42.300 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/25/2023 4:53:48.476 PM)

Ch1 512.0 mVrms

Gain (5/25/2023 4:53:48.476 PM)

Ch1 38.704 dB

THD+N Ratio (5/25/2023 4:53:48.476 PM)

Ch1 0.313530 %

Frequency (5/25/2023 4:53:48.476 PM)

Ch1 1.00000 kHz

5/25/2023 4:56 PM Page 9 of 45



Mic 2k 200k termination : Stepped Frequency Sweep MIC 2K

Generator Level: -42.300 dBu
DC Offset: 0.000 V
EQ: None

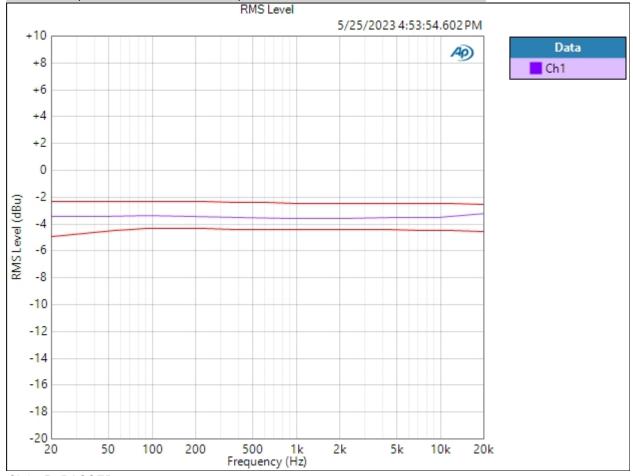
Start Frequency: 20.0000 kHz
Stop Frequency: 20.0000 Hz
Step Type: Logarithmic

Number of Points: 10

Weighting Filter: Signal Path
High-pass Filter: 20 Hz
Phase Ref Channel: Ch1

Measured 1 5/25/2023 4:53:54 PM

#### RMS Level (5/25/2023 4:53:54.602 PM)



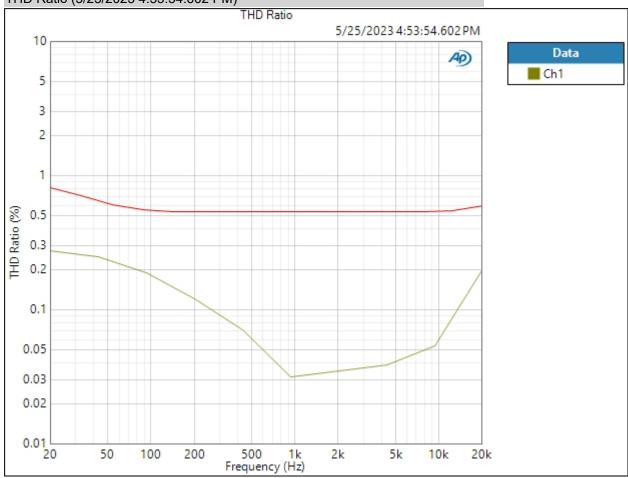
Ch1 🔮 PASSED

5/25/2023 4:56 PM Page 10 of 45



Result: V PASSED

THD Ratio (5/25/2023 4:53:54.602 PM)



Ch1 🔮 PASSED

Result: V PASSED

5/25/2023 4:56 PM Page 11 of 45



Mic 2k 15dB PAD 200k termination : Signal Path Setup
Output Connector:

Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1
Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm W(watts) (Input Power): 8.000 ohm

• DCX

DCX is not detected.

5/25/2023 4:56 PM Page 12 of 45



Mic 2k 15dB PAD 200k termination : Verify Connections

Waveform: Sine

Generator Level: -42.300 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/25/2023 4:54:00.332 PM)

Ch1 75.64 mVrms

Gain (5/25/2023 4:54:00.332 PM)

Ch1 22.094 dB

THD+N Ratio (5/25/2023 4:54:00.332 PM)

Ch1 2.341244 %

Frequency (5/25/2023 4:54:00.332 PM)

Ch1 1.00000 kHz

5/25/2023 4:56 PM Page 13 of 45



#### Mic 2k 15dB PAD 200k termination : Stepped Frequency Sweep 15dB PAD

Generator Level: -42.000 dBu
DC Offset: 0.000 V
EQ: None

Start Frequency: 20.0000 kHz
Stop Frequency: 20.0000 Hz
Step Type: Logarithmic

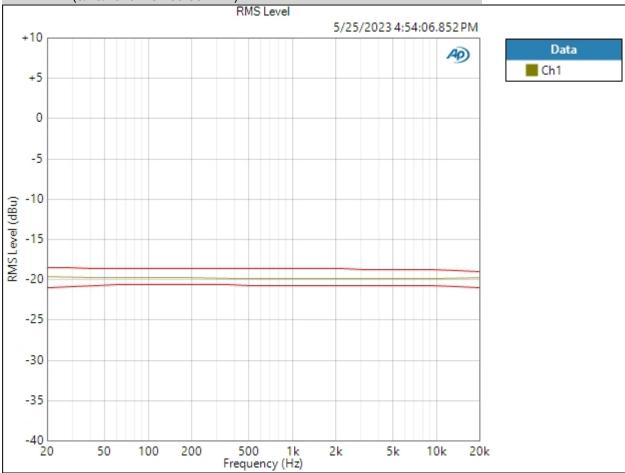
Number of Points: 10

Weighting Filter: Signal Path High-pass Filter: 20 Hz

Phase Ref Channel: Ch1

Measured 1 5/25/2023 4:54:06 PM

#### RMS Level (5/25/2023 4:54:06.852 PM)



Ch1 🔮 PASSED

5/25/2023 4:56 PM Page 14 of 45



Result: V PASSED

THD Ratio (5/25/2023 4:54:06.852 PM)



Ch1 A Failed Upper Limit

Result: A FAILED

5/25/2023 4:56 PM Page 15 of 45



Line Gain -10 200kTermination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

DCX is not detected.

W(watts) (Input Power):

5/25/2023 4:56 PM Page 16 of 45



Line Gain -10 200kTermination : Verify Connections

Waveform: Sine

Generator Level: 0.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/25/2023 4:54:12.616 PM)

Ch1 0.902 Vrms

Gain (5/25/2023 4:54:12.616 PM)

Ch1 1.325 dB

THD+N Ratio (5/25/2023 4:54:12.616 PM)

Ch1 0.212957 %

Frequency (5/25/2023 4:54:12.616 PM)

Ch1 1.00000 kHz

5/25/2023 4:56 PM Page 17 of 45



#### Line Gain -10 200kTermination: Stepped Frequency Sweep -10

Generator Level: 0.000 dBu
DC Offset: 0.000 V
EQ: None

Start Frequency: 20.0000 kHz
Stop Frequency: 20.0000 Hz
Step Type: Logarithmic

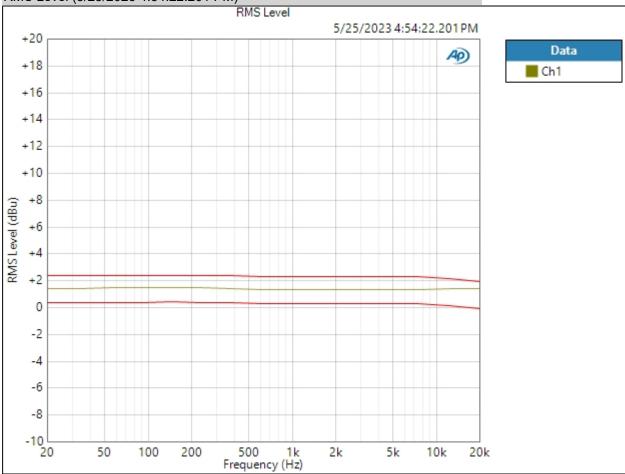
Number of Points: 15

Weighting Filter: Signal Path High-pass Filter: 20 Hz

Phase Ref Channel: Ch1

Measured 1 5/25/2023 4:54:22 PM

#### RMS Level (5/25/2023 4:54:22.201 PM)

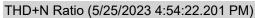


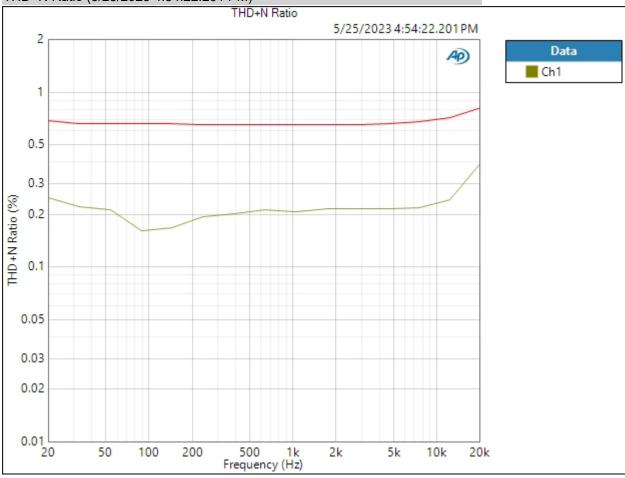
Ch1 🔮 PASSED

5/25/2023 4:56 PM Page 18 of 45



Result: V PASSED





Ch1 🔮 PASSED

Result: V PASSED

5/25/2023 4:56 PM Page 19 of 45



Line Gain -10 600 Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 600 ohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

DCX is not detected.

W(watts) (Input Power):

5/25/2023 4:56 PM Page 20 of 45



Line Gain -10 600 Termination: Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/25/2023 4:54:28.169 PM)

Ch1 250.7 mVrms

Gain (5/25/2023 4:54:28.169 PM)

Ch1 0.201 dB

THD+N Ratio (5/25/2023 4:54:28.169 PM)

Ch1 0.603330 %

Frequency (5/25/2023 4:54:28.169 PM)

Ch1 1.00000 kHz

Line Gain -10 600 Termination: Level and Gain -10

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/25/2023 4:54:31.282 PM)

Result: V PASSED

5/25/2023 4:56 PM Page 21 of 45



Line Gain +5 200kTermination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

DCX is not detected.

W(watts) (Input Power):

5/25/2023 4:56 PM Page 22 of 45



Line Gain +5 200kTermination : Verify Connections

Waveform: Sine

Generator Level: 0.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/25/2023 4:54:37.027 PM)

Ch1 4.857 Vrms

Gain (5/25/2023 4:54:37.027 PM)

Ch1 15.947 dB

THD+N Ratio (5/25/2023 4:54:37.027 PM)

Ch1 0.311912 %

Frequency (5/25/2023 4:54:37.027 PM)

Ch1 1.00000 kHz

5/25/2023 4:56 PM Page 23 of 45



#### Line Gain +5 200kTermination : Stepped Frequency Sweep +5

Generator Level: 0.000 dBu
DC Offset: 0.000 V
EQ: None

Start Frequency: 20.0000 kHz
Stop Frequency: 20.0000 Hz
Step Type: Logarithmic

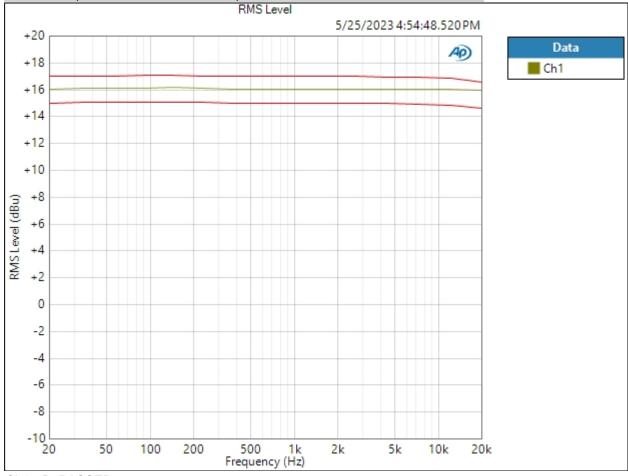
Number of Points: 15

Weighting Filter: Signal Path

High-pass Filter: 20 Hz Phase Ref Channel: Ch1

Measured 1 5/25/2023 4:54:48 PM

#### RMS Level (5/25/2023 4:54:48.520 PM)



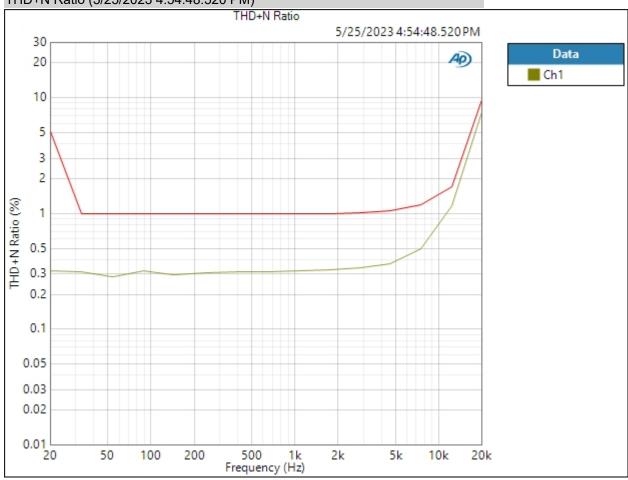
Ch1 🔮 PASSED

5/25/2023 4:56 PM Page 24 of 45



Result: V PASSED

THD+N Ratio (5/25/2023 4:54:48.520 PM)



Ch1 🔮 PASSED

Result: V PASSED

5/25/2023 4:56 PM Page 25 of 45



Line Gain +5 600 Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 600 ohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms 10.00 mVrms dBSPL2: dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

DCX is not detected.

W(watts) (Input Power):

5/25/2023 4:56 PM Page 26 of 45



Line Gain +5 600 Termination : Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/25/2023 4:54:54.851 PM)

Ch1 1.352 Vrms

Gain (5/25/2023 4:54:54.851 PM)

Ch1 14.839 dB

THD+N Ratio (5/25/2023 4:54:54.851 PM)

Ch1 0.603066 %

Frequency (5/25/2023 4:54:54.851 PM)

Ch1 1.00000 kHz

Line Gain +5 600 Termination: Level and Gain +5

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/25/2023 4:54:58.041 PM)

Channel Lower Limit Value Upper Limit
Ch1 +3.500 dBu +4.838 dBu +6.500 dBu ❖

Result: V PASSED

5/25/2023 4:56 PM Page 27 of 45



Line Gain -5 600 Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 600 ohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms 10.00 mVrms dBSPL2: dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

DCX is not detected.

W(watts) (Input Power):

5/25/2023 4:56 PM Page 28 of 45



Line Gain -5 600 Termination: Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/25/2023 4:55:04.021 PM)

Ch1 419.4 mVrms

Gain (5/25/2023 4:55:04.021 PM)

Ch1 4.670 dB

THD+N Ratio (5/25/2023 4:55:04.021 PM)

Ch1 0.602395 %

Frequency (5/25/2023 4:55:04.021 PM)

Ch1 1.00000 kHz

Line Gain -5 600 Termination: Level and Gain -5

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/25/2023 4:55:07.312 PM)

Result: V PASSED

5/25/2023 4:56 PM Page 29 of 45



Line Gain 0 600 Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 600 ohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms 10.00 mVrms dBSPL2: dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL

• DCX

DCX is not detected.

dBm (Input Power):

W(watts) (Input Power):

5/25/2023 4:56 PM Page 30 of 45

600.0 ohm



Line Gain 0 600 Termination: Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/25/2023 4:55:13.351 PM)

Ch1 731.3 mVrms

Gain (5/25/2023 4:55:13.351 PM)

Ch1 9.500 dB

THD+N Ratio (5/25/2023 4:55:13.351 PM)

Ch1 0.569891 %

Frequency (5/25/2023 4:55:13.351 PM)

Ch1 1.00000 kHz

Line Gain 0 600 Termination: Level and Gain 0

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/25/2023 4:55:16.574 PM)

Result: V PASSED

5/25/2023 4:56 PM Page 31 of 45



Line Gain +10 600 Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 600 ohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL

• DCX

DCX is not detected.

dBm (Input Power):

W(watts) (Input Power):

5/25/2023 4:56 PM Page 32 of 45

600.0 ohm



Line Gain +10 600 Termination: Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/25/2023 4:55:22.517 PM)

Ch1 2.383 Vrms

Gain (5/25/2023 4:55:22.517 PM)

Ch1 19.762 dB

THD+N Ratio (5/25/2023 4:55:22.517 PM)

Ch1 0.683125 %

Frequency (5/25/2023 4:55:22.517 PM)

Ch1 1.00000 kHz

Line Gain +10 600 Termination: Level and Gain +10

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/25/2023 4:55:25.818 PM)

Result: V PASSED

5/25/2023 4:56 PM Page 33 of 45



Line Gain +10 200k Termination Level Hi : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms 10.00 mVrms dBSPL2: dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

DCX is not detected.

W(watts) (Input Power):

5/25/2023 4:56 PM Page 34 of 45



Line Gain +10 200k Termination Level Hi: Verify Connections

Waveform: Sine

Generator Level: -20.000 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/25/2023 4:55:31.808 PM)

Ch1 858.3 mVrms

Gain (5/25/2023 4:55:31.808 PM)

Ch1 20.892 dB

THD+N Ratio (5/25/2023 4:55:31.808 PM)

Ch1 1.754925 %

Frequency (5/25/2023 4:55:31.808 PM)

Ch1 1.00000 kHz

5/25/2023 4:56 PM Page 35 of 45



#### Line Gain +10 200k Termination Level Hi: Noise Recorder (RMS) CW

Waveform: None
Low-pass Filter: 20 kHz
Weighting Filter: Signal Path

High-pass Filter: 20 Hz

Sweep Time: 0.00:00:03.000

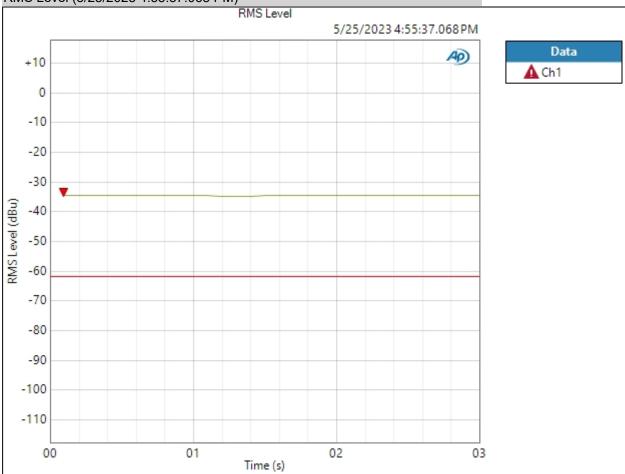
Reading Rate: 10/sec

Input Bandwidth: Use Signal Path

Record Acquisition: False

Measured 1 5/25/2023 4:55:37 PM

## RMS Level (5/25/2023 4:55:37.068 PM)



Result: A FAILED

5/25/2023 4:56 PM Page 36 of 45



Line Gain +10 200k Termination Level Low: Signal Path Setup

Output Connector: Analog Balanced

Channels:

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm W(watts) (Input Power): 8.000 ohm

• DCX

DCX is not detected.

5/25/2023 4:56 PM Page 37 of 45



Line Gain +10 200k Termination Level Low: Verify Connections

Waveform: Sine

Generator Level: -20.000 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/25/2023 4:55:43.027 PM)

Ch1 153.1 uVrms

Gain (5/25/2023 4:55:43.027 PM)

Ch1 -54.088 dB

THD+N Ratio (5/25/2023 4:55:43.027 PM)

Ch1 ---- %

Frequency (5/25/2023 4:55:43.027 PM)

Ch1 ---- Hz

5/25/2023 4:56 PM Page 38 of 45



#### Line Gain +10 200k Termination Level Low: Noise Recorder (RMS) CCW

Waveform: None
Low-pass Filter: 20 kHz
Weighting Filter: Signal Path

High-pass Filter: 20 Hz

Sweep Time: 0.00:00:03.000

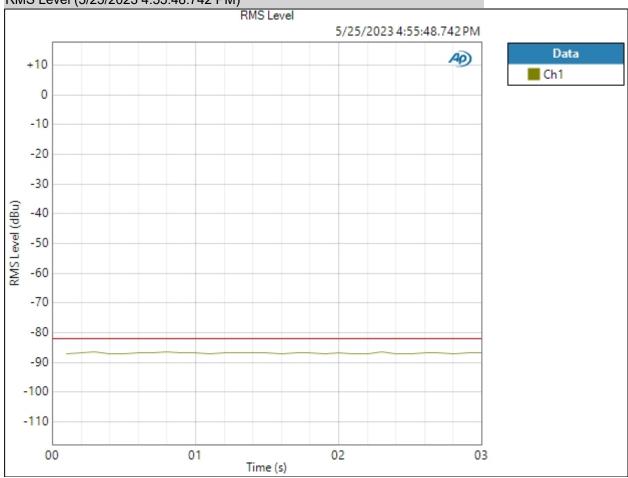
Reading Rate: 10/sec

Input Bandwidth: Use Signal Path

Record Acquisition: False

Measured 1 5/25/2023 4:55:48 PM

#### RMS Level (5/25/2023 4:55:48.742 PM)



Ch1 S PASSED

Result: V PASSED

5/25/2023 4:56 PM Page 39 of 45



Hi Z Gain -10 2.2M 200k Termination : Signal Path Setup

Output Connector: Analog Unbalanced

Channels: 2

Source Impedance: 50 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

DCX is not detected.

W(watts) (Input Power):

5/25/2023 4:56 PM Page 40 of 45



Hi Z Gain -10 2.2M 200k Termination: Verify Connections

Waveform: Sine

Generator Level: -22.300 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/25/2023 4:55:54.387 PM)

Ch1 2.945 mVrms

Gain (5/25/2023 4:55:54.387 PM)

Ch1 -26.070 dB

THD+N Ratio (5/25/2023 4:55:54.387 PM)

Ch1 ---- %

Frequency (5/25/2023 4:55:54.387 PM)

Ch1 ---- Hz

Hi Z Gain -10 2.2M 200k Termination: Level and Gain 2.2M

Waveform: Sine

Generator Level: -22.300 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/25/2023 4:55:58.364 PM)

Channel Lower Limit Value Upper Limit
Ch1 -2.000 dBu +0.051 dBu +2.000 dBu 

✓

Result: V PASSED

5/25/2023 4:56 PM Page 41 of 45



Hi Z Gain -10 47k 200k Termination : Signal Path Setup

Output Connector: Analog Unbalanced

Channels: 2

Source Impedance: 50 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms

dBSPL1 Calibrator Level: 94.000 dBSPL
dBSPL2 Calibrator Level: 94.000 dBSPL
dBm (Input Power): 600.0 ohm
W(watts) (Input Power): 8.000 ohm

• DCX

DCX is not detected.

5/25/2023 4:56 PM Page 42 of 45



Hi Z Gain -10 47k 200k Termination : Verify Connections

Waveform: Sine

Generator Level: -22.300 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/25/2023 4:56:04.300 PM)

Ch1 398.0 mVrms

Gain (5/25/2023 4:56:04.300 PM)

Ch1 16.517 dB

THD+N Ratio (5/25/2023 4:56:04.300 PM)

Ch1 0.805276 %

Frequency (5/25/2023 4:56:04.300 PM)

Ch1 1.00000 kHz

Hi Z Gain -10 47k 200k Termination: Level and Gain 47K

Waveform: Sine

Generator Level: -22.300 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/25/2023 4:56:07.514 PM)

Result: V PASSED

5/25/2023 4:56 PM Page 43 of 45



Dummy Signal Path For Report : Signal Path Setup

Output Connector: Analog Unbalanced

Channels: 2

Source Impedance: 50 ohm
Output EQ: None

Input Connector: Analog Unbalanced

Channels: 2

Termination: 100 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms 94.000 dBSPL dBSPL1 Calibrator Level: dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

DCX is not detected.

W(watts) (Input Power):

5/25/2023 4:56 PM Page 44 of 45



Dummy Signal Path For Report : Verify Connections

Waveform: Sine

Generator Level: 100.0 mVrms

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/25/2023 4:56:11.594 PM)

Ch1 70.23 uVrms Ch2 7.435 uVrms

5/25/2023 4:56 PM Page 45 of 45