

Summary

Mic500 200k Termination	
Signal Path Setup	🍼 PASSED
Stepped Frequency Sweep MIC 500	🛕 FAILED
Mic 2k 200k termination	
Signal Path Setup	PASSED
Stepped Frequency Sweep MIC 2K	▲ FAILED
Mic 2k 15dB PAD 200k termination	
Signal Path Setup	PASSED
Stepped Frequency Sweep 15dB PAD	🛕 FAILED
Line Gain -10 200kTermination	
Signal Path Setup	PASSED
Stepped Frequency Sweep -10	▲ FAILED
Line Gain -10 600 Termination	
Signal Path Setup	PASSED
Level and Gain -10	PASSED
Line Gain +5 200kTermination	
Signal Path Setup	PASSED
Stepped Frequency Sweep +5	▲ FAILED
Line Gain +5 600 Termination	
Signal Path Setup	PASSED
Level and Gain +5	▲ FAILED
Line Gain -5 600 Termination	
Signal Path Setup	PASSED
Level and Gain -5	▲ FAILED
Line Gain 0 600 Termination	
Signal Path Setup	PASSED
Level and Gain 0	▲ FAILED
Line Gain +10 600 Termination	
Signal Path Setup	PASSED
Level and Gain +10	▲ FAILED
Line Gain +10 200k Termination Level Hi	
Signal Path Setup	PASSED
Noise Recorder (RMS) CW	▲ FAILED
Line Gain +10 200k Termination Level Low	
Signal Path Setup	PASSED
Noise Recorder (RMS) CCW	▲ FAILED
Hi Z Gain -10 2.2M 200k Termination	

Signal Path Setup Level and Gain 2.2M	♥ PASSED ♠ FAILED
Hi Z Gain -10 47k 200k Termination	
Signal Path Setup Level and Gain 47K	▼ PASSED ▲ FAILED
Dummy Signal Path For Report	
Signal Path Setup	PASSED
Sequence Result:	
Sequence Result: 🛕 FAILED	

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Mic500 200k Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
AG52 Generator Option: Installed
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

100.0 mVrms dBr G: dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

W(watts) (Input Power):

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

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8.000 ohm



Port C (hex): 00
Port D (hex): 00

Clocks

Output Rate: Track Output SR

Sync Out Level: 3.300 V
Sync Out Polarity: Normal
Timebase Reference: Internal
Jitter: Disabled

• Triggers

Source: Off
Input Logic Level: 3.300 V
Edge: Rising

Mic500 200k Termination: Verify Connections

Waveform: Sine

Generator Level: -42.300 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/2/2023 8:15:27.443 PM)

Ch1 287.6 mVrms

Gain (5/2/2023 8:15:27.443 PM)

Ch1 33.695 dB

THD+N Ratio (5/2/2023 8:15:27.443 PM)

Ch1 ---- %

Frequency (5/2/2023 8:15:27.443 PM)

Ch1 ---- Hz

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Mic500 200k Termination: Stepped Frequency Sweep MIC 500

Generator Level: -42.300 dBu
DC Offset: 0.000 V
EQ: None

Start Frequency: 20.0000 kHz
Stop Frequency: 20.0000 Hz
Step Type: Logarithmic

Number of Points: 10

Weighting Filter: Signal Path

High-pass Filter: 20 Hz Phase Ref Channel: Ch1

Measured 1 5/2/2023 8:15:37 PM

RMS Level (5/2/2023 8:15:37.332 PM)

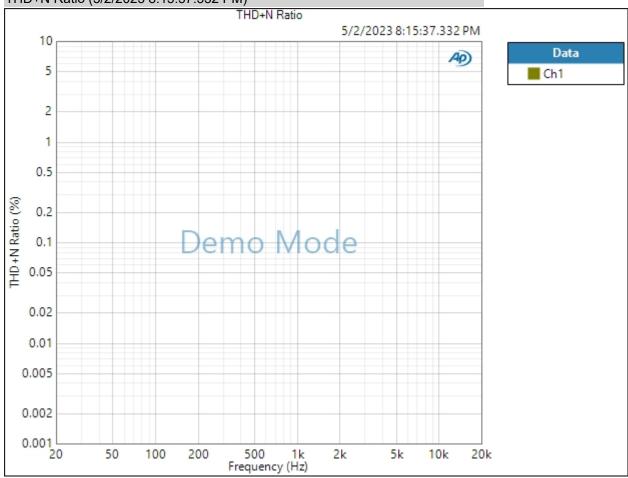


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Result: A FAILED

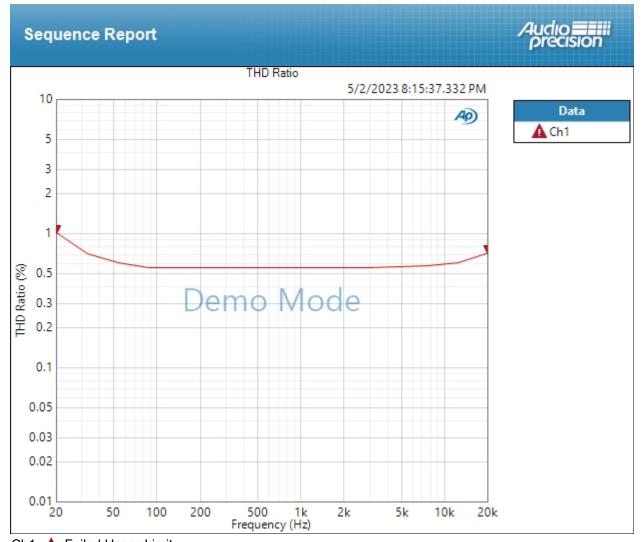
THD+N Ratio (5/2/2023 8:15:37.332 PM)



Result: V PASSED

THD Ratio (5/2/2023 8:15:37.332 PM)

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Result: 🛕 FAILED

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Mic 2k 200k termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
AG52 Generator Option: Installed
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm W(watts) (Input Power): 8.000 ohm

• DCX

DC Output 1: 0.000 V
DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00

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Port D (hex): 00

• Clocks

Output Rate: Track Output SR

Sync Out Level: 3.300 V
Sync Out Polarity: Normal
Timebase Reference: Internal
Jitter: Disabled

• Triggers

Source: Off

Input Logic Level: 3.300 V Edge: Rising

Mic 2k 200k termination: Verify Connections

Waveform: Sine

Generator Level: -42.300 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/2/2023 8:15:42.149 PM)

Ch1 292.9 mVrms

Gain (5/2/2023 8:15:42.149 PM)

Ch1 33.854 dB

THD+N Ratio (5/2/2023 8:15:42.149 PM)

Ch1 ---- %

Frequency (5/2/2023 8:15:42.149 PM)

Ch1 ---- Hz

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Mic 2k 200k termination : Stepped Frequency Sweep MIC 2K

Generator Level: -42.300 dBu
DC Offset: 0.000 V
EQ: None

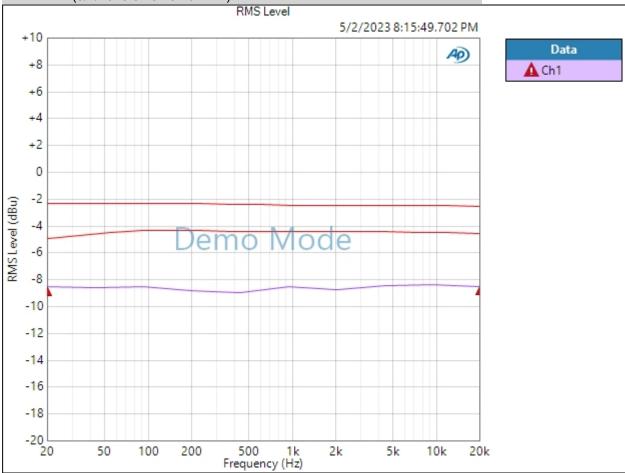
Start Frequency: 20.0000 kHz
Stop Frequency: 20.0000 Hz
Step Type: Logarithmic

Number of Points: 10

Weighting Filter: Signal Path
High-pass Filter: 20 Hz
Phase Ref Channel: Ch1

Measured 1 5/2/2023 8:15:49 PM

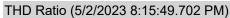
RMS Level (5/2/2023 8:15:49.702 PM)

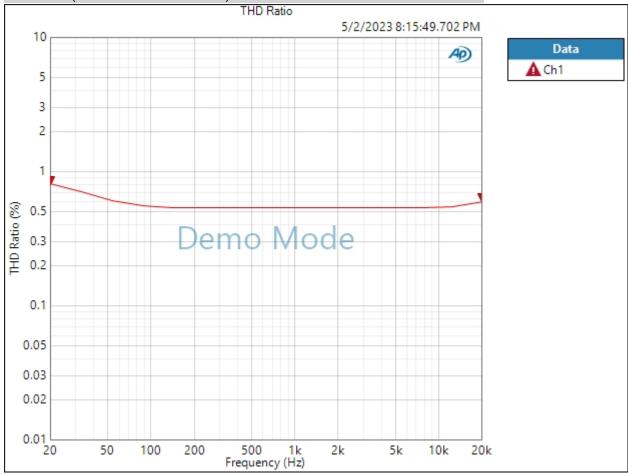


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Result: A FAILED





Ch1 A Failed Upper Limit

Result: A FAILED

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Mic 2k 15dB PAD 200k termination : Signal Path Setup
Output Connector:

Analog Balanced

Channels: 1

Source Impedance: 100 ohm
AG52 Generator Option: Installed
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm W(watts) (Input Power): 8.000 ohm

• DCX

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00

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Port D (hex): 00

• Clocks

Output Rate: Track Output SR

Sync Out Level: 3.300 V
Sync Out Polarity: Normal
Timebase Reference: Internal
Jitter: Disabled

• Triggers

Source: Off

Input Logic Level: 3.300 V Edge: Rising

Mic 2k 15dB PAD 200k termination: Verify Connections

Waveform: Sine

Generator Level: -42.300 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/2/2023 8:15:54.489 PM)

Ch1 282.2 mVrms

Gain (5/2/2023 8:15:54.489 PM)

Ch1 33.531 dB

THD+N Ratio (5/2/2023 8:15:54.489 PM)

Ch1 ---- %

Frequency (5/2/2023 8:15:54.489 PM)

Ch1 ---- Hz

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Mic 2k 15dB PAD 200k termination : Stepped Frequency Sweep 15dB PAD

Generator Level: -42.000 dBu
DC Offset: 0.000 V
EQ: None

Start Frequency: 20.0000 kHz
Stop Frequency: 20.0000 Hz
Step Type: Logarithmic

Number of Points: 10

Weighting Filter: Signal Path
High-pass Filter: 20 Hz
Phase Ref Channel: Ch1

Measured 1 5/2/2023 8:16:03 PM

RMS Level (5/2/2023 8:16:03.412 PM)

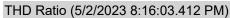


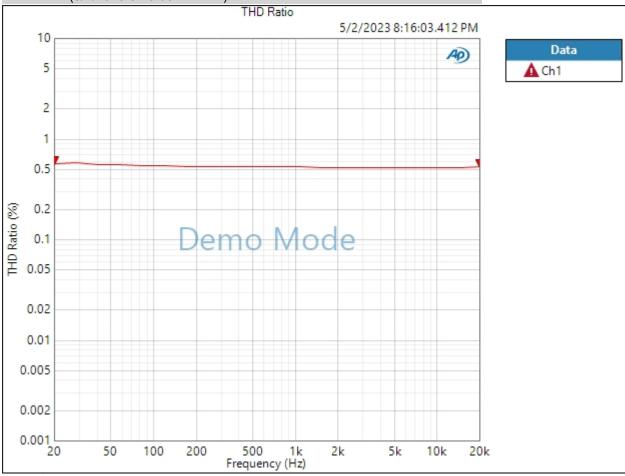
Ch1 A Failed Upper Limit

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Result: A FAILED





Ch1 A Failed Upper Limit

Result: A FAILED

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Line Gain -10 200kTermination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
AG52 Generator Option: Installed
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

W(watts) (Input Power):

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00

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8.000 ohm



Port D (hex): 00

• Clocks

Output Rate: Track Output SR

Sync Out Level: 3.300 V
Sync Out Polarity: Normal
Timebase Reference: Internal
Jitter: Disabled

• Triggers

Source: Off

Input Logic Level: 3.300 V Edge: Rising

Line Gain -10 200kTermination: Verify Connections

Waveform: Sine

Generator Level: 0.000 dBu

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 8:16:08.306 PM)

Ch1 291.4 mVrms

Gain (5/2/2023 8:16:08.306 PM)

Ch1 -8.493 dB

THD+N Ratio (5/2/2023 8:16:08.306 PM)

Ch1 ---- %

Frequency (5/2/2023 8:16:08.306 PM)

Ch1 ---- Hz

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Line Gain -10 200kTermination: Stepped Frequency Sweep -10

Generator Level: 0.000 dBu
DC Offset: 0.000 V
EQ: None

Start Frequency: 20.0000 kHz
Stop Frequency: 20.0000 Hz
Step Type: Logarithmic

Number of Points: 15

Weighting Filter: Signal Path
High-pass Filter: 20 Hz
Phase Ref Channel: Ch1

Measured 1 5/2/2023 8:16:18 PM

RMS Level (5/2/2023 8:16:18.303 PM)



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Result: A FAILED

THD+N Ratio (5/2/2023 8:16:18.303 PM)



Ch1 A Failed Upper Limit

Result: A FAILED

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Line Gain -10 600 Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
AG52 Generator Option: Installed
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 600 ohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

W(watts) (Input Power):

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00

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8.000 ohm



Port D (hex): 00

• Clocks

Output Rate: Track Output SR

Sync Out Level: 3.300 V
Sync Out Polarity: Normal
Timebase Reference: Internal
Jitter: Disabled

• Triggers

Source: Off

Input Logic Level: 3.300 V Edge: Rising

Line Gain -10 600 Termination: Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/2/2023 8:16:23.332 PM)

Ch1 285.4 mVrms

Gain (5/2/2023 8:16:23.332 PM)

Ch1 1.327 dB

THD+N Ratio (5/2/2023 8:16:23.332 PM)

Ch1 ---- %

Frequency (5/2/2023 8:16:23.332 PM)

Ch1 ---- Hz

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Line Gain -10 600 Termination: Level and Gain -10

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/2/2023 8:16:25.912 PM)

Channel Lower Limit Value Upper Limit
Ch1 -11.500 dBu -8.535 dBu -8.500 dBu

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Line Gain +5 200kTermination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
AG52 Generator Option: Installed
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

W(watts) (Input Power):

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00

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8.000 ohm



Port D (hex):

• Clocks

Output Rate: Track Output SR

Sync Out Level: 3.300 V
Sync Out Polarity: Normal
Timebase Reference: Internal
Jitter: Disabled

• Triggers

Source: Off

Input Logic Level: 3.300 V Edge: Rising

Line Gain +5 200kTermination : Verify Connections

Waveform: Sine

Generator Level: 0.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/2/2023 8:16:30.928 PM)

Ch1 290.8 mVrms

Gain (5/2/2023 8:16:30.928 PM)

Ch1 -8.510 dB

THD+N Ratio (5/2/2023 8:16:30.928 PM)

Ch1 ---- %

Frequency (5/2/2023 8:16:30.928 PM)

Ch1 ---- Hz

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Line Gain +5 200kTermination : Stepped Frequency Sweep +5

Generator Level: 0.000 dBu
DC Offset: 0.000 V
EQ: None

Start Frequency: 20.0000 kHz
Stop Frequency: 20.0000 Hz
Step Type: Logarithmic

Number of Points: 15

Weighting Filter: Signal Path High-pass Filter: 20 Hz

Phase Ref Channel: Ch1

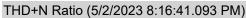
Measured 1 5/2/2023 8:16:41 PM

RMS Level (5/2/2023 8:16:41.093 PM) RMS Level 5/2/2023 8:16:41.093 PM +20 Data Ap) +18 ⚠ Ch1 +16 +14 +12 +10 +8 RMS Level (dBu) +6 Demo Mode +2 0 -2 -4 -6 -8 -10 50 20 100 200 500 2k 5k 10k 20k Frequency (Hz)

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Result: A FAILED





Ch1 A Failed Upper Limit

Result: A FAILED

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Line Gain +5 600 Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
AG52 Generator Option: Installed
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 600 ohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm W(watts) (Input Power): 8.000 ohm

• DCX

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00

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Port D (hex): 00

• Clocks

Output Rate: Track Output SR

Sync Out Level: 3.300 V
Sync Out Polarity: Normal
Timebase Reference: Internal
Jitter: Disabled

• Triggers

Source: Off

Input Logic Level: 3.300 V Edge: Rising

Line Gain +5 600 Termination: Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/2/2023 8:16:46.267 PM)

Ch1 281.9 mVrms

Gain (5/2/2023 8:16:46.267 PM)

Ch1 1.222 dB

THD+N Ratio (5/2/2023 8:16:46.267 PM)

Ch1 ---- %

Frequency (5/2/2023 8:16:46.267 PM)

Ch1 ---- Hz

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Line Gain +5 600 Termination: Level and Gain +5

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/2/2023 8:16:50.048 PM)

Channel Lower Limit Value Upper Limit
Ch1 +3.500 dBu -8.757 dBu +6.500 dBu

Result: A FAILED

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Line Gain -5 600 Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
AG52 Generator Option: Installed
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 600 ohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

W(watts) (Input Power):

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00

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8.000 ohm



Port D (hex): 00

• Clocks

Output Rate: Track Output SR

Sync Out Level: 3.300 V
Sync Out Polarity: Normal
Timebase Reference: Internal
Jitter: Disabled

• Triggers

Source: Off

Input Logic Level: 3.300 V Edge: Rising

Line Gain -5 600 Termination: Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/2/2023 8:16:55.321 PM)

Ch1 291.1 mVrms

Gain (5/2/2023 8:16:55.321 PM)

Ch1 1.499 dB

THD+N Ratio (5/2/2023 8:16:55.321 PM)

Ch1 ---- %

Frequency (5/2/2023 8:16:55.321 PM)

Ch1 ---- Hz

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Line Gain -5 600 Termination: Level and Gain -5

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/2/2023 8:16:59.162 PM)

Channel Lower Limit Value Upper Limit
Ch1 -6.500 dBu -8.620 dBu -3.500 dBu

Result: A FAILED

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Line Gain 0 600 Termination : Signal Path Setup

Analog Balanced Output Connector:

1 Channels:

100 ohm Source Impedance: AG52 Generator Option: Installed None Output EQ:

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 600 ohm

AC (<10 Hz) - 90k (192 kHz SR) Input Bandwidth:

Device Delay: 0.000 sInput EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm W(watts) (Input Power):

• DCX

DC Output 1: 0.000 V Off DC Output 1: DC Output 2: 0.000 V DC Output 2: Off Port A (hex): 00 00 Port B (hex): 00 Port C (hex):

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8.000 ohm



Port D (hex): 00

• Clocks

Output Rate: Track Output SR

Sync Out Level: 3.300 V
Sync Out Polarity: Normal
Timebase Reference: Internal
Jitter: Disabled

• Triggers

Source: Off

Input Logic Level: 3.300 V Edge: Rising

Line Gain 0 600 Termination: Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/2/2023 8:17:05.485 PM)

Ch1 295.3 mVrms

Gain (5/2/2023 8:17:05.485 PM)

Ch1 1.625 dB

THD+N Ratio (5/2/2023 8:17:05.485 PM)

Ch1 ---- %

Frequency (5/2/2023 8:17:05.485 PM)

Ch1 ---- Hz

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Line Gain 0 600 Termination: Level and Gain 0

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/2/2023 8:17:09.213 PM)

Channel Lower Limit Value Upper Limit
Ch1 -1.500 dBu -8.711 dBu +1.500 dBu

Result: A FAILED

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Line Gain +10 600 Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
AG52 Generator Option: Installed
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 600 ohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

W(watts) (Input Power):

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00

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8.000 ohm



Port D (hex): 00

• Clocks

Output Rate: Track Output SR

Sync Out Level: 3.300 V
Sync Out Polarity: Normal
Timebase Reference: Internal
Jitter: Disabled

• Triggers

Source: Off

Input Logic Level: 3.300 V
Edge: Rising

Line Gain +10 600 Termination: Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/2/2023 8:17:15.474 PM)

Ch1 281.1 mVrms

Gain (5/2/2023 8:17:15.474 PM)

Ch1 1.195 dB

THD+N Ratio (5/2/2023 8:17:15.474 PM)

Ch1 ---- %

Frequency (5/2/2023 8:17:15.474 PM)

Ch1 ---- Hz

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Line Gain +10 600 Termination: Level and Gain +10

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/2/2023 8:17:19.216 PM)

Channel Lower Limit Value Upper Limit
Ch1 +8.500 dBu -8.680 dBu +11.500 dBu

Result: A FAILED

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Line Gain +10 200k Termination Level Hi : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
AG52 Generator Option: Installed
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

W(watts) (Input Power):

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00

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8.000 ohm



Port D (hex): 00

• Clocks

Output Rate: Track Output SR

Sync Out Level: 3.300 V
Sync Out Polarity: Normal
Timebase Reference: Internal
Jitter: Disabled

• Triggers

Source: Off

Input Logic Level: 3.300 V Edge: Rising

Line Gain +10 200k Termination Level Hi: Verify Connections

Waveform: Sine

Generator Level: -20.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/2/2023 8:17:24.490 PM)

Ch1 307.0 mVrms

Gain (5/2/2023 8:17:24.490 PM)

Ch1 11.962 dB

THD+N Ratio (5/2/2023 8:17:24.490 PM)

Ch1 ---- %

Frequency (5/2/2023 8:17:24.490 PM)

Ch1 ---- Hz

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Line Gain +10 200k Termination Level Hi: Noise Recorder (RMS) CW

Waveform: None
Low-pass Filter: 20 kHz
Weighting Filter: Signal Path

High-pass Filter: 20 Hz

Sweep Time: 0.00:00:03.000

Reading Rate: 10/sec

Input Bandwidth: Use Signal Path

Record Acquisition: False

Measured 1 5/2/2023 8:17:36 PM

RMS Level (5/2/2023 8:17:36.951 PM)



Result: A FAILED

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Line Gain +10 200k Termination Level Low: Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
AG52 Generator Option: Installed
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

100.0 mVrms dBr G: dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm W(watts) (Input Power): 8.000 ohm

• DCX

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00

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Port D (hex): 00

• Clocks

Output Rate: Track Output SR

Sync Out Level: 3.300 V
Sync Out Polarity: Normal
Timebase Reference: Internal
Jitter: Disabled

• Triggers

Source: Off

Input Logic Level: 3.300 V
Edge: Rising

Line Gain +10 200k Termination Level Low: Verify Connections

Waveform: Sine

Generator Level: -20.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/2/2023 8:17:42.211 PM)

Ch1 293.0 mVrms

Gain (5/2/2023 8:17:42.211 PM)

Ch1 11.555 dB

THD+N Ratio (5/2/2023 8:17:42.211 PM)

Ch1 ---- %

Frequency (5/2/2023 8:17:42.211 PM)

Ch1 ---- Hz

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Line Gain +10 200k Termination Level Low: Noise Recorder (RMS) CCW

Waveform: None
Low-pass Filter: 20 kHz
Weighting Filter: Signal Path

High-pass Filter: 20 Hz

Sweep Time: 0.00:00:03.000

Reading Rate: 10/sec

Input Bandwidth: Use Signal Path

Record Acquisition: False

Measured 1 5/2/2023 8:17:56 PM

RMS Level (5/2/2023 8:17:56.216 PM)



Result: A FAILED

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Hi Z Gain -10 2.2M 200k Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 2

Source Impedance: 100 ohm
AG52 Generator Option: Installed
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm W(watts) (Input Power): 8.000 ohm

• DCX

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00

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Port D (hex): 00

• Clocks

Output Rate: Track Output SR

Sync Out Level: 3.300 V
Sync Out Polarity: Normal
Timebase Reference: Internal
Jitter: Disabled

• Triggers

Source: Off

Input Logic Level: 3.300 V Edge: Rising

Hi Z Gain -10 2.2M 200k Termination: Verify Connections

Waveform: Sine

Generator Level: -22.300 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/2/2023 8:18:01.310 PM)

Ch1 285.6 mVrms

Gain (5/2/2023 8:18:01.310 PM)

Ch1 13.634 dB

THD+N Ratio (5/2/2023 8:18:01.310 PM)

Ch1 ---- %

Frequency (5/2/2023 8:18:01.310 PM)

Ch1 ---- Hz

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Hi Z Gain -10 2.2M 200k Termination: Level and Gain 2.2M

Waveform: Sine

Generator Level: -22.300 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 8:18:06.690 PM)

Channel Lower Limit Value Upper Limit
Ch1 -2.000 dBu -8.456 dBu +2.000 dBu

Result: A FAILED

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Hi Z Gain -10 47k 200k Termination : Signal Path Setup
Output Connector:
Analog Unbalanced

Channels: 2

Source Impedance: 50 ohm
AG52 Generator Option: Installed
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

W(watts) (Input Power):

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00

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8.000 ohm



Port D (hex):

• Clocks

Output Rate: Track Output SR

Sync Out Level: 3.300 V
Sync Out Polarity: Normal
Timebase Reference: Internal
Jitter: Disabled

• Triggers

Source: Off

Input Logic Level: 3.300 V Edge: Rising

Hi Z Gain -10 47k 200k Termination: Verify Connections

Waveform: Sine

Generator Level: -22.300 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/2/2023 8:18:11.988 PM)

Ch1 284.7 mVrms

Gain (5/2/2023 8:18:11.988 PM)

Ch1 13.607 dB

THD+N Ratio (5/2/2023 8:18:11.988 PM)

Ch1 ---- %

Frequency (5/2/2023 8:18:11.988 PM)

Ch1 ---- Hz

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Hi Z Gain -10 47k 200k Termination : Level and Gain 47K

Waveform: Sine

Generator Level: -22.300 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/2/2023 8:18:15.671 PM)

Channel Lower Limit Value Upper Limit
Ch1 -8.000 dBu -8.588 dBu -4.000 dBu

Result: A FAILED

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Dummy Signal Path For Report : Signal Path Setup

Output Connector: Analog Unbalanced

Channels: 2

Source Impedance: 50 ohm
AG52 Generator Option: Installed
Output EQ: None

Input Connector: Analog Unbalanced

Channels: 2

Termination: 100 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL 600.0 ohm dBm (Input Power): W(watts) (Input Power): 8.000 ohm

• DCX

DC Output 1: 0.000 V DC Output 1: Off 0.000 V DC Output 2: DC Output 2: Off Port A (hex): 00 Port B (hex): 00 00 Port C (hex): 00 Port D (hex):

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• Clocks

Output Rate: Track Output SR

Sync Out Level: 3.300 V
Sync Out Polarity: Normal
Timebase Reference: Internal
Jitter: Disabled

• Triggers

Source: Off

Input Logic Level: 3.300 V Edge: Rising

Dummy Signal Path For Report : Verify Connections

Waveform: Sine

Generator Level: 100.0 mVrms

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/2/2023 8:18:19.262 PM)

Ch1 297.4 mVrms Ch2 275.5 mVrms

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