

Summary

Mic500 200k Termination	
Signal Path Setup	🍼 PASSED
Stepped Frequency Sweep MIC 500	🛕 FAILED
Mic 2k 200k termination	
Signal Path Setup	PASSED
Stepped Frequency Sweep MIC 2K	▲ FAILED
Mic 2k 15dB PAD 200k termination	
Signal Path Setup	PASSED
Stepped Frequency Sweep 15dB PAD	🛕 FAILED
Line Gain -10 200kTermination	
Signal Path Setup	PASSED
Stepped Frequency Sweep -10	▲ FAILED
Line Gain -10 600 Termination	
Signal Path Setup	PASSED
Level and Gain -10	PASSED
Line Gain +5 200kTermination	
Signal Path Setup	PASSED
Stepped Frequency Sweep +5	▲ FAILED
Line Gain +5 600 Termination	
Signal Path Setup	PASSED
Level and Gain +5	▲ FAILED
Line Gain -5 600 Termination	
Signal Path Setup	PASSED
Level and Gain -5	▲ FAILED
Line Gain 0 600 Termination	
Signal Path Setup	PASSED
Level and Gain 0	▲ FAILED
Line Gain +10 600 Termination	
Signal Path Setup	PASSED
Level and Gain +10	▲ FAILED
Line Gain +10 200k Termination Level Hi	
Signal Path Setup	PASSED
Noise Recorder (RMS) CW	▲ FAILED
Line Gain +10 200k Termination Level Low	
Signal Path Setup	PASSED
Noise Recorder (RMS) CCW	▲ FAILED
Hi Z Gain -10 2.2M 200k Termination	

Signal Path Setup Level and Gain 2.2M	♥ PASSED ♠ FAILED
Hi Z Gain -10 47k 200k Termination	_
Signal Path Setup Level and Gain 47K	▼ PASSED ▲ FAILED
Dummy Signal Path For Report	
Signal Path Setup	♥ PASSED
Sequence Result:	
Sequence Result:	

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Mic500 200k Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL

• DCX

dBm (Input Power):

W(watts) (Input Power):

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00

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600.0 ohm

8.000 ohm



Port D (hex): 00

Mic500 200k Termination: Verify Connections

Waveform: Sine

Generator Level: -42.300 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/8/2023 9:48:31.751 AM)

Ch1 285.1 mVrms

Gain (5/8/2023 9:48:31.751 AM)

Ch1 33.618 dB

THD+N Ratio (5/8/2023 9:48:31.751 AM)

Ch1 ---- %

Frequency (5/8/2023 9:48:31.751 AM)

Ch1 ---- Hz

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Mic500 200k Termination: Stepped Frequency Sweep MIC 500

Generator Level: -42.300 dBu
DC Offset: 0.000 V
EQ: None

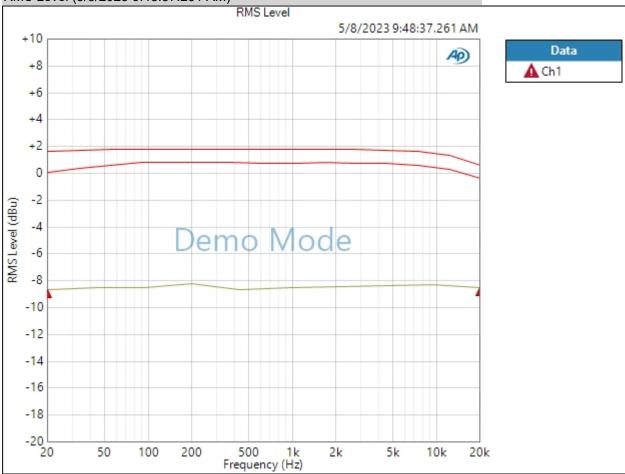
Start Frequency: 20.0000 kHz
Stop Frequency: 20.0000 Hz
Step Type: Logarithmic

Number of Points: 10

Weighting Filter: Signal Path
High-pass Filter: 20 Hz
Phase Ref Channel: Ch1

Measured 1 5/8/2023 9:48:37 AM

RMS Level (5/8/2023 9:48:37.261 AM)



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Result: A FAILED

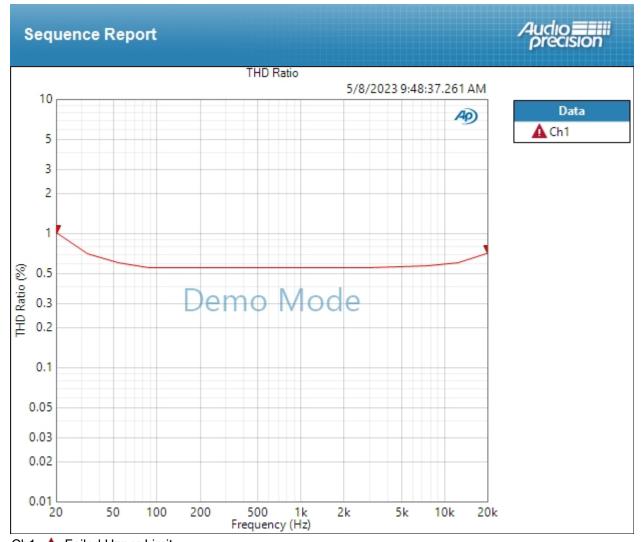
THD+N Ratio (5/8/2023 9:48:37.261 AM)



Result: V PASSED

THD Ratio (5/8/2023 9:48:37.261 AM)

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Result: 🛕 FAILED

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Mic 2k 200k termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL

• DCX

dBm (Input Power):

W(watts) (Input Power):

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00

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600.0 ohm

8.000 ohm



Port D (hex): 00

Mic 2k 200k termination: Verify Connections

Waveform: Sine

Generator Level: -42.300 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/8/2023 9:48:42.358 AM)

Ch1 286.5 mVrms

Gain (5/8/2023 9:48:42.358 AM)

Ch1 33.661 dB

THD+N Ratio (5/8/2023 9:48:42.358 AM)

Ch1 ---- %

Frequency (5/8/2023 9:48:42.358 AM)

Ch1 ---- Hz

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Mic 2k 200k termination : Stepped Frequency Sweep MIC 2K

Generator Level: -42.300 dBu
DC Offset: 0.000 V
EQ: None

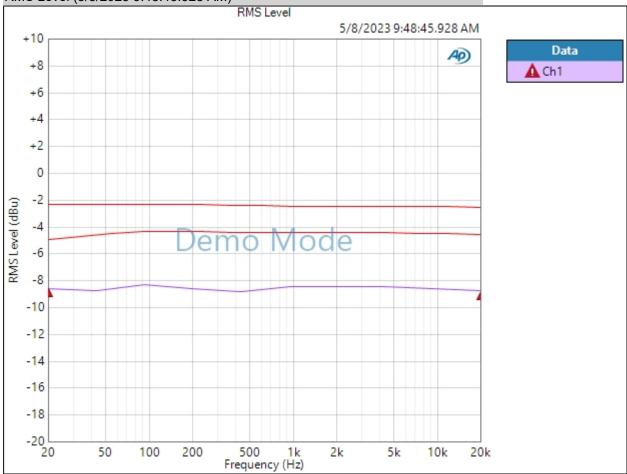
Start Frequency: 20.0000 kHz
Stop Frequency: 20.0000 Hz
Step Type: Logarithmic

Number of Points: 10

Weighting Filter: Signal Path
High-pass Filter: 20 Hz
Phase Ref Channel: Ch1

Measured 1 5/8/2023 9:48:45 AM

RMS Level (5/8/2023 9:48:45.928 AM)



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Result: 🛕 FAILED

THD Ratio (5/8/2023 9:48:45.928 AM)



Ch1 A Failed Upper Limit

Result: A FAILED

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Mic 2k 15dB PAD 200k termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL

dBm (Input Power): 600.0 ohm W(watts) (Input Power): 8.000 ohm

• DCX

Port C (hex):

dBSPL2 Calibrator Level:

 DC Output 1:
 0.000 V

 DC Output 1:
 Off

 DC Output 2:
 0.000 V

 DC Output 2:
 Off

 Port A (hex):
 00

 Port B (hex):
 00

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94.000 dBSPL

00



Port D (hex): 00

Mic 2k 15dB PAD 200k termination: Verify Connections

Waveform: Sine

Generator Level: -42.300 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/8/2023 9:48:51.722 AM)

Ch1 296.3 mVrms

Gain (5/8/2023 9:48:51.722 AM)

Ch1 33.952 dB

THD+N Ratio (5/8/2023 9:48:51.722 AM)

Ch1 ---- %

Frequency (5/8/2023 9:48:51.722 AM)

Ch1 ---- Hz

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Mic 2k 15dB PAD 200k termination : Stepped Frequency Sweep 15dB PAD

Generator Level: -42.000 dBu
DC Offset: 0.000 V
EQ: None

Start Frequency: 20.0000 kHz
Stop Frequency: 20.0000 Hz
Step Type: Logarithmic

Number of Points: 10

Weighting Filter: Signal Path
High-pass Filter: 20 Hz
Phase Ref Channel: Ch1

Measured 1 5/8/2023 9:48:55 AM

RMS Level (5/8/2023 9:48:55.844 AM)



Ch1 A Failed Upper Limit

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Result: A FAILED

THD Ratio (5/8/2023 9:48:55.844 AM)



Ch1 A Failed Upper Limit

Result: A FAILED

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Line Gain -10 200kTermination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1
Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

W(watts) (Input Power):

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00

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8.000 ohm



Port D (hex): 00

Line Gain -10 200kTermination: Verify Connections

Waveform: Sine

Generator Level: 0.000 dBu
DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/8/2023 9:49:00.732 AM)

Ch1 289.7 mVrms

Gain (5/8/2023 9:49:00.732 AM)

Ch1 -8.541 dB

THD+N Ratio (5/8/2023 9:49:00.732 AM)

Ch1 ---- %

Frequency (5/8/2023 9:49:00.732 AM)

Ch1 ---- Hz

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Line Gain -10 200kTermination: Stepped Frequency Sweep -10

Generator Level: 0.000 dBu
DC Offset: 0.000 V
EQ: None

Start Frequency: 20.0000 kHz
Stop Frequency: 20.0000 Hz
Step Type: Logarithmic

Number of Points: 15

Weighting Filter: Signal Path
High-pass Filter: 20 Hz
Phase Ref Channel: Ch1

Measured 1 5/8/2023 9:49:05 AM

RMS Level (5/8/2023 9:49:05.112 AM)



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Result: A FAILED

THD+N Ratio (5/8/2023 9:49:05.112 AM)



Ch1 A Failed Upper Limit

Result: A FAILED

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Line Gain -10 600 Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 600 ohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL

• DCX

dBm (Input Power):

W(watts) (Input Power):

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00

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600.0 ohm

8.000 ohm



Port D (hex): 00

Line Gain -10 600 Termination: Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/8/2023 9:49:10.213 AM)

Ch1 284.3 mVrms

Gain (5/8/2023 9:49:10.213 AM)

Ch1 1.294 dB

THD+N Ratio (5/8/2023 9:49:10.213 AM)

Ch1 ---- %

Frequency (5/8/2023 9:49:10.213 AM)

Ch1 ---- Hz

Line Gain -10 600 Termination: Level and Gain -10

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/8/2023 9:49:12.761 AM)

Channel Lower Limit Value Upper Limit
Ch1 -11.500 dBu -8.630 dBu -8.500 dBu

✓

Result: V PASSED

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Line Gain +5 200kTermination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

W(watts) (Input Power):

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00

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8.000 ohm



Port D (hex): 00

Line Gain +5 200kTermination : Verify Connections

Waveform: Sine

Generator Level: 0.000 dBu
DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/8/2023 9:49:17.754 AM)

Ch1 284.7 mVrms

Gain (5/8/2023 9:49:17.754 AM)

Ch1 -8.694 dB

THD+N Ratio (5/8/2023 9:49:17.754 AM)

Ch1 ---- %

Frequency (5/8/2023 9:49:17.754 AM)

Ch1 ---- Hz

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Line Gain +5 200kTermination : Stepped Frequency Sweep +5

Generator Level: 0.000 dBu
DC Offset: 0.000 V
EQ: None

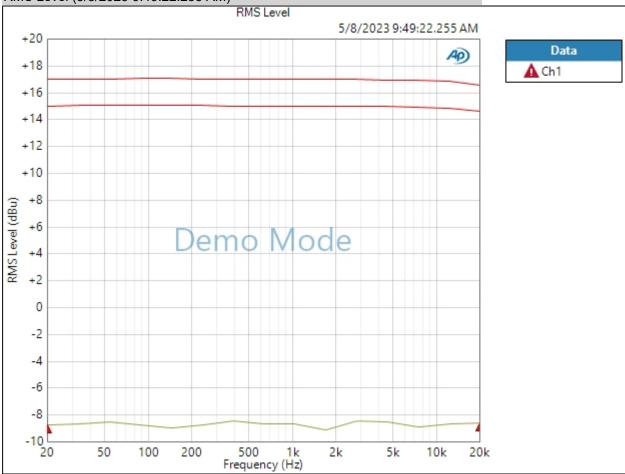
Start Frequency: 20.0000 kHz
Stop Frequency: 20.0000 Hz
Step Type: Logarithmic

Number of Points: 15

Weighting Filter: Signal Path High-pass Filter: 20 Hz

Phase Ref Channel: Ch1
Measured 1 5/8/2023 9:49:22 AM

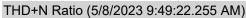
RMS Level (5/8/2023 9:49:22.255 AM)



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Result: A FAILED





Ch1 A Failed Upper Limit

Result: A FAILED

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Line Gain +5 600 Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 600 ohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

W(watts) (Input Power):

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00

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8.000 ohm



Port D (hex): 00

Line Gain +5 600 Termination : Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/8/2023 9:49:27.721 AM)

Ch1 282.9 mVrms

Gain (5/8/2023 9:49:27.721 AM)

Ch1 1.250 dB

THD+N Ratio (5/8/2023 9:49:27.721 AM)

Ch1 ---- %

Frequency (5/8/2023 9:49:27.721 AM)

Ch1 ---- Hz

Line Gain +5 600 Termination: Level and Gain +5

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/8/2023 9:49:30.467 AM)

Channel Lower Limit Value Upper Limit
Ch1 +3.500 dBu -8.679 dBu +6.500 dBu

Result: A FAILED

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Line Gain -5 600 Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 600 ohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL

dBm (Input Power): 600.0 ohm W(watts) (Input Power): 8.000 ohm

• DCX

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00

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Port D (hex): 00

Line Gain -5 600 Termination: Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/8/2023 9:49:35.651 AM)

Ch1 275.7 mVrms

Gain (5/8/2023 9:49:35.651 AM)

Ch1 1.026 dB

THD+N Ratio (5/8/2023 9:49:35.651 AM)

Ch1 ---- %

Frequency (5/8/2023 9:49:35.651 AM)

Ch1 ---- Hz

Line Gain -5 600 Termination: Level and Gain -5

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/8/2023 9:49:38.230 AM)

Channel Lower Limit Value Upper Limit
Ch1 -6.500 dBu -8.611 dBu -3.500 dBu

Result: A FAILED

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Line Gain 0 600 Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 600 ohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

W(watts) (Input Power):

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00

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8.000 ohm



Port D (hex): 00

Line Gain 0 600 Termination: Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/8/2023 9:49:43.404 AM)

Ch1 290.5 mVrms

Gain (5/8/2023 9:49:43.404 AM)

Ch1 1.483 dB

THD+N Ratio (5/8/2023 9:49:43.404 AM)

Ch1 ---- %

Frequency (5/8/2023 9:49:43.404 AM)

Ch1 ---- Hz

Line Gain 0 600 Termination: Level and Gain 0

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/8/2023 9:49:46.005 AM)

Channel Lower Limit Value Upper Limit
Ch1 -1.500 dBu -8.427 dBu +1.500 dBu

Result: A FAILED

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Line Gain +10 600 Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 600 ohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL

dBm (Input Power): 600.0 ohm W(watts) (Input Power): 8.000 ohm

• DCX

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00

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Port D (hex): 00

Line Gain +10 600 Termination: Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/8/2023 9:49:51.271 AM)

Ch1 294.0 mVrms

Gain (5/8/2023 9:49:51.271 AM)

Ch1 1.585 dB

THD+N Ratio (5/8/2023 9:49:51.271 AM)

Ch1 ---- %

Frequency (5/8/2023 9:49:51.271 AM)

Ch1 ---- Hz

Line Gain +10 600 Termination: Level and Gain +10

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/8/2023 9:49:54.092 AM)

Channel Lower Limit Value Upper Limit
Ch1 +8.500 dBu -8.655 dBu +11.500 dBu

Result: A FAILED

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Line Gain +10 200k Termination Level Hi : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms

dBm (Input Power): 600.0 ohm W(watts) (Input Power): 8.000 ohm

• DCX

dBSPL1 Calibrator Level:

dBSPL2 Calibrator Level:

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00

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94.000 dBSPL

94.000 dBSPL



Port D (hex): 00

Line Gain +10 200k Termination Level Hi: Verify Connections

Waveform: Sine

Generator Level: -20.000 dBu
DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/8/2023 9:49:59.321 AM)

Ch1 285.5 mVrms

Gain (5/8/2023 9:49:59.321 AM)

Ch1 11.331 dB

THD+N Ratio (5/8/2023 9:49:59.321 AM)

Ch1 ---- %

Frequency (5/8/2023 9:49:59.321 AM)

Ch1 ---- Hz

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Line Gain +10 200k Termination Level Hi: Noise Recorder (RMS) CW

Waveform: None
Low-pass Filter: 20 kHz
Weighting Filter: Signal Path

High-pass Filter: 20 Hz

Sweep Time: 0.00:00:03.000

Reading Rate: 10/sec

Input Bandwidth: Use Signal Path

Record Acquisition: False

Measured 1 5/8/2023 9:50:04 AM

RMS Level (5/8/2023 9:50:04.697 AM)



Result: A FAILED

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Line Gain +10 200k Termination Level Low: Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL

dBm (Input Power): 600.0 ohm W(watts) (Input Power): 8.000 ohm

• DCX

dBSPL2 Calibrator Level:

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00

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94.000 dBSPL



Port D (hex): 00

Line Gain +10 200k Termination Level Low: Verify Connections

Waveform: Sine

Generator Level: -20.000 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/8/2023 9:50:09.958 AM)

Ch1 298.1 mVrms

Gain (5/8/2023 9:50:09.958 AM)

Ch1 11.705 dB

THD+N Ratio (5/8/2023 9:50:09.958 AM)

Ch1 ---- %

Frequency (5/8/2023 9:50:09.958 AM)

Ch1 ---- Hz

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Line Gain +10 200k Termination Level Low: Noise Recorder (RMS) CCW

Waveform: None
Low-pass Filter: 20 kHz
Weighting Filter: Signal Path

High-pass Filter: 20 Hz

Sweep Time: 0.00:00:03.000

Reading Rate: 10/sec

Input Bandwidth: Use Signal Path

Record Acquisition: False

Measured 1 5/8/2023 9:50:15 AM

RMS Level (5/8/2023 9:50:15.930 AM)



Result: A FAILED

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Hi Z Gain -10 2.2M 200k Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 2

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL

W(watts) (Input Power):

• DCX

dBm (Input Power):

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00

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600.0 ohm

8.000 ohm



Port D (hex): 00

Hi Z Gain -10 2.2M 200k Termination: Verify Connections

Waveform: Sine

Generator Level: -22.300 dBu
DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/8/2023 9:50:21.081 AM)

Ch1 276.8 mVrms

Gain (5/8/2023 9:50:21.081 AM)

Ch1 13.360 dB

THD+N Ratio (5/8/2023 9:50:21.081 AM)

Ch1 ---- %

Frequency (5/8/2023 9:50:21.081 AM)

Ch1 ---- Hz

Hi Z Gain -10 2.2M 200k Termination: Level and Gain 2.2M

Waveform: Sine

Generator Level: -22.300 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/8/2023 9:50:24.335 AM)

Channel Lower Limit Value Upper Limit
Ch1 -2.000 dBu -8.423 dBu +2.000 dBu

Result: A FAILED

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Hi Z Gain -10 47k 200k Termination : Signal Path Setup

Output Connector: Analog Unbalanced

Channels: 2

Source Impedance: 50 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL

dBm (Input Power): 600.0 ohm W(watts) (Input Power): 8.000 ohm

• DCX

Port C (hex):

dBSPL2 Calibrator Level:

 DC Output 1:
 0.000 V

 DC Output 1:
 Off

 DC Output 2:
 0.000 V

 DC Output 2:
 Off

 Port A (hex):
 00

 Port B (hex):
 00

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94.000 dBSPL

00



Port D (hex): 00

Hi Z Gain -10 47k 200k Termination : Verify Connections

Waveform: Sine

Generator Level: -22.300 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/8/2023 9:50:29.647 AM)

Ch1 297.2 mVrms

Gain (5/8/2023 9:50:29.647 AM)

Ch1 13.981 dB

THD+N Ratio (5/8/2023 9:50:29.647 AM)

Ch1 ---- %

Frequency (5/8/2023 9:50:29.647 AM)

Ch1 ---- Hz

Hi Z Gain -10 47k 200k Termination: Level and Gain 47K

Waveform: Sine

Generator Level: -22.300 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/8/2023 9:50:32.253 AM)

Channel Lower Limit Value Upper Limit
Ch1 -8.000 dBu -8.601 dBu -4.000 dBu

Result: A FAILED

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Dummy Signal Path For Report : Signal Path Setup

Output Connector: Analog Unbalanced

Channels: 2

Source Impedance: 50 ohm
Output EQ: None

Input Connector: Analog Unbalanced

Channels: 2

Termination: 100 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm W(watts) (Input Power): 8.000 ohm

• DCX

0.000 V DC Output 1: DC Output 1: Off DC Output 2: 0.000 V Off DC Output 2: Port A (hex): 00 Port B (hex): 00 Port C (hex): 00 Port D (hex): 00

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Dummy Signal Path For Report : Verify Connections

Waveform: Sine

Generator Level: 100.0 mVrms

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/8/2023 9:50:35.862 AM)

Ch1 280.8 mVrms Ch2 292.4 mVrms

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