

# Summary

Mic500 200k Termination	
Signal Path Setup	🍼 PASSED
Stepped Frequency Sweep MIC 500	🛕 FAILED
Mic 2k 200k termination	
Signal Path Setup	PASSED
Stepped Frequency Sweep MIC 2K	▲ FAILED
Mic 2k 15dB PAD 200k termination	
Signal Path Setup	PASSED
Stepped Frequency Sweep 15dB PAD	🛕 FAILED
Line Gain -10 200kTermination	
Signal Path Setup	PASSED
Stepped Frequency Sweep -10	▲ FAILED
Line Gain -10 600 Termination	
Signal Path Setup	PASSED
Level and Gain -10	PASSED
Line Gain +5 200kTermination	
Signal Path Setup	PASSED
Stepped Frequency Sweep +5	▲ FAILED
Line Gain +5 600 Termination	
Signal Path Setup	PASSED
Level and Gain +5	▲ FAILED
Line Gain -5 600 Termination	
Signal Path Setup	PASSED
Level and Gain -5	▲ FAILED
Line Gain 0 600 Termination	
Signal Path Setup	PASSED
Level and Gain 0	▲ FAILED
Line Gain +10 600 Termination	
Signal Path Setup	PASSED
Level and Gain +10	▲ FAILED
Line Gain +10 200k Termination Level Hi	
Signal Path Setup	PASSED
Noise Recorder (RMS) CW	▲ FAILED
Line Gain +10 200k Termination Level Low	
Signal Path Setup	PASSED
Noise Recorder (RMS) CCW	▲ FAILED
Hi Z Gain -10 2.2M 200k Termination	

Signal Path Setup Level and Gain 2.2M	♥ PASSED ♠ FAILED
Hi Z Gain -10 47k 200k Termination	_
Signal Path Setup Level and Gain 47K	▼ PASSED  ▲ FAILED
Dummy Signal Path For Report	
Signal Path Setup	♥ PASSED
Sequence Result:	
Sequence Result:	

5/4/2023 1:46 PM Page 2 of 47



Mic500 200k Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL

W(watts) (Input Power):
• DCX

dBm (Input Power):

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00

5/4/2023 1:46 PM Page 3 of 47

600.0 ohm



Port D (hex): 00

Mic500 200k Termination: Verify Connections

Waveform: Sine

Generator Level: -42.300 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/4/2023 1:44:02.163 PM)

Ch1 279.0 mVrms

Gain (5/4/2023 1:44:02.163 PM)

Ch1 33.432 dB

THD+N Ratio (5/4/2023 1:44:02.163 PM)

Ch1 ---- %

Frequency (5/4/2023 1:44:02.163 PM)

Ch1 ---- Hz

5/4/2023 1:46 PM Page 4 of 47



Mic500 200k Termination: Stepped Frequency Sweep MIC 500

Generator Level: -42.300 dBu
DC Offset: 0.000 V
EQ: None

Start Frequency: 20.0000 kHz
Stop Frequency: 20.0000 Hz
Step Type: Logarithmic

Number of Points: 10

Weighting Filter: Signal Path

High-pass Filter: 20 Hz Phase Ref Channel: Ch1

Measured 1 5/4/2023 1:44:09 PM

#### RMS Level (5/4/2023 1:44:09.346 PM)



5/4/2023 1:46 PM Page 5 of 47



Result: A FAILED

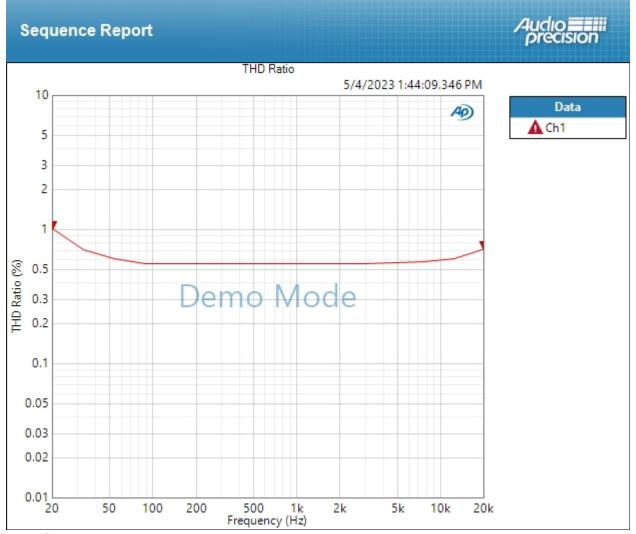
THD+N Ratio (5/4/2023 1:44:09.346 PM)



Result: V PASSED

THD Ratio (5/4/2023 1:44:09.346 PM)

5/4/2023 1:46 PM Page 6 of 47



Result: A FAILED

5/4/2023 1:46 PM Page 7 of 47



Mic 2k 200k termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

W(watts) (Input Power):

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00

5/4/2023 1:46 PM Page 8 of 47



Port D (hex): 00

Mic 2k 200k termination: Verify Connections

Waveform: Sine

Generator Level: -42.300 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/4/2023 1:44:15.396 PM)

Ch1 290.3 mVrms

Gain (5/4/2023 1:44:15.396 PM)

Ch1 33.776 dB

THD+N Ratio (5/4/2023 1:44:15.396 PM)

Ch1 ---- %

Frequency (5/4/2023 1:44:15.396 PM)

Ch1 ---- Hz

5/4/2023 1:46 PM Page 9 of 47



Mic 2k 200k termination : Stepped Frequency Sweep MIC 2K

Generator Level: -42.300 dBu
DC Offset: 0.000 V
EQ: None

Start Frequency: 20.0000 kHz
Stop Frequency: 20.0000 Hz
Step Type: Logarithmic

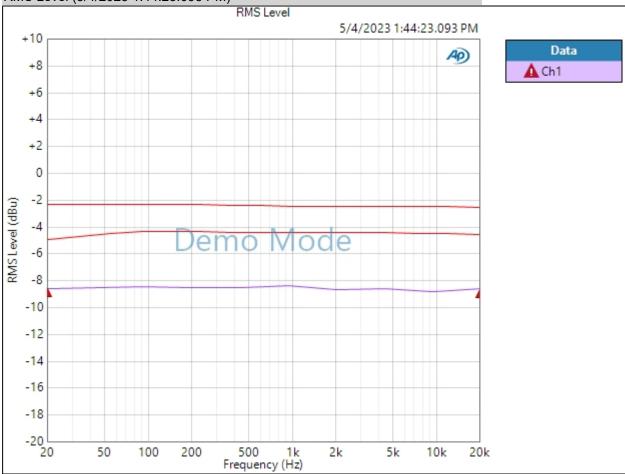
Number of Points: 10

Weighting Filter: Signal Path

High-pass Filter: 20 Hz Phase Ref Channel: Ch1

Measured 1 5/4/2023 1:44:23 PM

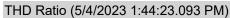
#### RMS Level (5/4/2023 1:44:23.093 PM)

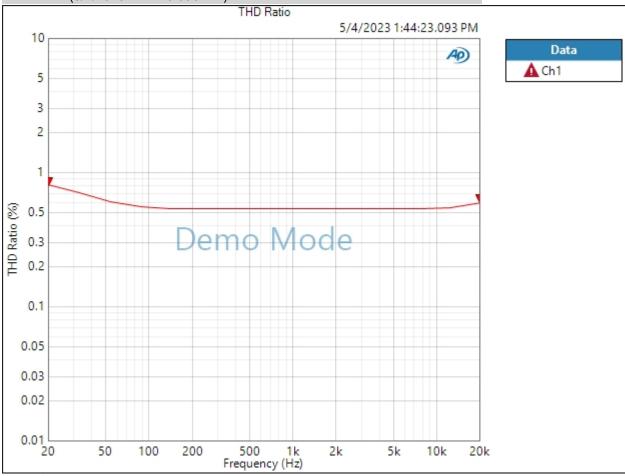


5/4/2023 1:46 PM Page 10 of 47



Result: 🛕 FAILED





Ch1 A Failed Upper Limit

Result: A FAILED

5/4/2023 1:46 PM Page 11 of 47



Mic 2k 15dB PAD 200k termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL

dBm (Input Power): 600.0 ohm W(watts) (Input Power): 8.000 ohm

• DCX

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00

5/4/2023 1:46 PM Page 12 of 47



Port D (hex): 00

Mic 2k 15dB PAD 200k termination: Verify Connections

Waveform: Sine

Generator Level: -42.300 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/4/2023 1:44:28.075 PM)

Ch1 294.9 mVrms

Gain (5/4/2023 1:44:28.075 PM)

Ch1 33.913 dB

THD+N Ratio (5/4/2023 1:44:28.075 PM)

Ch1 ---- %

Frequency (5/4/2023 1:44:28.075 PM)

Ch1 ---- Hz

5/4/2023 1:46 PM Page 13 of 47



#### Mic 2k 15dB PAD 200k termination : Stepped Frequency Sweep 15dB PAD

Generator Level: -42.000 dBu
DC Offset: 0.000 V
EQ: None

Start Frequency: 20.0000 kHz
Stop Frequency: 20.0000 Hz
Step Type: Logarithmic

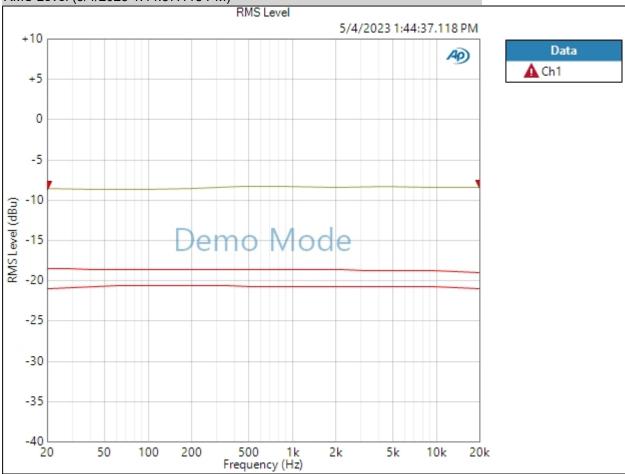
Number of Points: 10

Weighting Filter: Signal Path High-pass Filter: 20 Hz

Phase Ref Channel: Ch1

Measured 1 5/4/2023 1:44:37 PM

#### RMS Level (5/4/2023 1:44:37.118 PM)



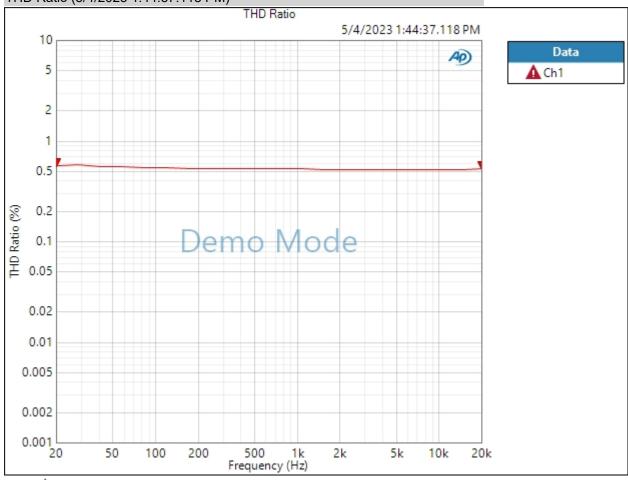
Ch1 A Failed Upper Limit

5/4/2023 1:46 PM Page 14 of 47



Result: A FAILED

THD Ratio (5/4/2023 1:44:37.118 PM)



Ch1 A Failed Upper Limit

Result: A FAILED

5/4/2023 1:46 PM Page 15 of 47



Line Gain -10 200kTermination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

W(watts) (Input Power):

• DCX

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00

5/4/2023 1:46 PM Page 16 of 47



Port D (hex): 00

Line Gain -10 200kTermination: Verify Connections

Waveform: Sine

Generator Level: 0.000 dBu
DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/4/2023 1:44:42.397 PM)

Ch1 285.8 mVrms

Gain (5/4/2023 1:44:42.397 PM)

Ch1 -8.659 dB

THD+N Ratio (5/4/2023 1:44:42.397 PM)

Ch1 ---- %

Frequency (5/4/2023 1:44:42.397 PM)

Ch1 ---- Hz

5/4/2023 1:46 PM Page 17 of 47



#### Line Gain -10 200kTermination : Stepped Frequency Sweep -10

Generator Level: 0.000 dBu
DC Offset: 0.000 V
EQ: None

Start Frequency: 20.0000 kHz
Stop Frequency: 20.0000 Hz
Step Type: Logarithmic

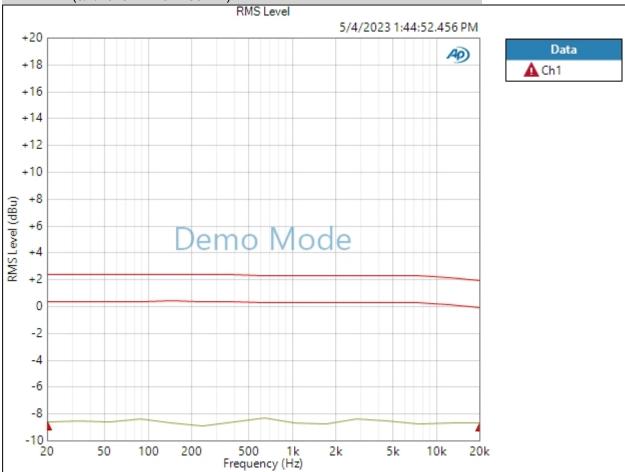
Number of Points: 15

Weighting Filter: Signal Path

High-pass Filter: 20 Hz Phase Ref Channel: Ch1

Measured 1 5/4/2023 1:44:52 PM

#### RMS Level (5/4/2023 1:44:52.456 PM)

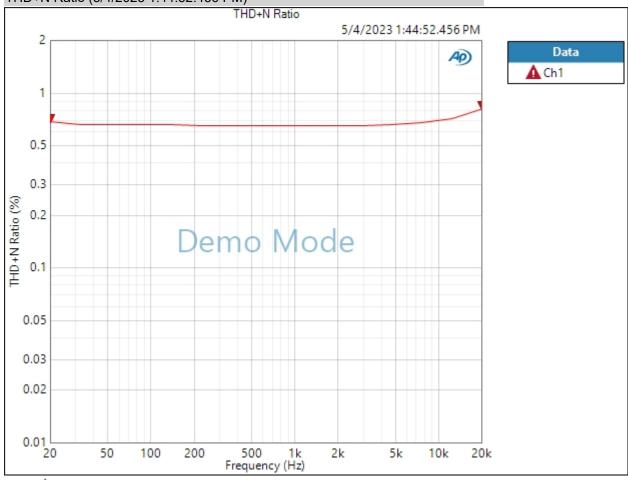


5/4/2023 1:46 PM Page 18 of 47



Result: A FAILED

THD+N Ratio (5/4/2023 1:44:52.456 PM)



Ch1 ▲ Failed Upper Limit

Result: A FAILED

5/4/2023 1:46 PM Page 19 of 47



Line Gain -10 600 Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 600 ohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

W(watts) (Input Power):

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00

5/4/2023 1:46 PM Page 20 of 47



Port D (hex): 00

Line Gain -10 600 Termination: Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/4/2023 1:44:57.645 PM)

Ch1 293.5 mVrms

Gain (5/4/2023 1:44:57.645 PM)

Ch1 1.570 dB

THD+N Ratio (5/4/2023 1:44:57.645 PM)

Ch1 ---- %

Frequency (5/4/2023 1:44:57.645 PM)

Ch1 ---- Hz

Line Gain -10 600 Termination: Level and Gain -10

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/4/2023 1:45:00.880 PM)

Result: V PASSED

5/4/2023 1:46 PM Page 21 of 47



Line Gain +5 200kTermination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

W(watts) (Input Power):

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00

5/4/2023 1:46 PM Page 22 of 47



Port D (hex): 00

Line Gain +5 200kTermination : Verify Connections

Waveform: Sine

Generator Level: 0.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/4/2023 1:45:06.220 PM)

Ch1 289.7 mVrms

Gain (5/4/2023 1:45:06.220 PM)

Ch1 -8.542 dB

THD+N Ratio (5/4/2023 1:45:06.220 PM)

Ch1 ---- %

Frequency (5/4/2023 1:45:06.220 PM)

Ch1 ---- Hz

5/4/2023 1:46 PM Page 23 of 47



#### Line Gain +5 200kTermination : Stepped Frequency Sweep +5

Generator Level: 0.000 dBu
DC Offset: 0.000 V
EQ: None

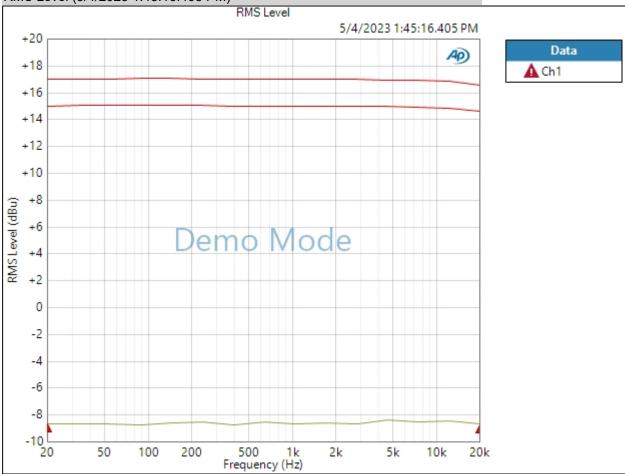
Start Frequency: 20.0000 kHz
Stop Frequency: 20.0000 Hz
Step Type: Logarithmic

Number of Points: 15

Weighting Filter: Signal Path
High-pass Filter: 20 Hz
Phase Ref Channel: Ch1

Measured 1 5/4/2023 1:45:16 PM

#### RMS Level (5/4/2023 1:45:16.405 PM)



5/4/2023 1:46 PM Page 24 of 47



Result: A FAILED

THD+N Ratio (5/4/2023 1:45:16.405 PM)



Ch1 A Failed Upper Limit

Result: A FAILED

5/4/2023 1:46 PM Page 25 of 47



Line Gain +5 600 Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 600 ohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

W(watts) (Input Power):

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00

5/4/2023 1:46 PM Page 26 of 47



Port D (hex): 00

Line Gain +5 600 Termination : Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/4/2023 1:45:21.736 PM)

Ch1 295.4 mVrms

Gain (5/4/2023 1:45:21.736 PM)

Ch1 1.628 dB

THD+N Ratio (5/4/2023 1:45:21.736 PM)

Ch1 ---- %

Frequency (5/4/2023 1:45:21.736 PM)

Ch1 ---- Hz

Line Gain +5 600 Termination: Level and Gain +5

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/4/2023 1:45:25.794 PM)

Channel Lower Limit Value Upper Limit
Ch1 +3.500 dBu -8.571 dBu +6.500 dBu

Result: A FAILED

5/4/2023 1:46 PM Page 27 of 47



Line Gain -5 600 Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 600 ohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL

• DCX

dBm (Input Power):

W(watts) (Input Power):

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00

5/4/2023 1:46 PM Page 28 of 47

600.0 ohm



Port D (hex): 00

Line Gain -5 600 Termination: Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/4/2023 1:45:31.121 PM)

Ch1 296.6 mVrms

Gain (5/4/2023 1:45:31.121 PM)

Ch1 1.662 dB

THD+N Ratio (5/4/2023 1:45:31.121 PM)

Ch1 ---- %

Frequency (5/4/2023 1:45:31.121 PM)

Ch1 ---- Hz

Line Gain -5 600 Termination: Level and Gain -5

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/4/2023 1:45:34.879 PM)

Channel Lower Limit Value Upper Limit
Ch1 -6.500 dBu -8.713 dBu -3.500 dBu

Result: A FAILED

5/4/2023 1:46 PM Page 29 of 47



Line Gain 0 600 Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 600 ohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL

dBm (Input Power):
W(watts) (Input Power):

dBSPL2 Calibrator Level:

600.0 ohm 8.000 ohm

00

94.000 dBSPL

• DCX

Port C (hex):

 DC Output 1:
 0.000 V

 DC Output 1:
 Off

 DC Output 2:
 0.000 V

 DC Output 2:
 Off

 Port A (hex):
 00

 Port B (hex):
 00

5/4/2023 1:46 PM Page 30 of 47



Port D (hex): 00

Line Gain 0 600 Termination: Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/4/2023 1:45:40.227 PM)

Ch1 290.5 mVrms

Gain (5/4/2023 1:45:40.227 PM)

Ch1 1.482 dB

THD+N Ratio (5/4/2023 1:45:40.227 PM)

Ch1 ---- %

Frequency (5/4/2023 1:45:40.227 PM)

Ch1 ---- Hz

Line Gain 0 600 Termination: Level and Gain 0

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/4/2023 1:45:43.990 PM)

Channel Lower Limit Value Upper Limit
Ch1 -1.500 dBu -8.536 dBu +1.500 dBu

Result: A FAILED

5/4/2023 1:46 PM Page 31 of 47



Line Gain +10 600 Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 600 ohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL

• DCX

dBm (Input Power):

W(watts) (Input Power):

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00

5/4/2023 1:46 PM Page 32 of 47

600.0 ohm



Port D (hex): 00

Line Gain +10 600 Termination: Verify Connections

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/4/2023 1:45:49.286 PM)

Ch1 288.6 mVrms

Gain (5/4/2023 1:45:49.286 PM)

Ch1 1.426 dB

THD+N Ratio (5/4/2023 1:45:49.286 PM)

Ch1 ---- %

Frequency (5/4/2023 1:45:49.286 PM)

Ch1 ---- Hz

Line Gain +10 600 Termination: Level and Gain +10

Waveform: Sine

Generator Level: -10.000 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/4/2023 1:45:53.085 PM)

Channel Lower Limit Value Upper Limit
Ch1 +8.500 dBu -8.642 dBu +11.500 dBu

Result: A FAILED

5/4/2023 1:46 PM Page 33 of 47



Line Gain +10 200k Termination Level Hi : Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL

W(watts) (Input Power):

dBSPL2 Calibrator Level:

dBm (Input Power):

• DCX

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00

5/4/2023 1:46 PM Page 34 of 47

94.000 dBSPL

600.0 ohm



Port D (hex): 00

Line Gain +10 200k Termination Level Hi: Verify Connections

Waveform: Sine

Generator Level: -20.000 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/4/2023 1:45:58.484 PM)

Ch1 289.1 mVrms

Gain (5/4/2023 1:45:58.484 PM)

Ch1 11.441 dB

THD+N Ratio (5/4/2023 1:45:58.484 PM)

Ch1 ---- %

Frequency (5/4/2023 1:45:58.484 PM)

Ch1 ---- Hz

5/4/2023 1:46 PM Page 35 of 47



#### Line Gain +10 200k Termination Level Hi: Noise Recorder (RMS) CW

Waveform: None 20 kHz Low-pass Filter: Weighting Filter: Signal Path

High-pass Filter:

Sweep Time: 0.00:00:03.000

Reading Rate: 10/sec

Input Bandwidth: Use Signal Path

Record Acquisition: False

Measured 1 5/4/2023 1:46:10 PM

20 Hz

#### RMS Level (5/4/2023 1:46:10.925 PM)



Result: A FAILED

5/4/2023 1:46 PM Page 36 of 47



5/4/2023 1:46 PM Page 37 of 47



Line Gain +10 200k Termination Level Low: Signal Path Setup

Output Connector: Analog Balanced

Channels: 1

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL

• DCX

dBSPL2 Calibrator Level:

W(watts) (Input Power):

dBm (Input Power):

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00

5/4/2023 1:46 PM Page 38 of 47

94.000 dBSPL

600.0 ohm



Port D (hex): 00

Line Gain +10 200k Termination Level Low: Verify Connections

Waveform: Sine

Generator Level: -20.000 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/4/2023 1:46:17.393 PM)

Ch1 290.8 mVrms

Gain (5/4/2023 1:46:17.393 PM)

Ch1 11.491 dB

THD+N Ratio (5/4/2023 1:46:17.393 PM)

Ch1 ---- %

Frequency (5/4/2023 1:46:17.393 PM)

Ch1 ---- Hz

5/4/2023 1:46 PM Page 39 of 47



#### Line Gain +10 200k Termination Level Low: Noise Recorder (RMS) CCW

Waveform: None
Low-pass Filter: 20 kHz
Weighting Filter: Signal Path

High-pass Filter: 20 Hz

Sweep Time: 0.00:00:03.000

Reading Rate: 10/sec

Input Bandwidth: Use Signal Path

Record Acquisition: False

Measured 1 5/4/2023 1:46:31 PM

#### RMS Level (5/4/2023 1:46:31.565 PM)



Result: A FAILED

5/4/2023 1:46 PM Page 40 of 47



5/4/2023 1:46 PM Page 41 of 47



Hi Z Gain -10 2.2M 200k Termination : Signal Path Setup

Output Connector: Analog Balanced

Channels: 2

Source Impedance: 100 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL

dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm W(watts) (Input Power): 8.000 ohm

• DCX

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00

5/4/2023 1:46 PM Page 42 of 47



Port D (hex): 00

Hi Z Gain -10 2.2M 200k Termination: Verify Connections

Waveform: Sine

Generator Level: -22.300 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/4/2023 1:46:36.780 PM)

Ch1 287.3 mVrms

Gain (5/4/2023 1:46:36.780 PM)

Ch1 13.685 dB

THD+N Ratio (5/4/2023 1:46:36.780 PM)

Ch1 ---- %

Frequency (5/4/2023 1:46:36.780 PM)

Ch1 ---- Hz

Hi Z Gain -10 2.2M 200k Termination: Level and Gain 2.2M

Waveform: Sine

Generator Level: -22.300 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/4/2023 1:46:42.290 PM)

Channel Lower Limit Value Upper Limit
Ch1 -2.000 dBu -8.485 dBu +2.000 dBu

Result: A FAILED

5/4/2023 1:46 PM Page 43 of 47



Hi Z Gain -10 47k 200k Termination : Signal Path Setup
Output Connector:
Analog Unbalanced

Channels: 2

Source Impedance: 50 ohm
Output EQ: None

Input Connector: Analog Balanced

Channels: 1

Channel: Ch1

Termination: 200 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL

• DCX

dBm (Input Power):

W(watts) (Input Power):

DC Output 1: 0.000 V

DC Output 1: Off

DC Output 2: 0.000 V

DC Output 2: Off

Port A (hex): 00

Port B (hex): 00

Port C (hex): 00

5/4/2023 1:46 PM Page 44 of 47

600.0 ohm



Port D (hex): 00

Hi Z Gain -10 47k 200k Termination : Verify Connections

Waveform: Sine

Generator Level: -22.300 dBu DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/4/2023 1:46:47.728 PM)

Ch1 294.4 mVrms

Gain (5/4/2023 1:46:47.728 PM)

Ch1 13.897 dB

THD+N Ratio (5/4/2023 1:46:47.728 PM)

Ch1 ---- %

Frequency (5/4/2023 1:46:47.728 PM)

Ch1 ---- Hz

Hi Z Gain -10 47k 200k Termination: Level and Gain 47K

Waveform: Sine

Generator Level: -22.300 dBu
DC Offset: 0.000 V
Frequency: 1.00000 kHz

RMS Level (5/4/2023 1:46:51.672 PM)

Channel Lower Limit Value Upper Limit
Ch1 -8.000 dBu -8.593 dBu -4.000 dBu

Result: A FAILED

5/4/2023 1:46 PM Page 45 of 47



Dummy Signal Path For Report : Signal Path Setup

Output Connector: Analog Unbalanced

Channels: 2

Source Impedance: 50 ohm
Output EQ: None

Input Connector: Analog Unbalanced

Channels: 2

Termination: 100 kohm

Input Bandwidth: AC (<10 Hz) - 90k (192 kHz SR)

Device Delay: 0.000 s
Input EQ: None

References

dBr G: 100.0 mVrms dBm (Output Power): 600.0 ohm W(watts) (Output Power): 8.000 ohm Shared Frequency Reference: 1.00000 kHz dBrA: 1.000 Vrms dBrB: 1.000 Vrms dBrA Offset: 0.000 dB dBrB Offset: 0.000 dB dBSPL1: 10.00 mVrms dBSPL2: 10.00 mVrms dBSPL1 Calibrator Level: 94.000 dBSPL dBSPL2 Calibrator Level: 94.000 dBSPL dBm (Input Power): 600.0 ohm

• DCX

W(watts) (Input Power):

0.000 V DC Output 1: DC Output 1: Off DC Output 2: 0.000 V Off DC Output 2: Port A (hex): 00 Port B (hex): 00 Port C (hex): 00 00 Port D (hex):

5/4/2023 1:46 PM Page 46 of 47



**Dummy Signal Path For Report : Verify Connections** 

Waveform: Sine

Generator Level: 100.0 mVrms

DC Offset: 0.000 V

Frequency: 1.00000 kHz

RMS Level (5/4/2023 1:46:55.361 PM)

Ch1 291.8 mVrms Ch2 288.8 mVrms

5/4/2023 1:46 PM Page 47 of 47