

April 2013

# MOCD217M Dual Channel Phototransistor

# **Dual Channel Phototransistor Small Outline Surface Mount Optocouplers (Low Input Current)**

## **Features**

- UL Recognized (File #E90700, Volume 2)
- VDE Recognized (File #136616) (add option "V" for VDE approval, i.e, MOCD217VM)
- Low Input Current (specified @ 1mA)
- Minimum BV<sub>CEO</sub> of 30 Volts Guaranteed
- Convenient Plastic SOIC-8 Surface Mountable Package Style
- Standard SOIC-8 Footprint, with 0.050" Lead Spacing
- High Input-Output Isolation of 2500 V<sub>AC(rms)</sub>
   Guaranteed

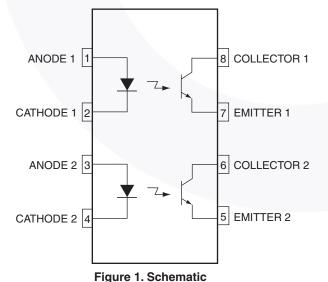
# **Applications**

- Interfacing and Coupling Systems of Different Potentials and Impedances
- General Purpose Switching Circuits
- Monitor and Detection Circuits

### **Description**

The MOCD217M device consists of two gallium arsenide infrared emitting diodes optically coupled to two monolithic silicon phototransistor detectors, in a surface mountable, small outline plastic package. It is ideally suited for high density applications and eliminates the need for through-the-board mounting.

#### **Schematic**



# **Package Outline**

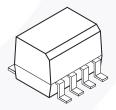


Figure 2. Package Outline

# **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.  $T_A = 25^{\circ}C$  unless otherwise specified.

Symbol	Rating	Value	Unit
Emitter			
I <sub>F</sub>	Forward Current – Continuous	60	mA
I <sub>F</sub> (pk)	Forward Current – Peak (PW = 100 µs, 120 pps)	1.0	А
$V_{R}$	Reverse Voltage	6.0	V
P <sub>D</sub>	LED Power Dissipation @ T <sub>A</sub> = 25°C	90	mW
	Derate above 25°C	0.8	mW/°C
Detector			
V <sub>CEO</sub>	Collector-Emitter Voltage	30	V
V <sub>ECO</sub>	Emitter-Base Voltage	7.0	V
I <sub>C</sub>	Collector Current-Continuous	150	mA
$P_{D}$	Detector Power Dissipation @ T <sub>A</sub> = 25°C	150	mW
	Derate above 25°C	1.76	mW/°C
Total Device			
V <sub>ISO</sub>	Input-Output Isolation Voltage (f = 60 Hz, t = 1 minute)	2500	Vac(rms)
P <sub>D</sub>	Total Device Power Dissipation @ T <sub>A</sub> = 25°C	250	mW
	Derate above 25°C	2.94	mW/°C
T <sub>A</sub>	Ambient Operating Temperature Range	-40 to +100	°C
T <sub>stg</sub>	Storage Temperature Range	-40 to +150	°C

#### Notes:

- 1. Input-Output Isolation Voltage,  $V_{\text{ISO}}$ , is an internal device dielectric breakdown rating.
- 2. For this test, pins 1, 2, 3 and 4 are common and pins 5, 6, 7 and 8 are common.
- 3.  $V_{ISO}$  rating of 2500  $V_{AC(rms)}$  for t = 1 minute is equivalent to a rating of 3,000  $V_{AC(rms)}$  for t = 1 second.

#### **Electrical Characteristics**

 $T_A = 25^{\circ}C$  unless otherwise specified.

Symbol	Parameter	Test Conditions	Min.	Тур.*	Max.	Unit
Emitter						
V <sub>F</sub>	Input Forward Voltage	I <sub>F</sub> = 10 mA		1.05	1.3	V
I <sub>R</sub>	Reverse Leakage Current	V <sub>R</sub> = 6.0 V		0.1	100	μΑ
С	Capacitance			18		pF
Detector			Į.			
I <sub>CEO1</sub>	Collector-Emitter Dark Current	V <sub>CE</sub> = 10 V, T <sub>A</sub> = 25°C		1.0	50	nA
I <sub>CEO2</sub>		V <sub>CE</sub> = 10 V, T <sub>A</sub> = 100°C		1.0		μΑ
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = 100 μA	30	90		V
BV <sub>ECO</sub>	Emitter-Collector Breakdown Voltage	Ι <sub>Ε</sub> = 100 μΑ	7.0	7.8		V
C <sub>CE</sub>	Collector-Emitter Capacitance	f = 1.0 MHz, V <sub>CE</sub> = 0 V		7.0		pF
Coupled					'	
CTR	Current Transfer Ratio <sup>(4)</sup>	I <sub>F</sub> = 1.0 mA, V <sub>CE</sub> = 5 V	100	130		%
V <sub>CE (sat)</sub>	Collector-Emitter Saturation Voltage	$I_C = 2.0 \text{ mA}, I_F = 10 \text{ mA}$		0.35	0.4	V
t <sub>on</sub>	Turn-On Time	$I_C$ = 2.0 mA, $V_{CC}$ = 10 V, $R_L$ = 100 $\Omega$ (Fig. 8)		7.5		μs
t <sub>off</sub>	Turn-Off Time	$I_C$ = 2.0 mA, $V_{CC}$ = 10 V, $R_L$ = 100 $\Omega$ (Fig. 8)		5.7		μs
t <sub>r</sub>	Rise Time	$I_C$ = 2.0 mA, $V_{CC}$ = 10 V, $R_L$ = 100 $\Omega$ (Fig. 8)		3.2		μs
t <sub>f</sub>	Fall Time	$I_C$ = 2.0 mA, $V_{CC}$ = 10 V, $R_L$ = 100 $\Omega$ (Fig. 8)		4.7		μs
V <sub>ISO</sub>	Isolation Surge Voltage <sup>(1)(2)(3)</sup>	f = 60 Hz, t = 1 minute	2500			Vac(rms)
R <sub>ISO</sub>	Isolation Resistance <sup>(2)</sup>	V <sub>I-O</sub> = 500 V	10 <sup>11</sup>			Ω
C <sub>ISO</sub>	Isolation Capacitance <sup>(2)</sup>	V <sub>I-O</sub> = 0 V, f = 1 MHz		0.2		pF

<sup>\*</sup>Typical values at T<sub>A</sub> = 25°C

#### Notes

- 1. Input-Output Isolation Voltage,  $V_{\text{ISO}}$ , is an internal device dielectric breakdown rating.
- 2. For this test, pins 1, 2, 3 and 4 are common and pins 5, 6, 7 and 8 are common.
- 3.  $V_{ISO}$  rating of 2500  $V_{AC(rms)}$  for t = 1 minute is equivalent to a rating of 3,000  $V_{AC(rms)}$  for t = 1 second.
- 4. Current Transfer Ratio (CTR) =  $I_C / I_F \times 100\%$ .

# **Typical Performance Curves**

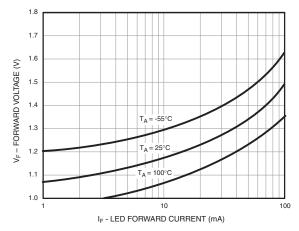


Figure 3. LED Forward Voltage vs. Forward Current

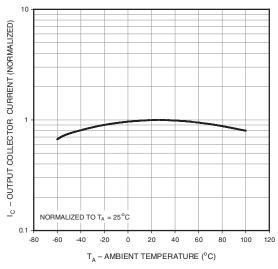


Figure 5. Output Current vs. Ambient Temperature

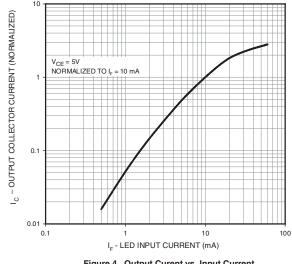


Figure 4. Output Curent vs. Input Current

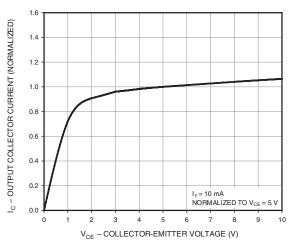


Figure 6. Output Current vs. Collector - Emitter Voltage

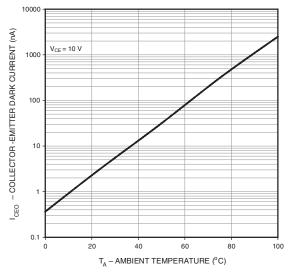


Figure 7. Dark Current vs. Ambient Temperature

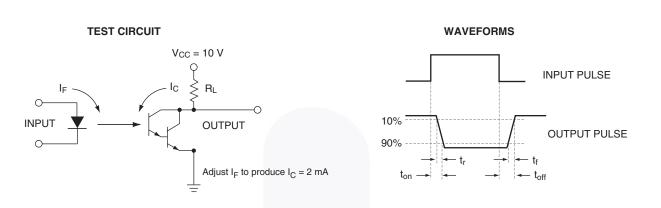
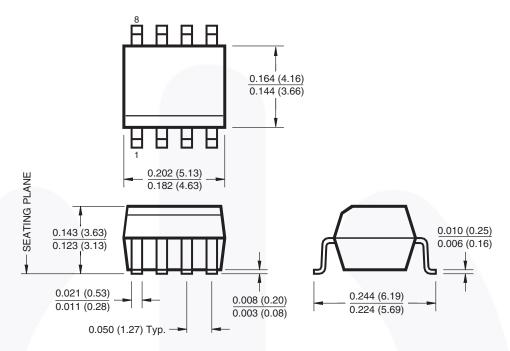


Figure 8. Switching Time Test Circuit and Waveform

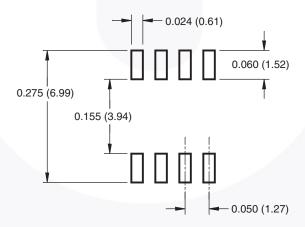
## **Package Dimensions**

#### 8-pin SOIC Surface Mount



Lead Coplanarity: 0.004 (0.10) MAX

#### **Recommended Pad Layout**



Dimensions in inches (mm).

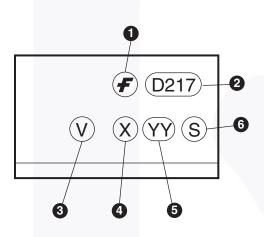
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# **Ordering Information**

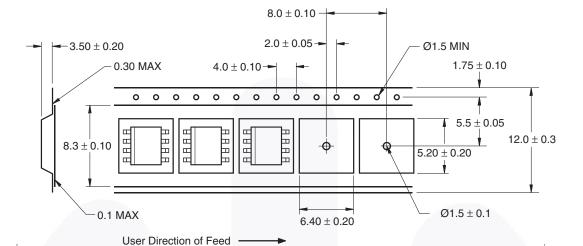
Option	Order Entry Identifier	Description	
V	V	VDE 0884	
R2	R2	Tape and Reel (2500 units per reel)	
R2V	R2V	VDE 0884, Tape and Reel (2500 units per reel)	

# **Marking Information**



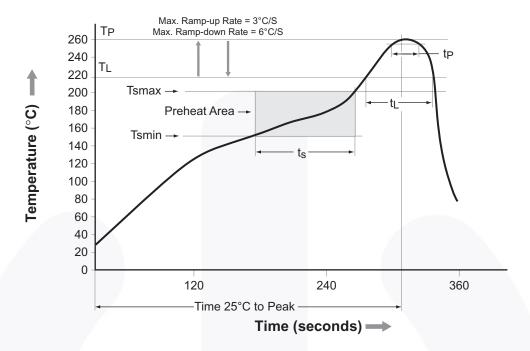
Definitions			
1	Fairchild logo		
2	Device number		
3	VDE mark (Note: Only appears on parts ordered with VDE option – See order entry table)		
4	One digit year code, e.g., '8'		
5	Two digit work week ranging from '01' to '53'		
6	Assembly package code		

# **Carrier Tape Specifications**



Dimensions in mm

## **Reflow Profile**



Profile Freature	Pb-Free Assembly Profile
Temperature Minimum (Tsmin)	150°C
Temperature Maximum (Tsmax)	200°C
Time (t <sub>S</sub> ) from (Tsmin to Tsmax)	60-120 seconds
Ramp-up Rate (t <sub>L</sub> to t <sub>P</sub> )	3°C/second maximum
Liquidous Temperature (T <sub>L</sub> )	217°C
Time (t <sub>L</sub> ) Maintained Above (T <sub>L</sub> )	60-150 seconds
Peak Body Package Temperature	260°C +0°C / -5°C
Time (t <sub>P</sub> ) within 5°C of 260°C	30 seconds
Ramp-down Rate (T <sub>P</sub> to T <sub>L</sub> )	6°C/second maximum
Time 25°C to Peak Temperature	8 minutes maximum



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