

jlink 和雷达按照如下接法对应接好，并将 jlink 连接到电脑上

JLINK            雷达

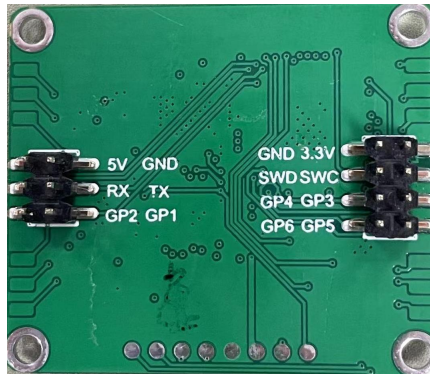
VCC -----3V3

GND-----GND

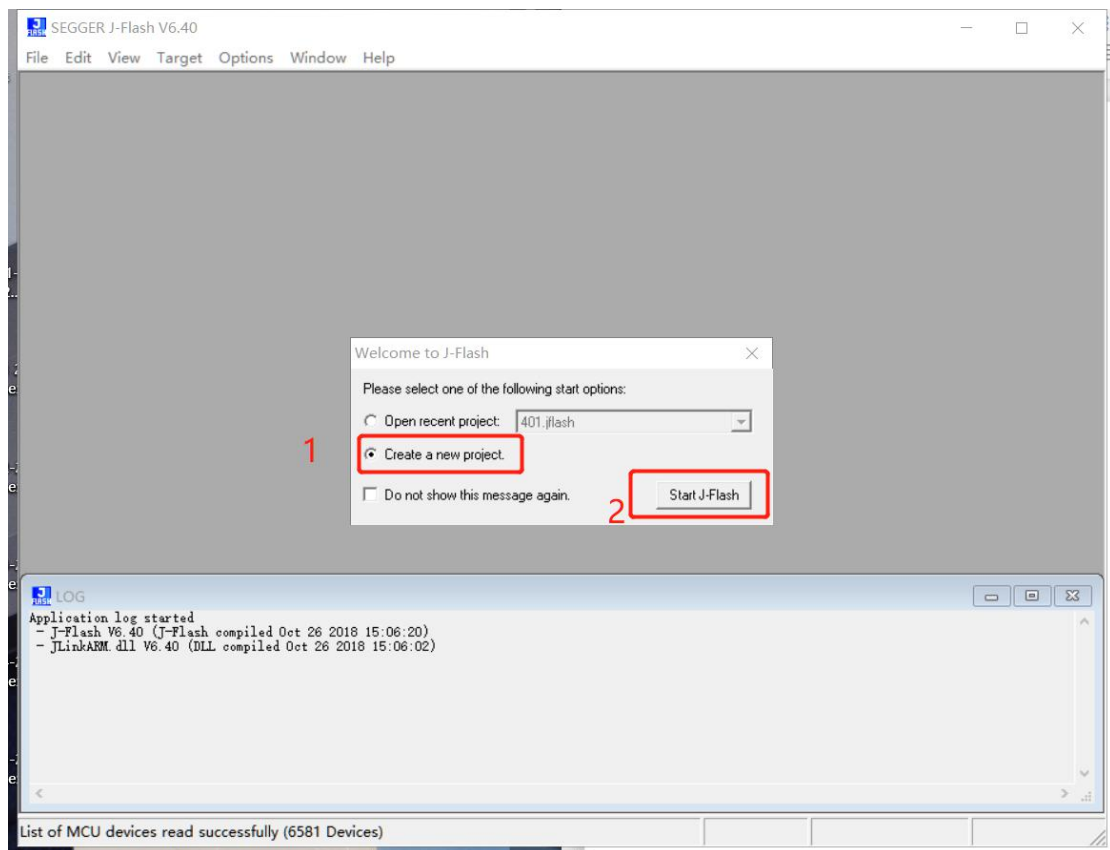
SWDIO-----SWD

SWCLK-----SWC

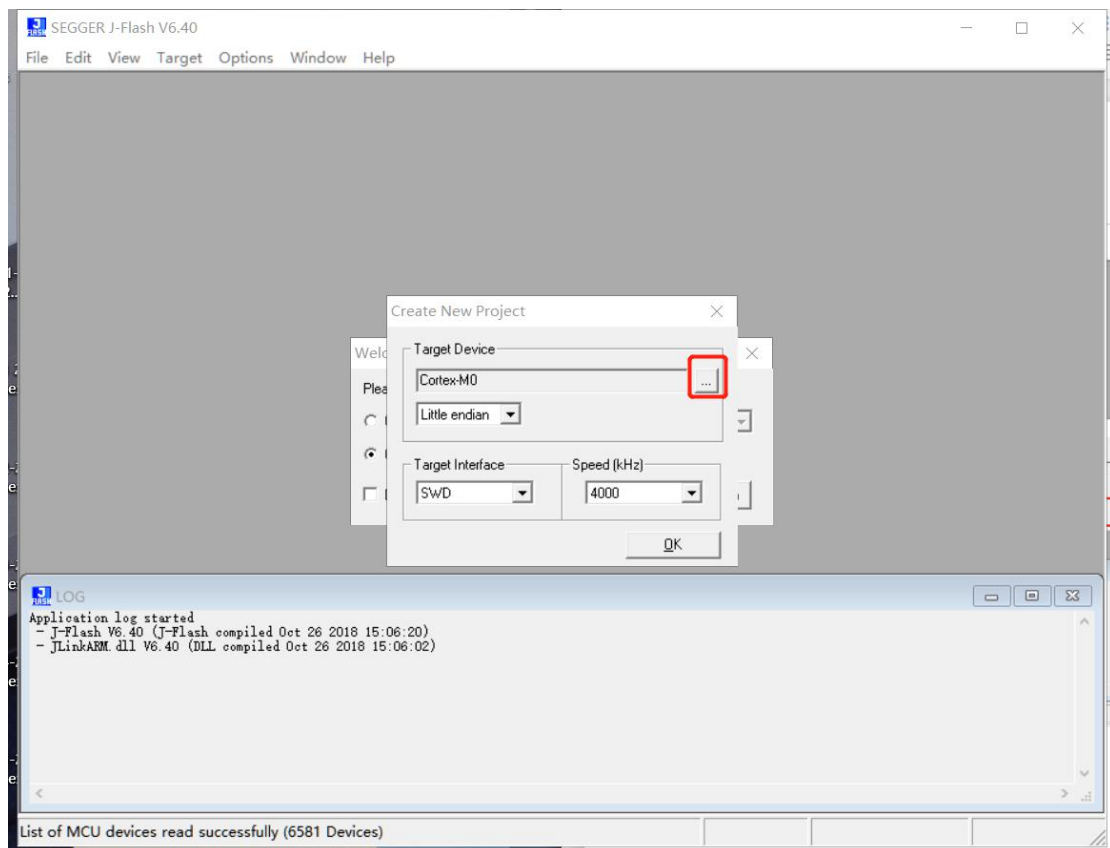
VCC	1	<input type="checkbox"/>	<input type="checkbox"/>	2	VCC (optional)
N/U	3	<input type="checkbox"/>	<input type="checkbox"/>	4	GND
N/U	5	<input type="checkbox"/>	<input type="checkbox"/>	6	GND
SWDIO	7	<input type="checkbox"/>	<input type="checkbox"/>	8	GND
SWCLK	9	<input type="checkbox"/>	<input type="checkbox"/>	10	GND
N/U	11	<input type="checkbox"/>	<input type="checkbox"/>	12	GND
SWO	13	<input type="checkbox"/>	<input type="checkbox"/>	14	GND
RESET	15	<input type="checkbox"/>	<input type="checkbox"/>	16	GND
N/C	17	<input type="checkbox"/>	<input type="checkbox"/>	18	GND
N/C	19	<input type="checkbox"/>	<input type="checkbox"/>	20	GND

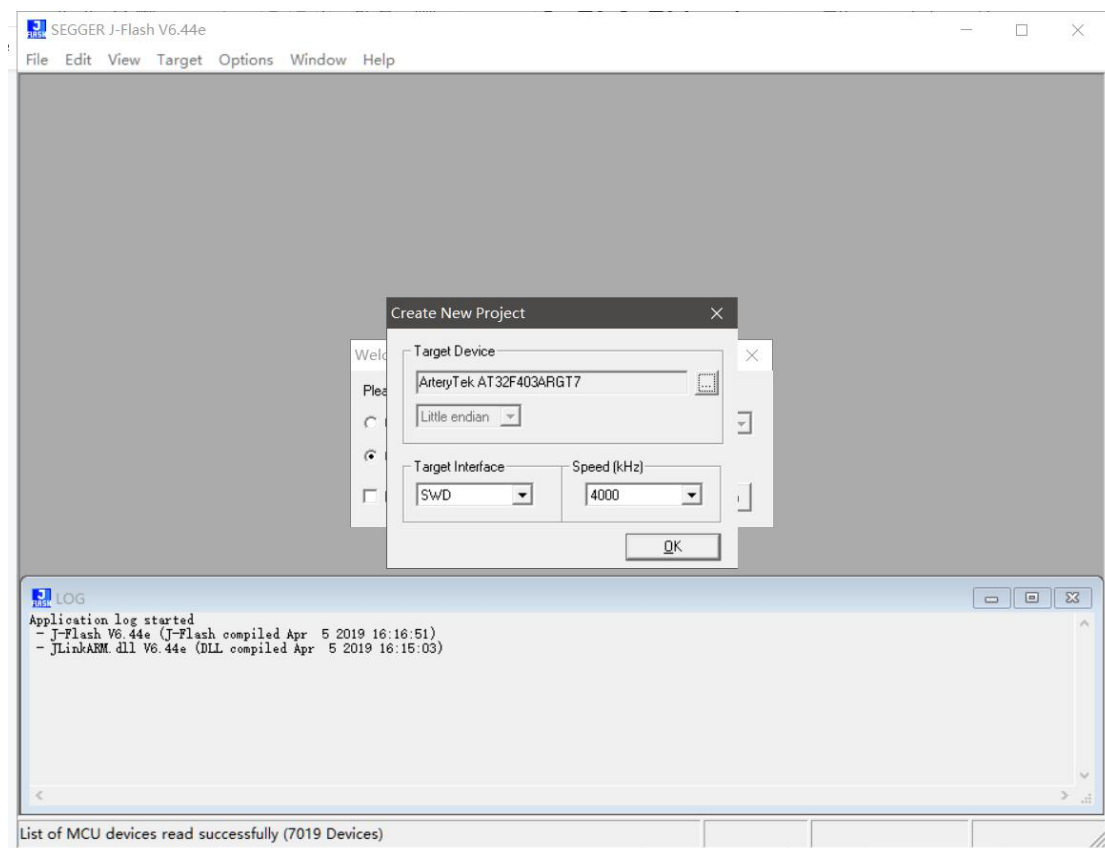
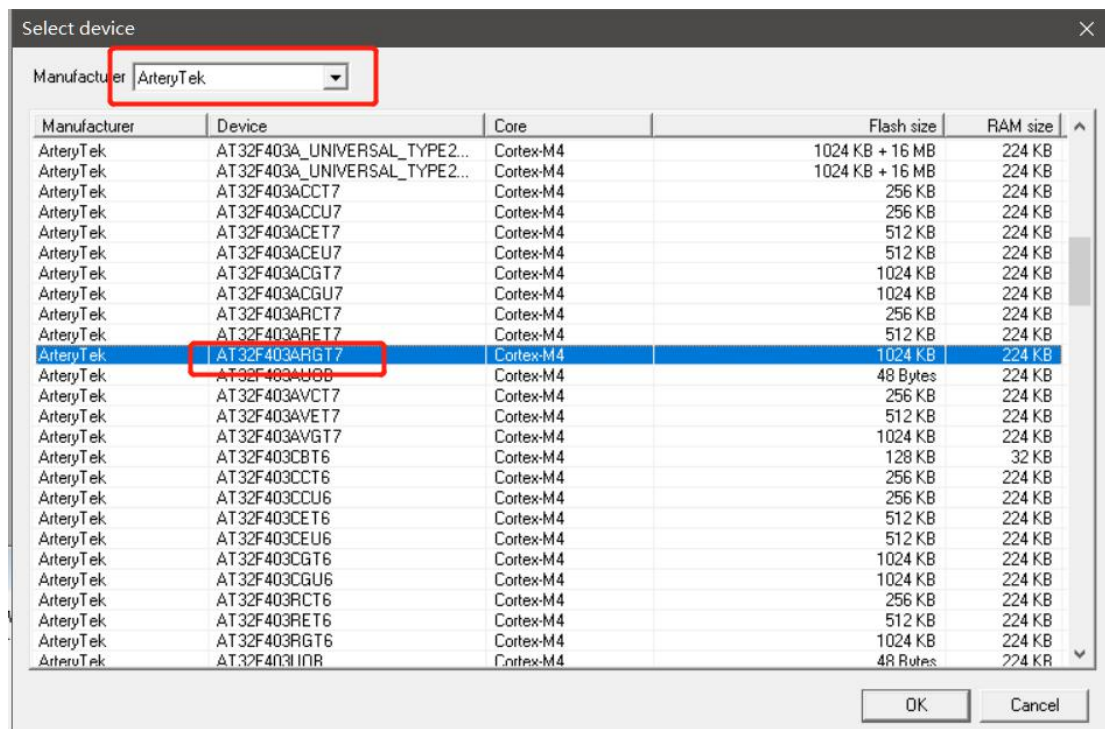


## 1: 创建一个新的 project

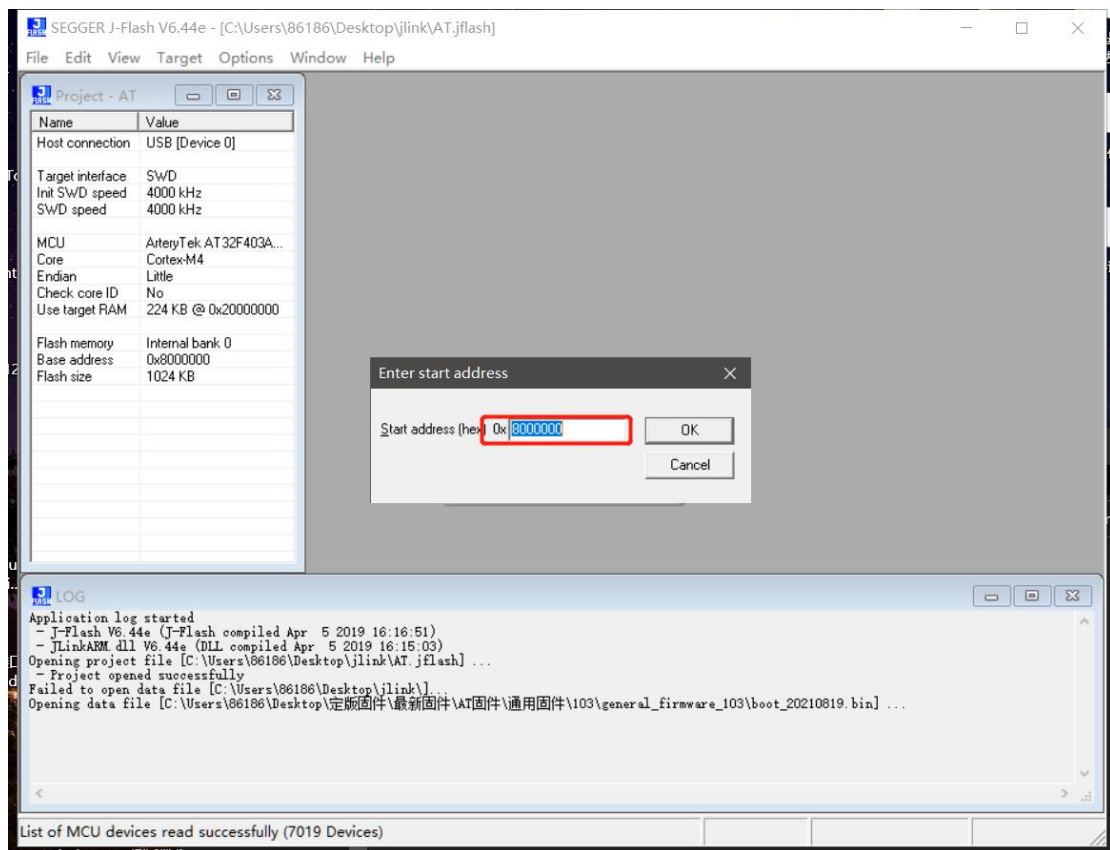


## 2: 选择芯片类型：AT 类目下的 AT32F403ARGT7，然后点 OK (如果找不到该芯片目录则联系提供芯片包安装即可)

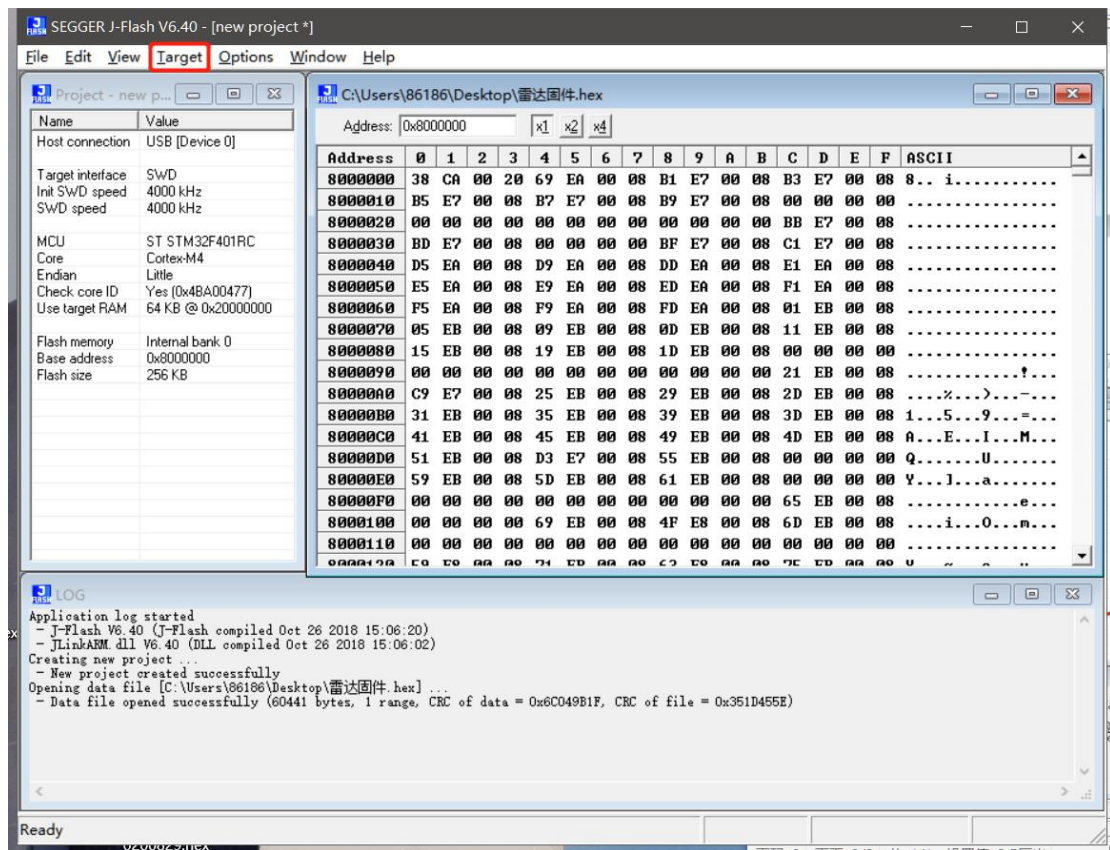




3: 将 boot 雷达固件拖拽到该区域，会提醒地址修改，正常起始地址为 0X8000000 即可



4: 单击 Target -> Connect



The screenshot displays the SEGGER J-Flash V6.40 software interface. The main window shows a memory dump starting at address 0x80000000. The left sidebar contains a 'Project' window with details for a target STM32F401RC microcontroller. The bottom 'LOG' window shows a sequence of events, with the final status 'Connected successfully' highlighted by a red rectangle.

**Project Details:**

- Name: Value
- Host connection: USB [Device 0]
- Target interface: SWD
- Init SWD speed: 4000 kHz
- SWD speed: 4000 kHz
- MCU: ST STM32F401RC
- Core: Cortex-M4
- Endian: Little
- Check core ID: Yes (0x4BA00477)
- Use target RAM: 64 KB @ 0x20000000
- Flash memory: Internal bank 0
- Base address: 0x80000000
- Flash size: 256 KB

**Memory Dump (Address 0x80000000):**

Address	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	ASCII
80000000	38	CA	00	20	69	EA	00	08	B1	E7	00	08	B3	E7	00	08	8 . i . . . . .
80000010	B5	E7	00	08	B7	E7	00	08	B9	E7	00	08	00	00	00	00	.....
80000020	00	00	00	00	00	00	00	00	00	00	00	00	BB	E7	00	08	.....
80000030	BD	E7	00	08	00	00	00	00	BF	E7	00	08	C1	E7	00	08	.....
80000040	D5	EA	00	08	D9	EA	00	08	DD	EA	00	08	E1	EA	00	08	.....
80000050	E5	EA	00	08	E9	EA	00	08	ED	EA	00	08	F1	EA	00	08	.....
80000060	F5	EA	00	08	F9	EA	00	08	FD	EA	00	08	01	EB	00	08	.....
80000070	05	EB	00	08	09	EB	00	08	0D	EB	00	08	11	EB	00	08	.....
80000080	15	EB	00	08	19	EB	00	08	1D	EB	00	08	00	00	00	00	.....
80000090	00	00	00	00	00	00	00	00	00	00	00	00	21	EB	00	08	.....!
800000A0	C9	E7	00	08	25	EB	00	08	29	EB	00	08	2D	EB	00	08	.....%>.
800000B0	31	EB	00	08	35	EB	00	08	39	EB	00	08	3D	EB	00	08	.....1.5.9.=
800000C0	41	EB	00	08	45	EB	00	08	49	EB	00	08	4D	EB	00	08	.....A...E...I...M...
800000D0	51	EB	00	08	D3	E7	00	08	55	EB	00	08	00	00	00	00	.....Q.....U.....
800000E0	59	EB	00	08	5D	EB	00	08	61	EB	00	08	00	00	00	00	.....V...l...a.....
800000F0	00	00	00	00	00	00	00	00	00	00	00	00	65	EB	00	08	.....e.....
80001000	00	00	00	00	69	EB	00	08	4F	E8	00	08	6D	EB	00	08	.....i...O...m...
80001100	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	.....

**LOG:**

- ROMtbl[0][0]: E000E000, CID: B105E0D, PID: 000B800C SCS-M7
- ROMtbl[0][1]: E0001000, CID: B105E0D, PID: 003B8002 DWT
- ROMtbl[0][2]: E0002000, CID: B105E0D, PID: 002B8003 FFB
- ROMtbl[0][3]: E0000000, CID: B105E0D, PID: 003B8001 ITM
- ROMtbl[0][4]: E0040000, CID: B105900D, PID: 000B89A1 TPIU
- ROMtbl[0][5]: E0041000, CID: B105900D, PID: 000B8925 ETM
- Executing init sequence ...
- Initialized successfully
- Target interface speed: 4000 kHz (Fixed)
- J-Link found 1 JTAG device, Core ID: 0x2BA01477 (None)
- Connected successfully

Ready Connected Core Id: 0x2BA01477 Speed: 4000 kHz

SEGGER J-Flash V6.40 - [new project \*]

File Edit View **Target** Options Window Help

Project - new p... [Icons]

Name	Value
Host connection	USB [Device 0]
Target interface	SWD
Init SWD speed	4000 kHz
SWD speed	4000 kHz
MCU	ST STM32F401RC
Core	Cortex-M4
Endian	Little
Check core ID	Yes (0x4BA00477)
Use target RAM	64 KB @ 0x20000000
Flash memory	Internal bank 0
Base address	0x80000000
Flash size	256 KB

Address: 0x8000000 [x1 x2 x4]

Address	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	ASCII
80000000	38	CA	00	20	69	EA	00	08	B1	E7	00	08	B3	E7	00	08	8...i.....
80000010	B5	E7	00	08	B7	E7	00	08	B9	E7	00	08	00	00	00	00	.....
80000020	00	00	00	00	00	00	00	00	00	00	00	00	BB	E7	00	08	.....
80000030	BD	E7	00	08	00	00	00	00	BF	E7	00	08	C1	E7	00	08	.....
80000040	D5	EA	00	08	D9	EA	00	08	DD	EA	00	08	E1	EA	00	08	.....
80000050	E5	EA	00	08	E9	EA	00	08	ED	EA	00	08	F1	EA	00	08	.....
80000060	F5	EA	00	08	F9	EA	00	08	FD	EA	00	08	01	EB	00	08	.....
80000070	05	EB	00	08	09	EB	00	08	0D	EB	00	08	11	EB	00	08	.....
80000080	15	EB	00	08	19	EB	00	08	1D	EB	00	08	00	00	00	00	.....
80000090	00	00	00	00	00	00	00	00	00	00	00	21	EB	00	08	.....!	
800000A0	C9	E7	00	08	25	EB	00	08	29	EB	00	08	2D	EB	00	08	.....%>.-
800000B0	31	EB	00	08	35	EB	00	08	39	EB	00	08	3D	EB	00	08	1...5...9...=
800000C0	41	EB	00	08	45	EB	00	08	49	EB	00	08	4D	EB	00	08	A...E...I...M...
800000D0	51	EB	00	08	D3	E7	00	08	55	EB	00	08	00	00	00	00	Q...U...
800000E0	59	EB	00	08	5D	EB	00	08	61	EB	00	08	00	00	00	00	Y...l...a...
800000F0	00	00	00	00	00	00	00	00	00	00	00	65	EB	00	08	.....e...	
80001000	00	00	00	00	69	EB	00	08	4F	E8	00	08	6D	EB	00	08	...i...0...m...
80001100	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	.....
80001200	CA	E9	00	08	71	ED	00	08	C3	E9	00	08	75	ED	00	08	U...>...a...

LOG

Application log started

- J-Flash V6.40 (J-Flash compiled Oct 26 2018 15:06:20)
- JLinkARM.dll V6.40 (DLL compiled Oct 26 2018 15:06:02)

Creating new project ...

- New project created successfully

Opening data file [C:\Users\86186\Desktop\雷达固件.hex] ...

- Data file opened successfully (60441 bytes, 1 range, CRC of data = 0x6C049B1F, CRC of file = 0x351D455E)

Ready



7: 单击 Target -> manual Programming ->Program & Verify 进行固件烧录

