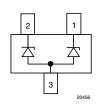


Vishay Semiconductors

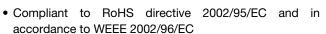
# **Two-Line ESD-Protection in SOT-23**





### **FEATURES**

- Two-line ESD-protection device
- ESD-protection acc. IEC 61000-4-2
  - ± 30 kV contact discharge
  - ± 30 kV air discharge
- Space saving SOT-23 package
- AEC-Q101 qualified
- e3 Sn

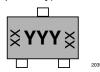






COMPLIANT GREEN (5-2008)<sup>3</sup> Available

**MARKING** (example only)



YYY = type code (see table below)

XX = date code

<b>ORDERING INFO</b>	RMATION				
DEVICE NAME	ENVIRONMENTAL STATUS	ORDERING CODE	TAPED UNITS PER REEL (8 mm TAPE ON 7" REEL)	MINIMUM ORDER QUANTITY	
CCOTO2C	Standard	GSOT03C-GS08	3000	15 000	
GSOT03C	Green	GSOT03C-V-G-08	3000	15 000	
CCOTO4C	Standard	GSOT04C-GS08	3000	15.000	
GSOT04C	Green	GSOT04C-V-G-08	3000	15 000	
GSOT05C	Standard	GSOT05C-GS08	3000	15 000	
G50105C	Green	GSOT05C-V-G-08	3000	13 000	
CCCTOOC	Standard	GSOT08C-GS08	2000	15 000	
GSOT08C	Green	GSOT08C-V-G-08	3000	15 000	
CCCT10C	Standard	GSOT12C-GS08	3000	15.000	
GSOT12C	Green	GSOT12C-V-G-08	3000	15 000	
000T1F0	Standard	GSOT15C-GS08	3000	15.000	
GSOT15C	Green	GSOT15C-V-G-08	3000	15 000	
CCOT04C	Standard	GSOT24C-GS08	3000	15.000	
GSOT24C	Green	GSOT24C-V-G-08	3000	15 000	
CCCTGCC	Standard	GSOT36C-GS08	3000	15.000	
GSOT36C	Green	GSOT36C-V-G-08	3000	15 000	

<sup>\*\*</sup> Please see document "Vishay Material Category Policy": www.vishay.com/doc?99902

# **GSOT03C to GSOT36C**

# Vishay Semiconductors Two-Line ESD-Protection in SOT-23



PACKA	GE DATA								
DEVICE NAME	PACKAGE NAME	TYPE CODE	ENVIRONMENTAL STATUS	WEIGHT	MOLDING COMPOUND FLAMMABILITY RATING	MOISTURE SENSITIVITY LEVEL	SOLDERING CONDITIONS		
GSOT03C	SOT-23	03C	Standard	8.8 mg	UL 94 V-0	MSL level 1	260 °C/10 s at terminals		
4301030	301-23	C1G	Green	8.1 mg	OL 94 V-0	(according J-STD-020)	200 O/ 10 3 at terrimas		
GSOT04C	SOT-23	04C	Standard	8.8 mg	UL 94 V-0	MSL level 1	260 °C/10 s at terminals		
G501040	301-23	C8G	Green	8.1 mg	OL 94 V-0	(according J-STD-020)	200 O/ 10 3 at terrimais		
GSOT05C	SOT-23	05C	Standard	8.8 mg	UL 94 V-0	MSL level 1	260 °C/10 s at terminals		
4301030	301-23	C2C	Green	8.1 mg	0204 0	OL 54 V-0	OL 34 V-0	(according J-STD-020)	200 O/ 10 3 at terrimais
GSOT08C	SOT-23	08C	Standard	8.8 mg	UL 94 V-0	MSL level 1	260 °C/10 s at terminals		
4301000	301-23	C3G	Green	8.1 mg	OL 94 V-0	(according J-STD-020)	260 °C/10 s at terminais		
GSOT12C	SOT-23	12C	Standard	8.8 mg	UL 94 V-0	MSL level 1	260 °C/10 s at terminals		
4301120	301-23	C4G	Green	8.1 mg	OL 94 V-0	(according J-STD-020)	200 O/10 3 at terminais		
GSOT15C	SOT-23	15C	Standard	8.8 mg	UL 94 V-0	MSL level 1	260 °C/10 s at terminals		
4301130	301-23	C5G	Green	8.1 mg	OL 94 V-0	(according J-STD-020)	200 C/10 S at terrilliais		
GSOT24C	SOT-23	24C	Standard	8.8 mg	UL 94 V-0	MSL level 1	260 °C/10 s at terminals		
G301240	301-23	C6G	Green	8.1 mg	OL 34 V-0	(according J-STD-020)	200 O/ 10 S at terrillials		
GSOT36C	SOT-23	36C	Standard	8.8 mg	UL 94 V-0	MSL level 1	260 °C/10 s at terminals		
G301300	301-23	C7G	Green	8.1 mg	OL 34 V-0	(according J-STD-020)	200 O/ TO S at terrillials		

ABSOLUTE MAXIMUM RATINGS GSOT03C					
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT	
Peak pulse current	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	lane.	30 30	Α	
reak puise current	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	Іррм		Α	
Peak pulse power	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	P <sub>PP</sub>	369	W	
reak puise power	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	ГРР	504	W	
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	W	± 30	kV	
ESD IIIIIIdility	Air discharge acc. IEC 61000-4-2; 10 pulses	$V_{ESD}$	± 30	kV	
Operating temperature	Junction temperature	T <sub>J</sub>	- 40 to + 125	°C	
Storage temperature		T <sub>STG</sub>	- 55 to + 150	°C	

PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT
Dook pulse comment	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	30	30	А
Peak pulse current	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	ІРРМ	30	А
Dook pulse power	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, t <sub>p</sub> = 8/20 μs; single shot	D	429	W
Peak pulse power	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	P <sub>PP</sub>	564	W
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V	± 30	kV
ESD IIIIIIuliity	Air discharge acc. IEC 61000-4-2; 10 pulses	$V_{ESD}$	± 30	kV
Operating temperature	Junction temperature	TJ	- 40 to + 125	°C
Storage temperature		T <sub>STG</sub>	- 55 to + 150	°C





ABSOLUTE MAXIMUM RATINGS GSOT05C					
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT	
Dook pulse current	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, t <sub>p</sub> = 8/20 μs; single shot	l	30	А	
Peak pulse current	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	Іррм	30	А	
Dock pulse power	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, t <sub>p</sub> = 8/20 μs; single shot	$P_PP$	480	W	
Peak pulse power	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	ГРР	612	W	
ECD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	\/	± 30	kV	
ESD immunity	Air discharge acc. IEC 61000-4-2; 10 pulses	$V_{ESD}$	± 30	kV	
Operating temperature	Junction temperature	T <sub>J</sub>	- 40 to + 125	°C	
Storage temperature		T <sub>STG</sub>	- 55 to + 150	°C	

ABSOLUTE MAXIMUM RATINGS GSOT08C					
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT	
Peak pulse current	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, t <sub>p</sub> = 8/20 μs; single shot	1	18	А	
	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	ІРРМ	18	А	
Dook pulse power	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, t <sub>p</sub> = 8/20 μs; single shot	P <sub>PP</sub>	345	W	
Peak pulse power	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	ГРР	400	W	
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	$V_{ESD}$	± 30	kV	
ESD immunity	Air discharge acc. IEC 61000-4-2; 10 pulses	VESD	± 30	kV	
Operating temperature	Junction temperature	T <sub>J</sub>	- 40 to + 125	°C	
Storage temperature		T <sub>STG</sub>	- 55 to + 150	°C	

ABSOLUTE MAXIMUM RATINGS GSOT12C					
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT	
Dook pulse ourrent	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, t <sub>p</sub> = 8/20 μs; single shot	12	12	А	
Peak pulse current	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, t <sub>p</sub> = 8/20 μs; single shot	Іррм	12	А	
Pook pulso power	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, t <sub>p</sub> = 8/20 μs; single shot	$P_PP$	312	W	
Peak pulse power	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	ГРР	337	W	
ECD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses		± 30	kV	
ESD immunity	Air discharge acc. IEC 61000-4-2; 10 pulses	$V_{ESD}$	± 30	kV	
Operating temperature	Junction temperature	TJ	- 40 to + 125	°C	
Storage temperature		T <sub>STG</sub>	- 55 to + 150	°C	

## **GSOT03C to GSOT36C**

# Vishay Semiconductors Two-Line ESD-Protection in SOT-23



ABSOLUTE MAXIMUM RATINGS GSOT15C				
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT
Peak pulse current	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, t <sub>p</sub> = 8/20 μs; single shot	l	8	Α
i ear puise current	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	IPPM	Α	
Peak pulse power	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	$P_PP$	345	W
reak puise power	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	ГРР	400	W
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V <sub>ESD</sub>	± 30	kV
ESD illillidility	Air discharge acc. IEC 61000-4-2; 10 pulses	VESD	± 30	kV
Operating temperature	Junction temperature	TJ	- 40 to + 125	°C
Storage temperature		T <sub>STG</sub>	- 55 to + 150	°C

ABSOLUTE MAXIMUM RATINGS GSOT24C					
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT	
Dook pulse gurrent	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, t <sub>p</sub> = 8/20 μs; single shot	I	5	Α	
Peak pulse current	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	Іррм	5	Α	
Dook pulse power	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, t <sub>p</sub> = 8/20 μs; single shot	р	235	W	
Peak pulse power	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, t <sub>p</sub> = 8/20 μs; single shot	P <sub>PP</sub>	240	W	
ECD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V	± 30	kV	
ESD immunity	Air discharge acc. IEC 61000-4-2; 10 pulses	$V_{ESD}$	± 30	kV	
Operating temperature	Junction temperature	TJ	- 40 to + 125	°C	
Storage temperature		T <sub>STG</sub>	- 55 to + 150	°C	

ABSOLUTE MAXIMUM RATINGS GSOT36C					
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT	
Deal or leave and	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	l	3.5	А	
Peak pulse current	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	I <sub>PPM</sub>	3.5	А	
Peak pulse power	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	$P_PP$	248	W	
reak puise powei	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	ГРР	252	W	
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V	± 30	kV	
ESD immunity	Air discharge acc. IEC 61000-4-2; 10 pulses	$V_{ESD}$	± 30	kV	
Operating temperature	Junction temperature	TJ	- 40 to + 125	°C	
Storage temperature		T <sub>STG</sub>	- 55 to + 150	°C	

### **BIAs-MODE** (2-line bidirectional asymmetrical protection mode)

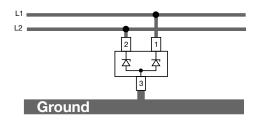
With the GSOTxx one signal- or data-lines (L1) can be protected against voltage transients. With pin 1 connected to ground and pin 3 connected to a signal- or data-line which has to be protected. As long as the voltage level on the data- or signal-line is between 0 V (ground level) and the specified maximum reverse working voltage (V<sub>RWM</sub>) the protection diode between pin 1 and pin 3 offer a high isolation to the ground line. The protection device behaves like an open switch.

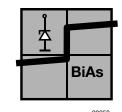
As soon as any positive transient voltage signal exceeds the break through voltage level of the protection diode, the diode becomes conductive and shorts the transient current to ground. Now the protection device behaves like a closed switch. The clamping voltage ( $V_C$ ) is defined by the breakthrough voltage ( $V_{BR}$ ) level plus the voltage drop at the series impedance (resistance and inductance) of the protection device.

Any negative transient signal will be clamped accordingly. The negative transient current is flowing in the forward direction of the protection diode. The low forward voltage  $(V_F)$  clamps the negative transient close to the ground level.

Due to the different clamping levels in forward and reverse direction the GSOTxx clamping behaviour is bidirectional and asymmetrical (BiAs).



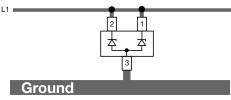




If a higher surge current or peak pulse current (I<sub>PP</sub>) is needed, both protection diodes in the GSOTxxC can also be used in parallel in order to "double" the performance.

### This offers:

- double surge power = double peak pulse current (2 x I<sub>PPM</sub>)
- half of the line inductance = reduced clamping voltage
- half of the line resistance = reduced clamping voltage
- double line capacitance (2 x C<sub>D</sub>)
- double reverse leakage current (2 x I<sub>B</sub>)



20359

ELECTRICAL CHARACTERISTICS GSOT03C						
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	=	-	2	lines
Reverse working voltage	at I <sub>R</sub> = 100 μA	$V_{RWM}$	3.3	-	-	V
Reverse current	at V <sub>R</sub> = 3.3 V	I <sub>R</sub>	-	-	100	μΑ
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	4	4.6	-	V
Reverse clamping voltage	at I <sub>PP</sub> = 1 A	V <sub>C</sub>	-	5.7	7.5	V
neverse clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 30 A	v <sub>C</sub>	=	10	12.3	V
Forward clamping voltage	at I <sub>PP</sub> = 1 A	V <sub>F</sub>	-	1	6 - 7 7.5 0 12.3 1.2	V
Forward clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 30 A	VF	- 2 M 3.3 100 - 100 - 100 - 100 - 100 - 100 - 100 - 100 - 100 - 12.3 - 1 1.2 - 4.5 420 - 600	V		
Capacitance	at V <sub>R</sub> = 0 V; f = 1 MHz	- C <sub>D</sub>	=	420	600	pF
Сараспапсе	at V <sub>R</sub> = 1.6 V; f = 1 MHz	OD	=	260	-	pF

#### Note

• Ratings at 25 °C, ambient temperature unless otherwise specified. BiAs mode (between pin 1 to 3 or pin 2 to 3)

ELECTRICAL CHARACTERISTICS GSOT04C							
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	2	lines	
Reverse working voltage	at I <sub>R</sub> = 20 μA	$V_{RWM}$	4	-	-	٧	
Reverse current	at V <sub>R</sub> = 4 V	I <sub>R</sub>	-	-	20	μA	
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	5	6.1	-	V	
Reverse clamping voltage	at I <sub>PP</sub> = 1 A	V <sub>C</sub>	-	7.5	9	V	
heverse ciamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 30 A	v <sub>C</sub>	-	11.2	- 2 20 6.1 - 7.5 9 1.2 14.3 1 1.2 4.5 - 310 450	V	
Forward clamping voltage	at I <sub>PP</sub> = 1 A	V <sub>F</sub>	-	1	1.2	V	
Forward clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 30 A	VF	-	- 2 20 6.1 - 7.5 9 11.2 14.3 1 1.2 4.5 -	V		
0	at V <sub>R</sub> = 0 V; f = 1 MHz		-	310	450	pF	
Capacitance	at V <sub>R</sub> = 2 V; f = 1 MHz	C <sub>D</sub>	-	200	-	pF	

#### Note

• Ratings at 25 °C, ambient temperature unless otherwise specified. BiAs mode (between pin 1 to 3 or pin 2 to 3)

# **GSOT03C to GSOT36C**

# Vishay Semiconductors Two-Line ESD-Protection in SOT-23



ELECTRICAL CHARACTERISTICS GSOT05C								
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	2	lines		
Reverse working voltage	at I <sub>R</sub> = 10 μA	$V_{RWM}$	5	-	-	V		
Reverse current	at V <sub>R</sub> = 5 V	I <sub>R</sub>	-	-	10	μA		
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	6	6.8	-	V		
Reverse clamping voltage	at I <sub>PP</sub> = 1 A	V <sub>C</sub>	-	7	8.7	V		
heverse clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 30 A	VC	-	12	16	V		
Forward clamping voltage	at I <sub>PP</sub> = 1 A	$V_{F}$	-	1	1.2	V		
Torward clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 30 A	VF	-	4.5	-	V		
Capacitance	at V <sub>R</sub> = 0 V; f = 1 MHz	- C <sub>D</sub>	-	260	350	pF		
Сараспансе	at V <sub>R</sub> = 2.5 V; f = 1 MHz	OD	ı	150	-	pF		

### Note

• Ratings at 25 °C, ambient temperature unless otherwise specified. BiAs mode (between pin 1 to 3 or pin 2 to 3)

ELECTRICAL CHARAC	TERISTICS GSOT08C					
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	2	lines
Reverse working voltage	at I <sub>R</sub> = 5 μA	$V_{RWM}$	8	-	-	V
Reverse current	at V <sub>R</sub> = 8 V	I <sub>R</sub>	-	-	5	μΑ
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	9	10	-	V
Reverse clamping voltage	at I <sub>PP</sub> = 1 A	V <sub>C</sub>	-	10.7	13	V
neverse clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 18 A	v <sub>C</sub>	-	15.2	19.2	V
Forward clamping voltage	at I <sub>PP</sub> = 1 A	V <sub>F</sub>	-	1	1.2	V
Forward clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 18 A	VF	-	3	-	V
Capacitance	at V <sub>R</sub> = 0 V; f = 1 MHz	C-	-	160	250	pF
Capacitarice	at V <sub>R</sub> = 4 V; f = 1 MHz	C <sub>D</sub>	-	80	-	pF

#### Note

• Ratings at 25 °C, ambient temperature unless otherwise specified. BiAs mode (between pin 1 to 3 or pin 2 to 3)

ELECTRICAL CHARAC	TERISTICS GSOT12C					
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	=	-	2	lines
Reverse working voltage	at I <sub>R</sub> = 1 μA	$V_{RWM}$	12	-	-	V
Reverse current	at V <sub>R</sub> = 12 V	I <sub>R</sub>	=	-	1	μA
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	13.5	15	-	V
Reverse clamping voltage	at I <sub>PP</sub> = 1 A	V <sub>C</sub>	=	15.4	18.7	V
neverse clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 12 A	v <sub>C</sub>	=	21.2	26	V
Forward clamping voltage	at I <sub>PP</sub> = 1 A	V <sub>F</sub>	=	1	1.2	V
Forward clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 12 A	v <sub>F</sub>	=	2.2	-	V
Capacitance	at V <sub>R</sub> = 0 V; f = 1 MHz	C-	-	115	150	pF
Capacitarice	at $V_R = 6 V$ ; $f = 1 MHz$	C <sub>D</sub>	1	50	-	pF

#### Note

• Ratings at 25 °C, ambient temperature unless otherwise specified. BiAs mode (between pin 1 to 3 or pin 2 to 3)



ELECTRICAL CHARACTERISTICS GSOT15C								
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	2	lines		
Reverse working voltage	at I <sub>R</sub> = 1 μA	$V_{RWM}$	15	-	-	V		
Reverse current	at V <sub>R</sub> = 15 V	I <sub>R</sub>	-	-	1	μΑ		
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	16.5	18	-	V		
Reverse clamping voltage	at I <sub>PP</sub> = 1 A	V <sub>C</sub>	-	19.4	23.5	V		
neverse clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 8 A	v <sub>C</sub>	-	24.8	28.8	V		
Forward clamping voltage	at I <sub>PP</sub> = 1 A	V <sub>F</sub>	-	1	1.2	V		
Forward clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 8 A	VF	-	1.8	-	V		
Capacitance	at V <sub>R</sub> = 0 V; f = 1 MHz	0	-	90	120	pF		
Сараспапсе	at V <sub>R</sub> = 7.5 V; f = 1 MHz	C <sub>D</sub>	-	35	-	pF		

#### Note

• Ratings at 25 °C, ambient temperature unless otherwise specified. BiAs mode (between pin 1 to 3 or pin 2 to 3)

PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	2	lines
Reverse working voltage	at I <sub>R</sub> = 1 μA	$V_{RWM}$	24	-	-	V
Reverse current	at V <sub>R</sub> = 24 V	I <sub>R</sub>	-	-	1	μΑ
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	27	30	-	V
Poverse elemping voltage	at I <sub>PP</sub> = 1 A	V	-	34	41	V
Reverse clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 5 A	V <sub>C</sub>	-	41	47	V
Forward clamping voltage	at I <sub>PP</sub> = 1 A	W	-	1	1.2	V
Forward clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 5 A	V <sub>F</sub>	-	1.4	-	V
Capacitance	at V <sub>R</sub> = 0 V; f = 1 MHz	-	-	65	80	pF
	at V <sub>R</sub> = 12 V; f = 1 MHz	- C <sub>D</sub>	-	20	-	pF

### Note

• Ratings at 25 °C, ambient temperature unless otherwise specified. BiAs mode (between pin 1 to 3 or pin 2 to 3)

ELECTRICAL CHARAC	TERISTICS GSOT36C					
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	=	-	2	lines
Reverse working voltage	at I <sub>R</sub> = 1 μA	$V_{RWM}$	36	-	-	V
Reverse current	at V <sub>R</sub> = 36 V	I <sub>R</sub>	=	-	1	μA
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	39	43	-	V
Reverse clamping voltage	at I <sub>PP</sub> = 1 A	V <sub>C</sub>	=	49	60	V
neverse clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 3.5 A	v <sub>C</sub>	=	59	71	V
Forward alamping valtage	at I <sub>PP</sub> = 1 A	W	=	1	1.2	V
Forward clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 3.5 A	V <sub>F</sub>	=	1.3	-	V
0	at V <sub>R</sub> = 0 V; f = 1 MHz	_	-	52	65	pF
Capacitance	at V <sub>R</sub> = 18 V; f = 1 MHz	C <sub>D</sub>	-	12	-	pF

#### Note

• Ratings at 25 °C, ambient temperature unless otherwise specified. BiAs mode (between pin 1 to 3 or pin 2 to 3)

### **BISy-MODE** (1-line bidirectional symmetrical protection mode)

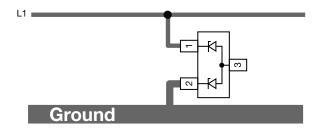
If a bipolar symmetrical protection device is needed the GSOTxxC can also be used as a single line protection device. Therefore pin 1 has to be connected to the signal- or data-line (L1) and pin 2 to ground (or vice versa). Pin 3 must not be connected. Positive and negative voltage transients will be clamped in the same way. The clamping current through the GSOTxxC passes one diode in forward direction and the other one in reverse direction. The clamping voltage (V<sub>C</sub>) is defined by the breakthrough voltage (V<sub>BR</sub>) level of one diode plus the forward voltage of the other diode plus the voltage drop at the series impedances (resistances and inductances) of the protection device.

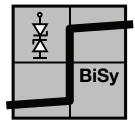
Due to the same clamping levels in positive and negative direction the GSOTxxC voltage clamping behaviour is bidirectional and symmetrical (BiSy).

Document Number: 85824 Rev. 2.0, 22-Jul-10

# Vishay Semiconductors Two-Line ESD-Protection in SOT-23







20361

ELECTRICAL CHARAC	TERISTICS GSOT03C					
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	=	-	1	lines
Reverse working voltage	at I <sub>R</sub> = 100 μA	$V_{RWM}$	3.8	-	-	V
Reverse current	at V <sub>R</sub> = 3.8 V	I <sub>R</sub>	=	-	100	μΑ
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	4.5	5.3	-	V
Doverse elemning veltage	at I <sub>PP</sub> = 1 A	W	=	7	8.4	V
Reverse clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 30 A	V <sub>C</sub>	=	14	16.8	V
Canacitanas	at V <sub>R</sub> = 0 V; f = 1 MHz	- C <sub>D</sub>	=	210	300	pF
Capacitance	at V <sub>R</sub> = 1.6 V; f = 1 MHz		=	190	-	pF

#### Note

• Ratings at 25 °C, ambient temperature unless otherwise specified. BiAs mode (between pin 1 to 2 or pin 2 to 1; pin 3 not connected)

ELECTRICAL CHARAC	TERISTICS GSOT04C					
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	=	-	1	lines
Reverse working voltage	at I <sub>R</sub> = 20 μA	$V_{RWM}$	4.5	-	-	V
Reverse current	at V <sub>R</sub> = 4:5 V	I <sub>R</sub>	=	-	20	μA
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	5.5	6.8	-	V
Payaraa alamping valtaga	at I <sub>PP</sub> = 1 A	V	=	7.5	9	V
Reverse clamping voltage at	at I <sub>PP</sub> = I <sub>PPM</sub> = 30 A	- V <sub>C</sub>	=	15.7	18.8	V
0	at V <sub>R</sub> = 0 V; f = 1 MHz	- C <sub>D</sub>	=	155	225	pF
Capacitance	at V <sub>R</sub> = 2 V; f = 1 MHz		-	135	-	pF

### Note

• Ratings at 25 °C, ambient temperature unless otherwise specified. BiAs mode (between pin 1 to 2 or pin 2 to 1; pin 3 not connected)

ELECTRICAL CHARAC	TERISTICS GSOT05C					
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	1	lines
Reverse working voltage	at I <sub>R</sub> = 10 μA	$V_{RWM}$	5.5	-	-	V
Reverse current	at V <sub>R</sub> = 5.5 V	I <sub>R</sub>	-	-	10	μΑ
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	6.5	7.5	-	V
Reverse clamping voltage	at I <sub>PP</sub> = 1 A	W	-	8.1	9.7	V
neverse clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 18 A	V <sub>C</sub>	-	17	20.4	V
Capacitance	at V <sub>R</sub> = 0 V; f = 1 MHz	C-	-	130	175	pF
Сараснансе	at V <sub>R</sub> = 4 V; f = 1 MHz	C <sub>D</sub>	ı	100	-	pF

### Note

• Ratings at 25 °C, ambient temperature unless otherwise specified. BiAs mode (between pin 1 to 2 or pin 2 to 1; pin 3 not connected)



ELECTRICAL CHARACTERISTICS GSOT08C								
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	1	lines		
Reverse working voltage	at I <sub>R</sub> = 5 μA	$V_{RWM}$	8.5	-	-	V		
Reverse current	at V <sub>R</sub> = 8.5 V	I <sub>R</sub>	-	-	5	μA		
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	9.5	10.7	-	V		
Reverse clamping voltage	at I <sub>PP</sub> = 1 A	W	-	11.7	14	V		
neverse clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 18 A	- V <sub>C</sub>	-	18.5	22.2	V		
Capacitance	at V <sub>R</sub> = 0 V; f = 1 MHz	C-	-	80	125	pF		
Сараспансе	at $V_R = 4 V$ ; $f = 1 MHz$	C <sub>D</sub>	-	60	-	pF		

#### Note

• Ratings at 25 °C, ambient temperature unless otherwise specified. BiAs mode (between pin 1 to 2 or pin 2 to 1; pin 3 not connected)

ELECTRICAL CHARACTERISTICS GSOT12C								
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	1	lines		
Reverse working voltage	at I <sub>R</sub> = 1 μA	$V_{RWM}$	12.5	-	-	V		
Reverse current	at V <sub>R</sub> = 12.5 V	I <sub>R</sub>	-	-	1	μA		
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	13.5	15.7	-	V		
Reverse clamping voltage	at I <sub>PP</sub> = 1 A	V	-	16.4	19.7	V		
neverse clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 12 A	V <sub>C</sub>	-	23.4	28.1	V		
Capacitance	at V <sub>R</sub> = 0 V; f = 1 MHz	C-	-	58	75	pF		
Capacitance	at V <sub>R</sub> = 7.5 V; f = 1 MHz	C <sub>D</sub>	-	36	_	pF		

#### Note

• Ratings at 25 °C, ambient temperature unless otherwise specified. BiAs mode (between pin 1 to 2 or pin 2 to 1; pin 3 not connected)

ELECTRICAL CHARACTERISTICS GSOT15C								
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	=	-	1	lines		
Reverse working voltage	at I <sub>R</sub> = 1 μA	$V_{RWM}$	15.5	-	-	V		
Reverse current	at V <sub>R</sub> = 15.5 V	I <sub>R</sub>	-	-	1	μΑ		
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	17	18.7	-	V		
Deverse elemning valtage	at I <sub>PP</sub> = 1 A	W	-	20.4	24.5	V		
Reverse clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 8 A	V <sub>C</sub>	-	26.6	30.6	V		
Canacitanas	at V <sub>R</sub> = 0 V; f = 1 MHz	C-	=	45	60	pF		
Capacitance	at V <sub>R</sub> = 7.5 V; f = 1 MHz	$C_D$	ı	25	-	pF		

### Note

• Ratings at 25 °C, ambient temperature unless otherwise specified. BiAs mode (between pin 1 to 2 or pin 2 to 1; pin 3 not connected)

ELECTRICAL CHARACTERISTICS GSOT24C									
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT			
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	=.	1	lines			
Reverse working voltage	at I <sub>R</sub> = 1 μA	$V_{RWM}$	24.5	-	-	V			
Reverse current	at V <sub>R</sub> = 24.5 V	I <sub>R</sub>	-	-	1	μΑ			
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	27.5	30.7	-	V			
Reverse clamping voltage	at I <sub>PP</sub> = 1 A	- V <sub>C</sub>	-	34	41	V			
	at I <sub>PP</sub> = I <sub>PPM</sub> = 5 A		-	40	48	V			
Capacitance	at V <sub>R</sub> = 0 V; f = 1 MHz	- C <sub>D</sub>	-	33	40	pF			
	at V <sub>R</sub> = 12 V; f = 1 MHz		-	18	-	pF			

### Note

• Ratings at 25 °C, ambient temperature unless otherwise specified. BiAs mode (between pin 1 to 2 or pin 2 to 1; pin 3 not connected)

# Vishay Semiconductors Two-Line ESD-Protection in SOT-23

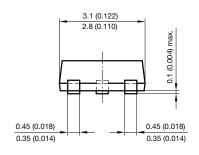


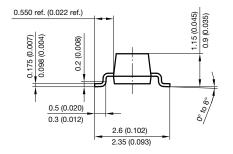
ELECTRICAL CHARACTERISTICS GSOT36C									
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT			
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	1	lines			
Reverse working voltage	at I <sub>R</sub> = 1 μA	$V_{RWM}$	36.5	-	-	V			
Reverse current	at V <sub>R</sub> = 36.5 V	I <sub>R</sub>	-	-	1	μΑ			
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	39.5	43.7	-	V			
Reverse clamping voltage	at I <sub>PP</sub> = 1 A	V <sub>C</sub>	-	50	60	V			
	at I <sub>PP</sub> = I <sub>PPM</sub> = 3.5 A		-	60	72	V			
Capacitance	at $V_R = 0 V$ ; $f = 1 MHz$	C <sub>D</sub>	-	26	33	pF			
	at V <sub>R</sub> = 18 V; f = 1 MHz		-	10	-	pF			

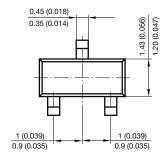
### Note

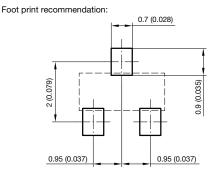
• Ratings at 25 °C, ambient temperature unless otherwise specified. BiAs mode (between pin 1 to 2 or pin 2 to 1; pin 3 not connected)

## PACKAGE DIMENSIONS in millimeters (inches): SOT-23









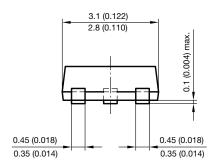
Document no.: 6.541-5014.01-4 Rev. 8 - Date: 23.Sept.2009

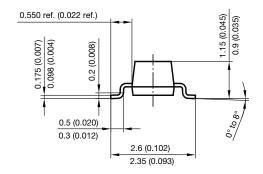
1741

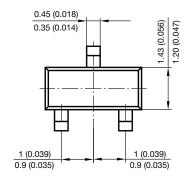
## SOT-23



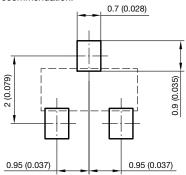
## **PACKAGE DIMENSIONS** in millimeters (inches)







Foot print recommendation:



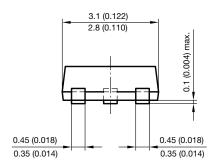
Document no.: 6.541-5014.01-4 Rev. 8 - Date: 23.Sept.2009

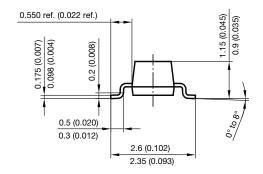
17418

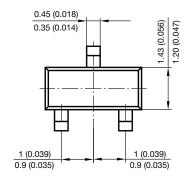
## SOT-23



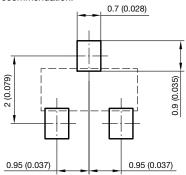
## **PACKAGE DIMENSIONS** in millimeters (inches)







Foot print recommendation:



Document no.: 6.541-5014.01-4 Rev. 8 - Date: 23.Sept.2009

17418



# **Legal Disclaimer Notice**

Vishay

## **Disclaimer**

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and/or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk and agree to fully indemnify and hold Vishay and its distributors harmless from and against any and all claims, liabilities, expenses and damages arising or resulting in connection with such use or sale, including attorneys fees, even if such claim alleges that Vishay or its distributor was negligent regarding the design or manufacture of the part. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.

# **Material Category Policy**

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as RoHS-Compliant fulfill the definitions and restrictions defined under Directive 2011/65/EU of The European Parliament and of the Council of June 8, 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment (EEE) - recast, unless otherwise specified as non-compliant.

Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.