ATWINC3400A/ATWINC3400-MR210xA Errata
ATWINC3400A/ATWINC3400-MR210xA

www.microchip.com Product Pages: ATWINC3400

Overview

The ATWINC3400A family of devices that you received conform functionally to the current device data sheets available at Microchip websites www.microchip.com/wwwproducts/en/ATWINC3400 and www.microchip.com/DS70005390, except for the anomalies described in this document.

The silicon issues discussed in the following pages are for silicon revisions with the Chip ID listed in the following table.

Table 1. ATWINC3400A Family Device Identification

Part Number	Chip ID	Silicon Revision	Affected Revision
ATWINC3400A/ATWINC3400-MR210xA	3400D2	D2	D2

Note: The errata details described in this document will be addressed in future revisions of the ATWINC3400A family of devices.

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1.   Errata Details

The following issue applies to the ATWINC3400A family of devices.

1.1   EVM Performance Degradation

In Wi-Fi® transmission, EVM performance is degraded at higher data rates at Channel 10 (2457 MHz) only, leading to a reduced data rate transmission but still meeting the IEEE 802.11 specification limit.

Work around: None

1.2   Incorrect MAC Address Serialization in WINC3400 Modules Causes Connectivity Issues

Affected Date Code Range

2311 – 2412

For more details, refer to the Microchip Knowledge Base Article, *Connectivity Issues in WINC3400 Modules*.

2.   Document Revision History

Revision	Date	Section	Description
B	12/2024	Incorrect MAC Address Serialization in WINC3400 Modules Causes Connectivity Issues	Added an erratum
A	04/2020	Document	Initial Revision

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