

MCP2200 Rev. A2 Errata

The MCP2200 device that you have received conforms functionally to the current MCP2200 Data Sheet (DS22228), except for the anomalies described in this document.

Silicon Errata Issues

Note: This document summarizes all silicon errata issues from all revisions of silicon, previous as well as current.

1. Module: MCP2200 Device USB Compliance

Revision A0 was not registered with the USB IF as a compliant device. Revision A1 is registered as a USB compliant device.

Work around

Use revision A1. Revision A0 parts were primarily samples. All devices manufactured after the second Work Week of 2010 are revision A1 and USB compliant.

To determine the year and working week correctly, look for the first four digits on the package markings, which are Year (YY) and Work Week (WW) codes. Anything after 1001NNN is USB compliant.

2. Module: MCP2200 Device UART

Revision A1 and earlier had a reduced sustained throughput at higher baud rates on the UART side of the bridge. When not using hardware flow control on the UART, data could be lost if attempting high data throughput when the UART baud rate is high.

Work around

Use revision A2. The revision number can be identified using [Table 1](#).

If using rev A1 or earlier: Enable the RTS and CTS pins to enable hardware handshaking. The throughput will be reduced, however, no data will be lost.

TABLE 1: REVISION NUMBER IDENTIFICATION

Silicon Revision	YYWWNNN		Condition
	Start Date	End Date	
Rev A0	—	1001NNN	
Rev A1	1002NNN	1051NNN	
Rev A2	1052NNN	—	In Production

Legend: "N" is any alphanumeric character.

Data Sheet Clarifications

None.

MCP2200

APPENDIX A: DOCUMENT REVISION HISTORY

Rev A Document (5/2010)

Initial release of this document.

Rev B Document (2/2011)

Added errata issue regarding the UART and updated
Table 1 with the Rev A2 entry.

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