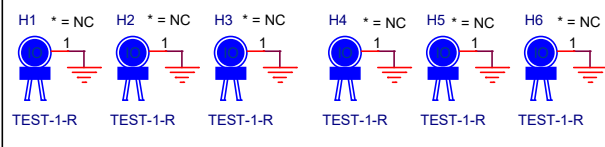


Date	Description
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OPL1000 External

20191206	(1)Add : R176:1M ohm , R175&R178: 0 ohm , C81:1uF (2)Add Q1:MOSFET and D1:DIODE function
20191213	Add R127:47k ohm
20200116	(1)Add brown out schematic: Q2: P-MOSFET, R1:680K ohm, R2:3.9K ohm, R3:10M ohm, C36:0.1 uF (2)EN: 0 ohm (3)UART note (4)Pin and line definition (5)Modify E-PAD GND
20200513	(1)Modify R126:510K , C36:4.7uF

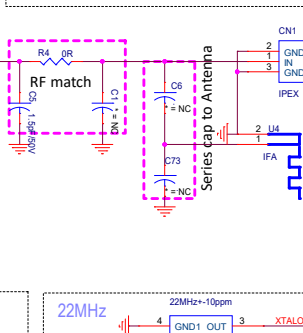
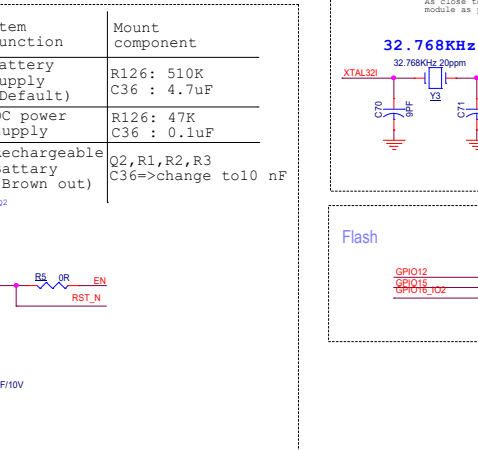


Title		
History		
Size A	Document Number <Doc>	Rev V1.1
Date:	Tuesday, May 26, 2020	Sheet 1 of 2

Pin 10: VDD_IO (IO supply voltage)

Connections:

- GPIO4/ADC
- GPIO5/ADC
- GPIO23/PHM
- GPIO7/ADC
- GPIO8/DART1_TX
- GPIO9/DART1_RX
- GPIO10/SCL
- GPIO11/SDA
- GPIO12



DC to DC

EX_DCDC

C81
1uF 10V

R171 68K

R170 75K

U6

EX_DCDC

FB NC EN
OUT VIN
EPAD

RT9053AZW

1 2 3

10K

I/OSENSE

R172 0R

R176 1M

R175 0R

* = NC

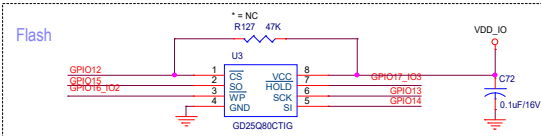
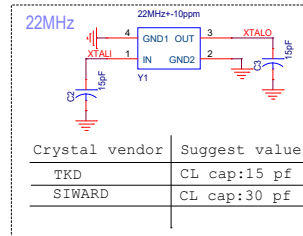

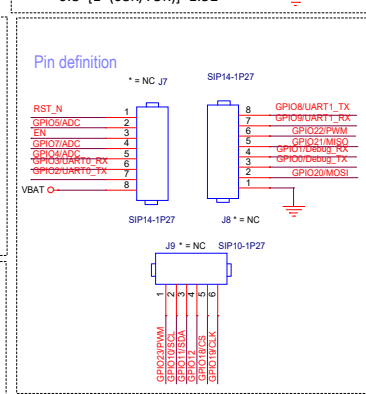
VBA

C80
4.7uF 10V

0.8 * [1 + (58K/75K)] = 1.52

co-ly R172 and R175

SMPS_IND1



Pin definition

Pin definition diagram for the SIP14-1P27 connector. The diagram shows two views of the connector: a top view and a side view.

Top View (SIP14-1P27):

- Pin 1: RST_N
- Pin 2: GPIO2/ADC
- Pin 3: EN
- Pin 4: GPIO1/ADC
- Pin 5: GPIO2/AD
- Pin 6: GPIO2/UARTS_TX
- Pin 7: VBAT
- Pin 8: (unlabeled)

Side View (SIP14-1P27):

- Pin 8: GPIO5/UART1_TX
- Pin 7: GPIO5/UART1_RX
- Pin 6: GPIO23/PMU
- Pin 5: GPIO21/MISO
- Pin 4: GPIO1/Debug_RX
- Pin 3: GPIO25/MOSI
- Pin 2: (unlabeled)
- Pin 1: (unlabeled)

A ground symbol is connected to Pin 2.

Additional labels: J9 = NC, J8 = NC.