

<10> CAN_RD1
<10> CAN_TD1
<8> U0_TXD
<8> U0_RXD
<6,10> P0_4
<6,10> P0_5
<6> VD8
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<6> VD16
<6> VD17
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<8> U2_RXD
<6> P0_12
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<10> USB_CONNECT
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<8> U1_RXD
<8> U1_CTS
<8> U1_DCD
<8> U1_DSR
<8> U1_DTR
<8> U1_RI
<8> U1_RTS
<6> P0_23
<6> P0_24
<6> P0_25
<10> DAC_OUT
<11> I2C0_SDA
<11> I2C0_SCL
<10> USB_H_D1+
<10> USB_H_D1-
<10> USB_D_D2+
<10> USB_D_D2+

U1A
LPC1788

94 P0[0]/CAN_RD1/U3_TXD/I2C1_SDA/U0_TXD
96 P0[1]/CAN_TD1/U3_RXD/I2C1_SCL/U0_RXD
202 P0[2]/U0_TXD/U3_TXD
204 P0[3]/U0_RXD/U3_RXD
168 P0[4]/I2S_RX_SCK/CAN_RD2/T2_CAP0/LCD_VD[0]
166 P0[5]/I2S_RX_WS/CAN_TD2/T2_CAP1/LCD_VD[1]
164 P0[6]/I2S_RX_SDA/SSP1_SSEL/T2_MAT0/U1_RTS/LCD_VD[8]
162 P0[7]/I2S_TX_SCK/SSP1_SCK/T2_MAT1/LCD_VD[9]/RTC_EV0
160 P0[8]/I2S_TX_WS/SSP1_MISO/T2_MAT2/LCD_VD[16]/RTC_EV1
158 P0[9]/I2S_TX_SDA/SSP1_MOSI/T2_MAT3/LCD_VD[17]/RTC_EV2
98 P0[10]/U2_TXD/I2C2_SDA/T3_MAT0
100 P0[11]/U2_RXD/I2C2_SCL/T3_MAT1
41 P0[12]/#USB_PPWR2/SSP1_MISO/AD0[6]
45 P0[13]/USB_UP_LED2/SSP1_MOSI/AD0[7]
69 P0[14]/#USB_HSTEN2/SSP1_SSEL/USB_CONNECT2
128 P0[15]/U1_TXD/SSP0_SCK

130 P0[16]/U1_RXD/SSP0_SSEL
126 P0[17]/U1_CTS/SSP0_MISO
124 P0[18]/U1_DCD/SSP0_MOSI
122 P0[19]/U1_DSR/SD_CLK/I2C1_SDA
120 P0[20]/U1_DTR/SD_CMD/I2C1_SCL
118 P0[21]/U1_RI/SD_PPWR/U4_OE/CAN_RD1
116 P0[22]/U1_RTS/SD_DAT[0]/U4_TXD/CAN_TD1
18 P0[23]/AD0[0]/I2S_RX_SCK/T3_CAP0
16 P0[24]/AD0[1]/I2S_RX_WS/T3_CAP1
14 P0[25]/AD0[2]/I2S_RX_SDA/U3_TXD
50 P0[26]/AD0[3]/DAC_OUT/U3_RXD
48 P0[27]/I2C0_SDA/USB_SDA
61 P0[28]/I2C0_SCL/USB_SCL
62 P0[29]/USB_D+1/EINT0
51 P0[30]/USB_D-1/EINT1
52 P0[31]/USB_D+2
USB_D-2

P1[0]/ENET_TXD0/T3_CAP1/SSP2_SCK
P1[1]/ENET_TXD1/T3_MAT3/SSP2_MOSI
P1[2]/ENET_TXD2/SD_CLK/PWM0[1]
P1[3]/ENET_TXD3/SD_CMD/PWM0[2]
P1[4]/#ENET_TX_EN/T3_MAT2/SSP2_MISO
P1[5]/ENET_TX_ER/SD_PPWR/PWM0[3]
P1[6]/ENET_TX_CLK/SD_DAT[0]/PWM0[4]
P1[7]/ENET_COL/SD_DAT[1]/PWM0[5]
P1[8]/ENET_CRCS/T3_MAT1/SSP2_SSEL
P1[9]/ENET_RXD0/T3_MAT0
P1[10]/ENET_RXD1/T3_CAP0
P1[11]/ENET_RXD2/SD_DAT[2]/PWM0[6]
P1[12]/ENET_RXD3/SD_DAT[3]/PWM0_CAP0
P1[13]/ENET_RX_DV
P1[14]/ENET_RX_ER/T2_CAP0
P1[15]/ENET_RX_CLK/I2C2_SDA

P1[16]/ENET_MDC/I2S_TX_MCLK
P1[17]/ENET_MDIO/I2S_RX_MCLK
P1[18]/USB_UP_LED1/PWM1[1]/T1_CAP0/SSP1_MISO
P1[19]/#USB_TX_E1/#USB_PPWR1/T1_CAP1/MC_0A/SSP1_SCK/U2_OE
P1[20]/USB_TX_DP1/PWM1[2]/QE1_PHA/MC_FB0/SSP0_SCK/LCD_VD[6]/LCD_VD[10]
P1[21]/USB_TX_DM1/PWM1[3]/SSP0_SSEL/#MCABORT/LCD_VD[7]/LCD_VD[11]
P1[22]/USB_RCV1/USB_PPWRD1/T1_MAT0/MC_0B/SSP1_MOSI/LCD_VD[8]/LCD_VD[12]
P1[23]/USB_RX_DP1/PWM1[4]/QE1_PHB/MC_FB1/SSP0_MISO/LCD_VD[9]/LCD_VD[13]
P1[24]/USB_RX_DM1/PWM1[5]/QE1_IDX/MC_FB2/SSP0_MOSI/LCD_VD[10]/LCD_VD[14]
P1[25]/#USB_LS1/#USB_HSTEN1/T1_MAT1/MC_1A/CLKOUT/LCD_VD[11]/LCD_VD[15]
P1[26]/#USB_SSPND1/PWM1[6]/T0_CAP0/MC_1B/SSP1_SSEL/LCD_VD[12]/LCD_VD[20]
P1[27]/#USB_INT1/#USB_OVRCCR1/T0_CAP1/CLKOUT/LCD_VD[13]/LCD_VD[21]
P1[28]/USB_SCL1/PWM1_CAP0/T0_MAT0/MC_2A/SSP0_SSEL/LCD_VD[14]/LCD_VD[22]
P1[29]/USB_SDA1/PWM1_CAP1/T0_MAT1/MC_2B/U4_TXD/LCD_VD[15]/LCD_VD[23]
P1[30]/USB_PPWRD2/USB_VBUS/AD0[4]/I2C0_SDA/U3_OE
P1[31]/#USB_OVRCCR2/SSP1_SCK/AD0[5]/I2C0_SCL

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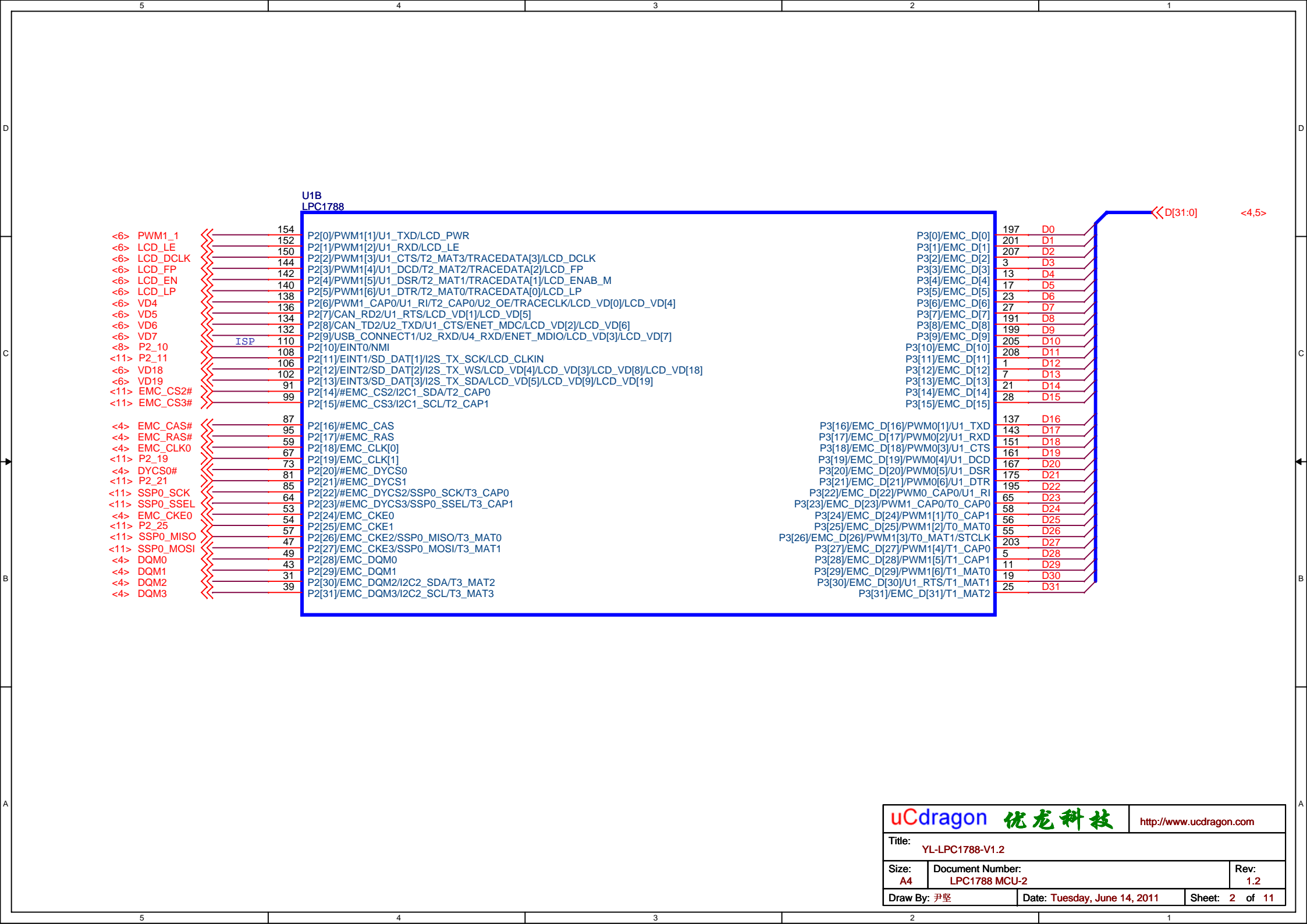
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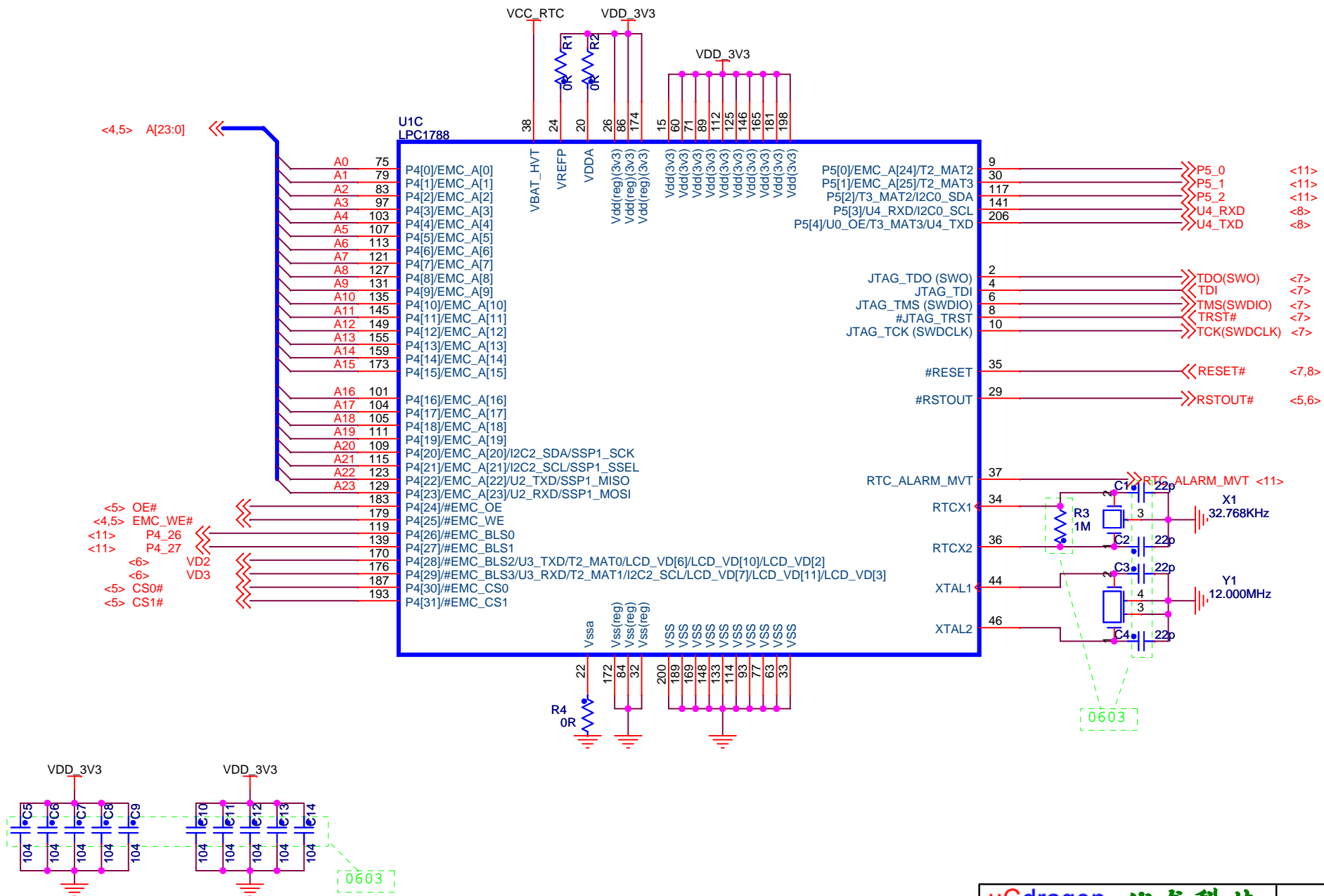
Rev: 1.2

Draw By: 尹坚

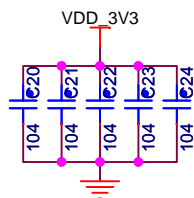
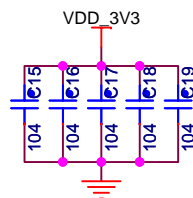
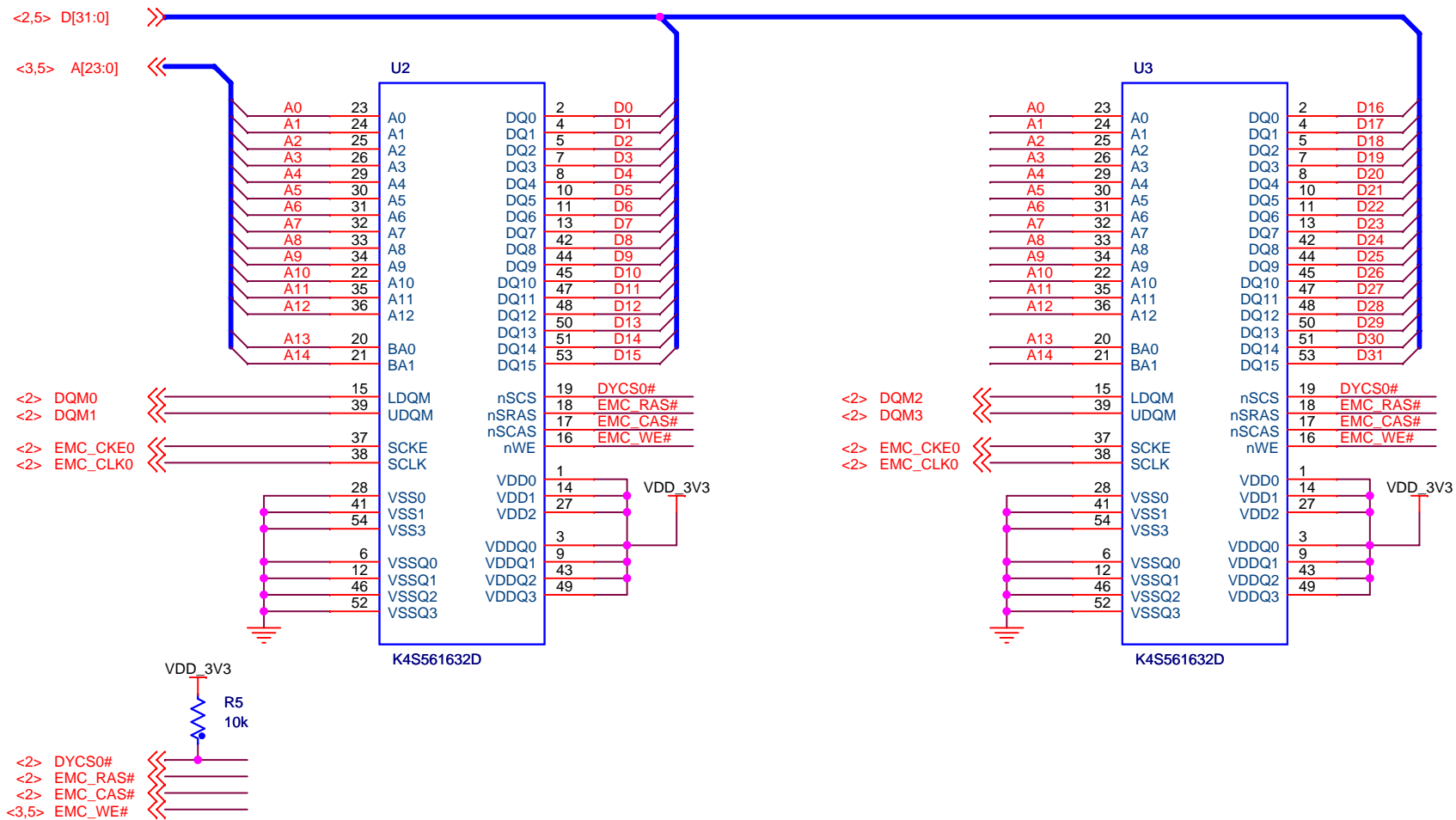
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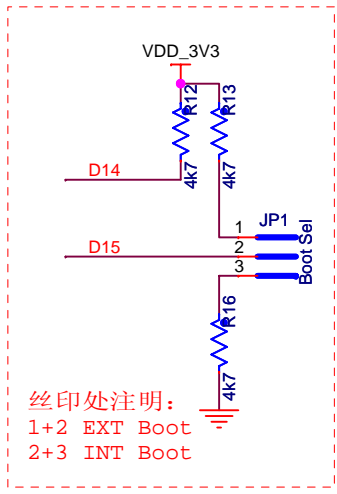
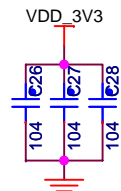
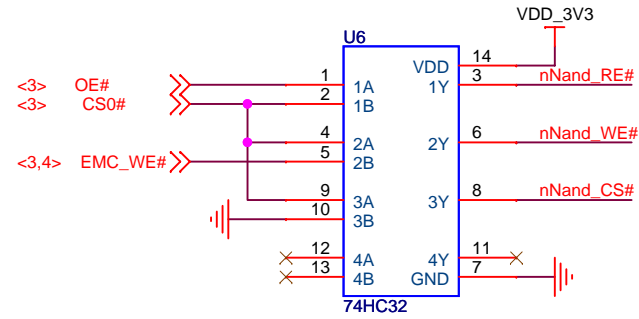
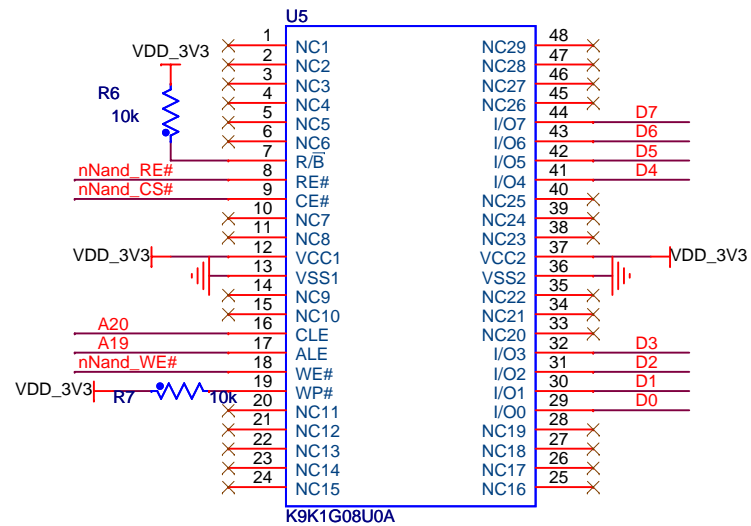
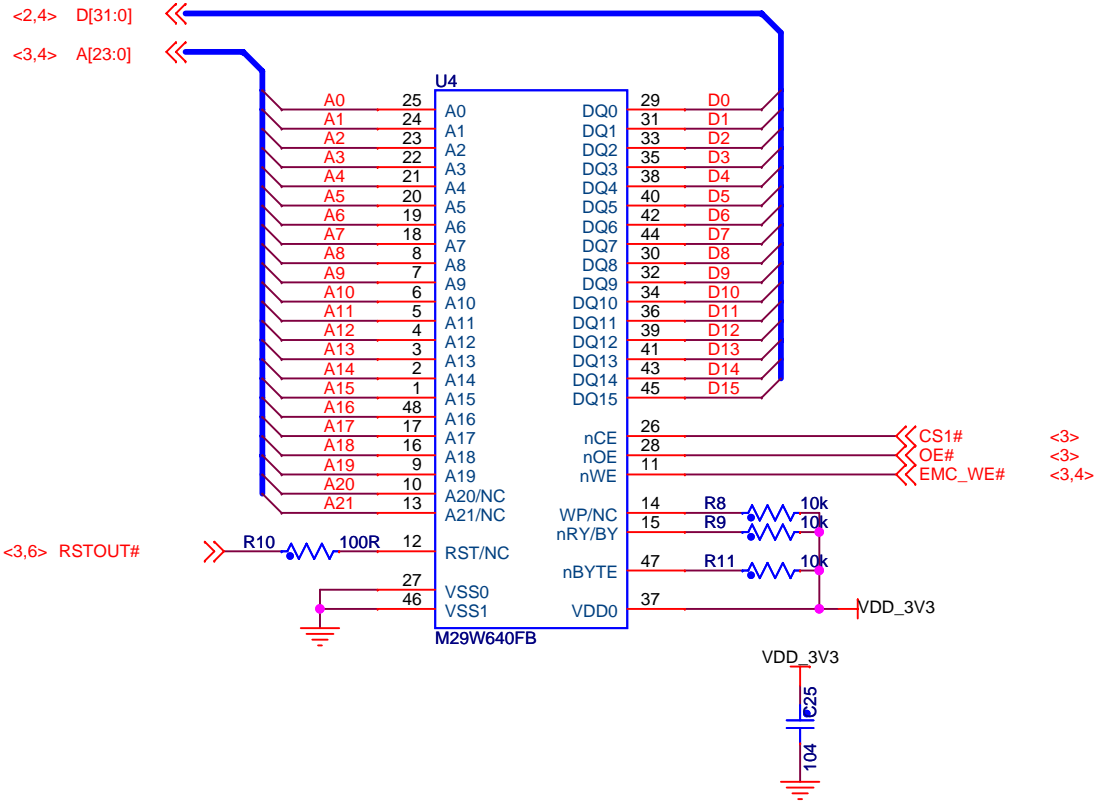
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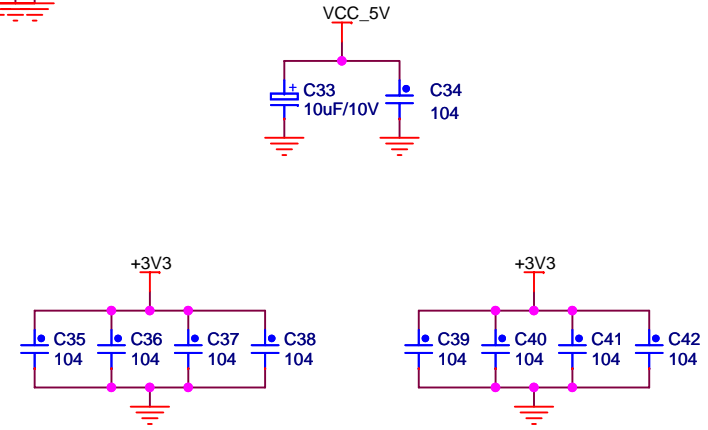
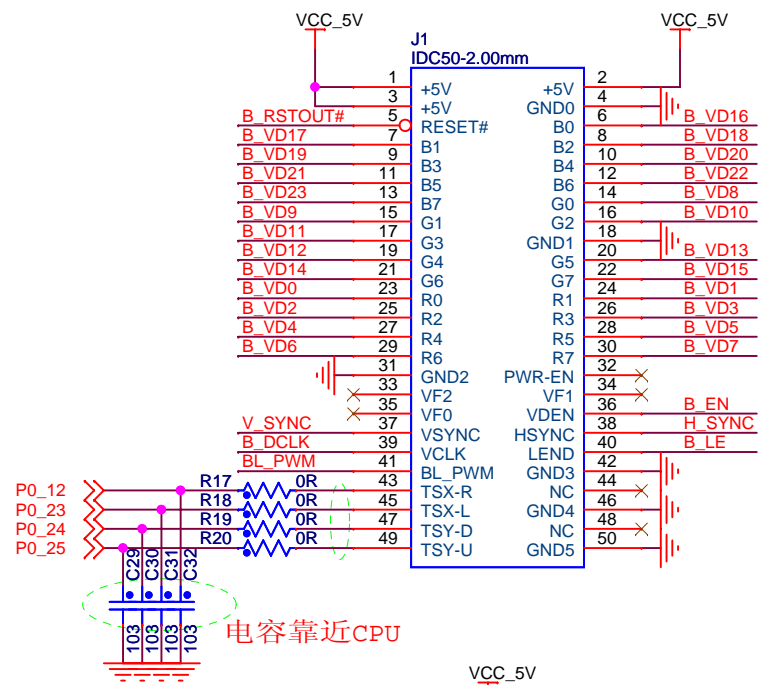
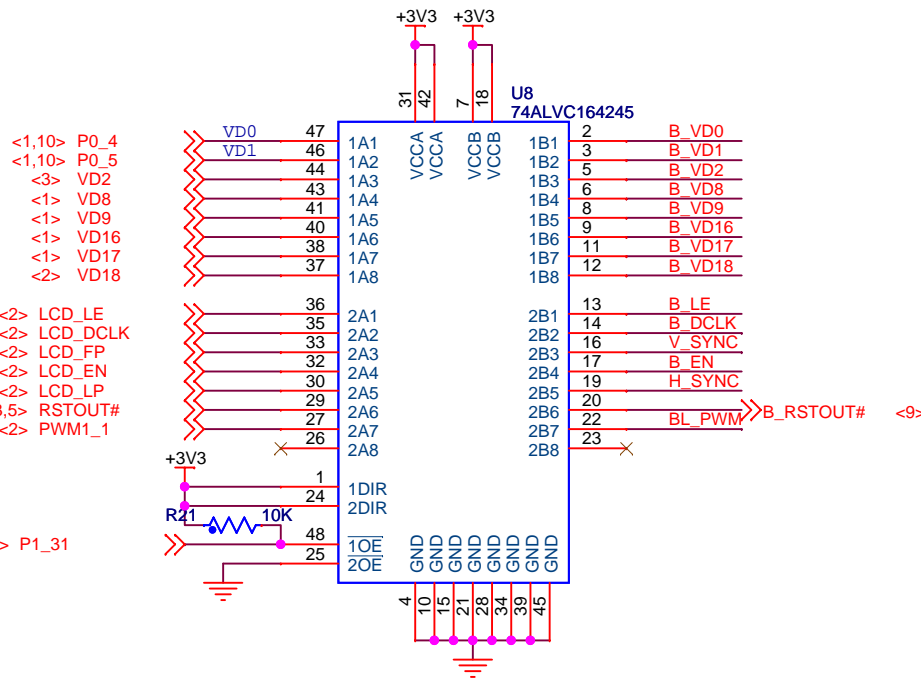
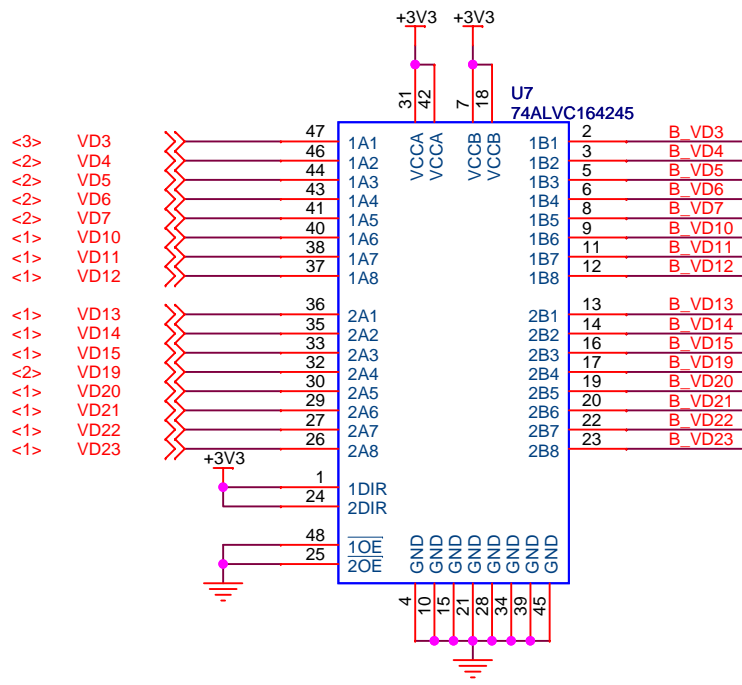
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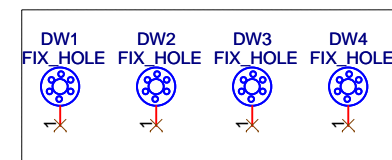
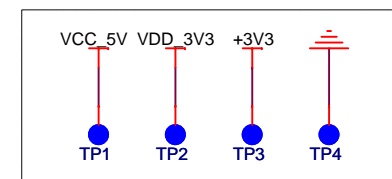
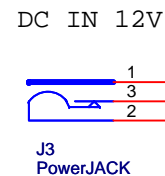




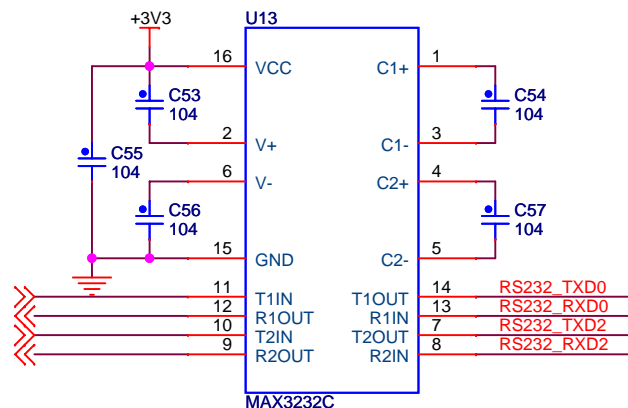
丝印处注明：
1+2 EXT Boot
2+3 INT Boot

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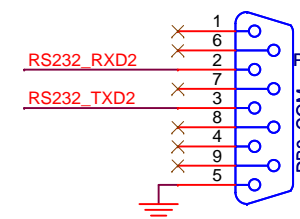
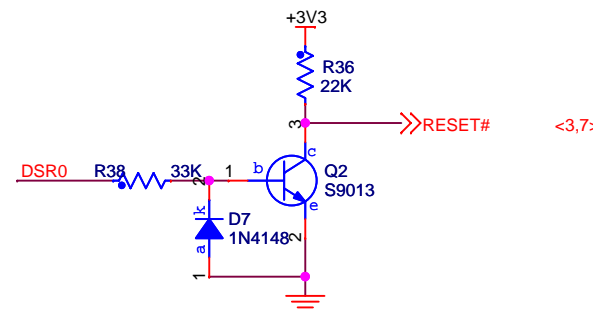
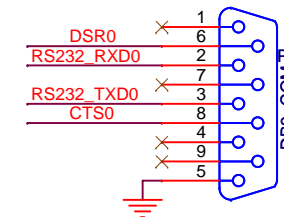
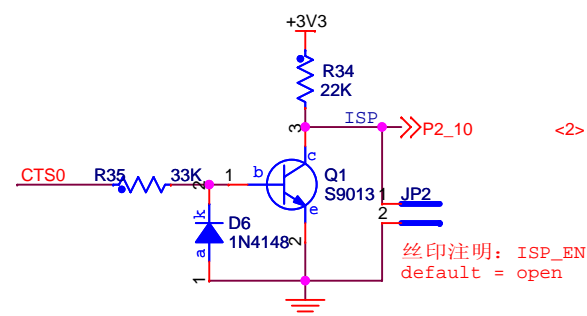
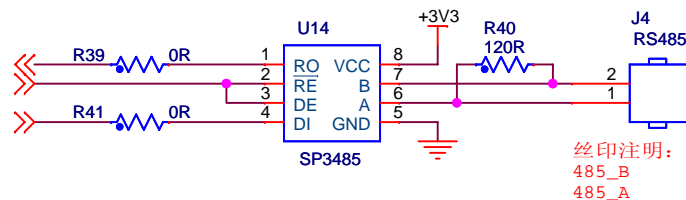




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<3> U4_RXD

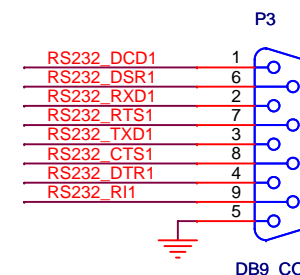
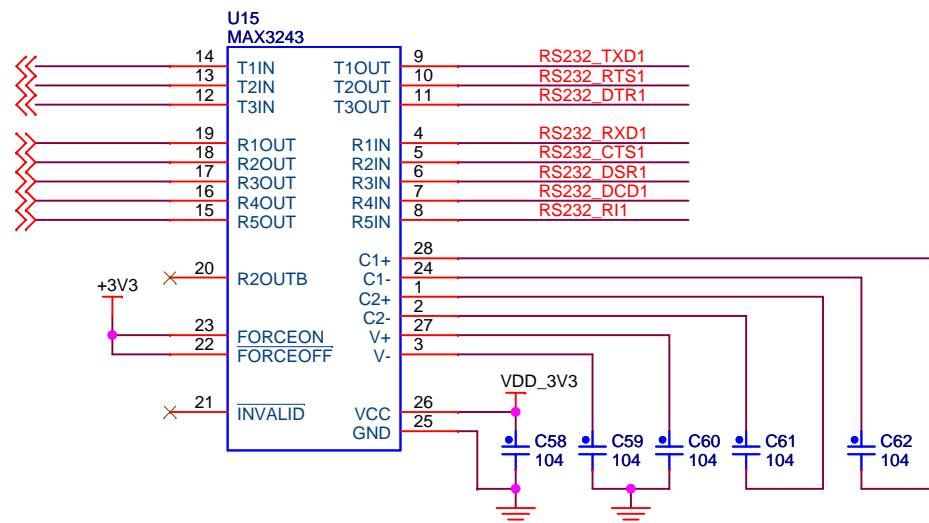


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<1> U2_OE
<1> U2_TXD



<1> U1_TXD
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<1> U1_DTR

<1> U1_RXD
<1> U1_CTS
<1> U1_DSR
<1> U1_DCD
<1> U1_RI



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Title: YL-LPC1788-V1.2			
Size: A4	Document Number: RS232,RS485		Rev: 1.2
Draw By: 尹坚		Date: Tuesday, June 14, 2011	Sheet: 8 of 11

