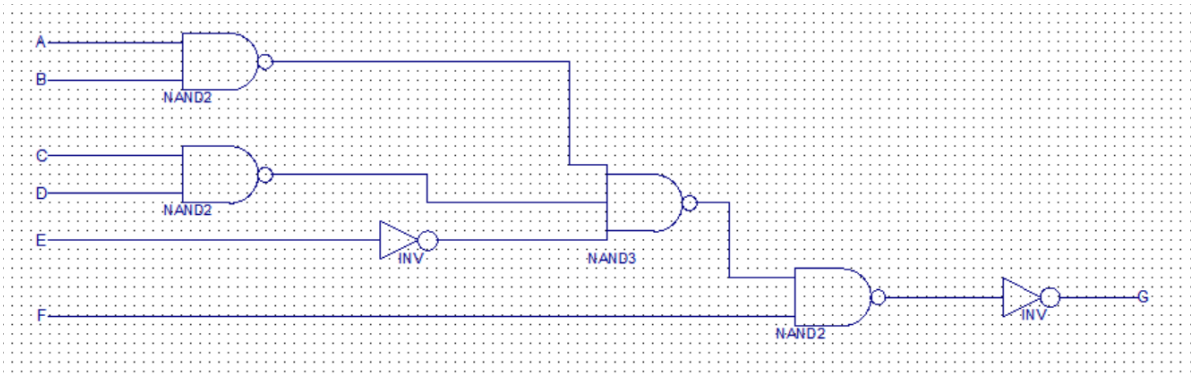
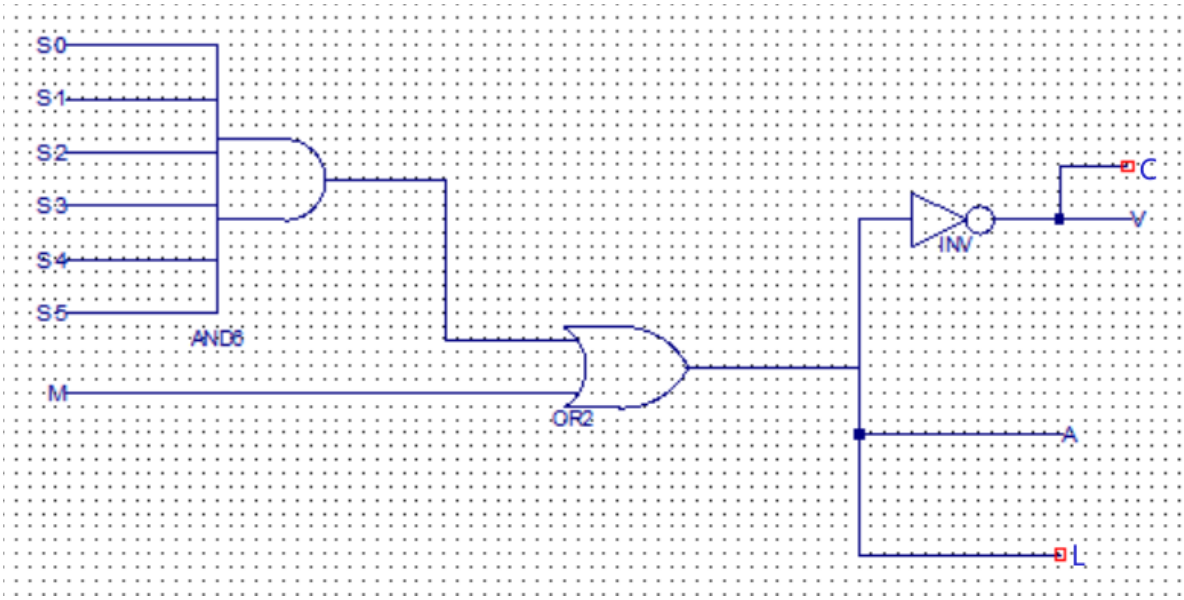
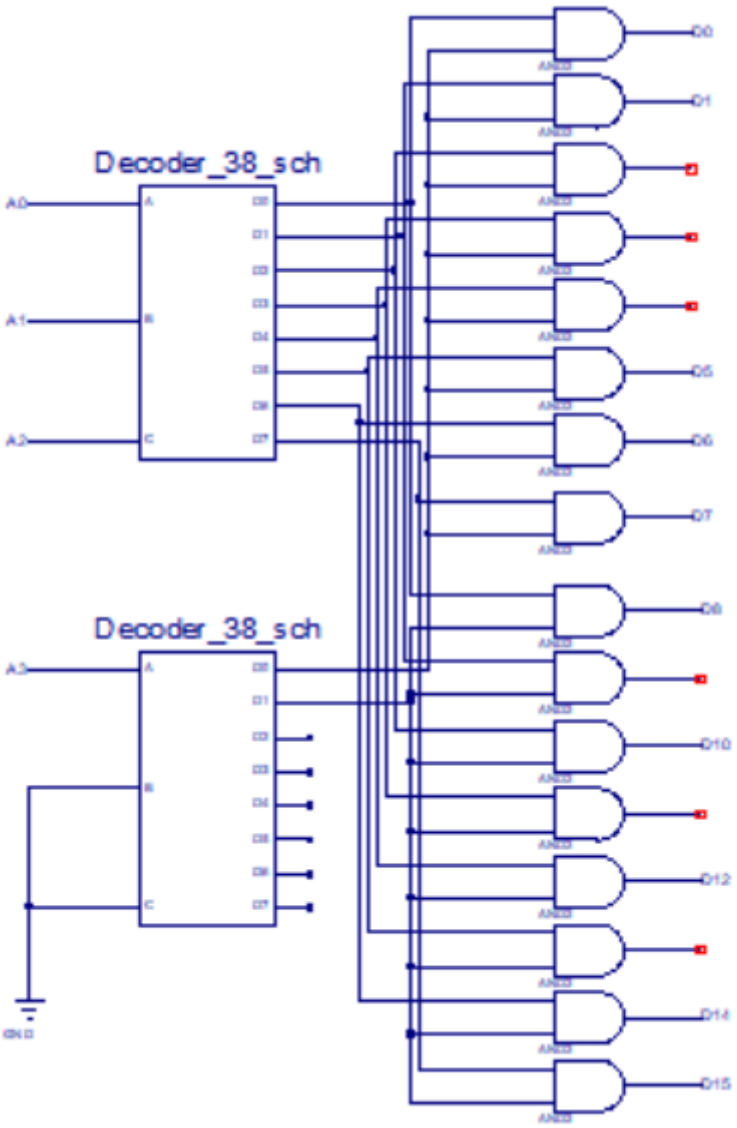
3-16

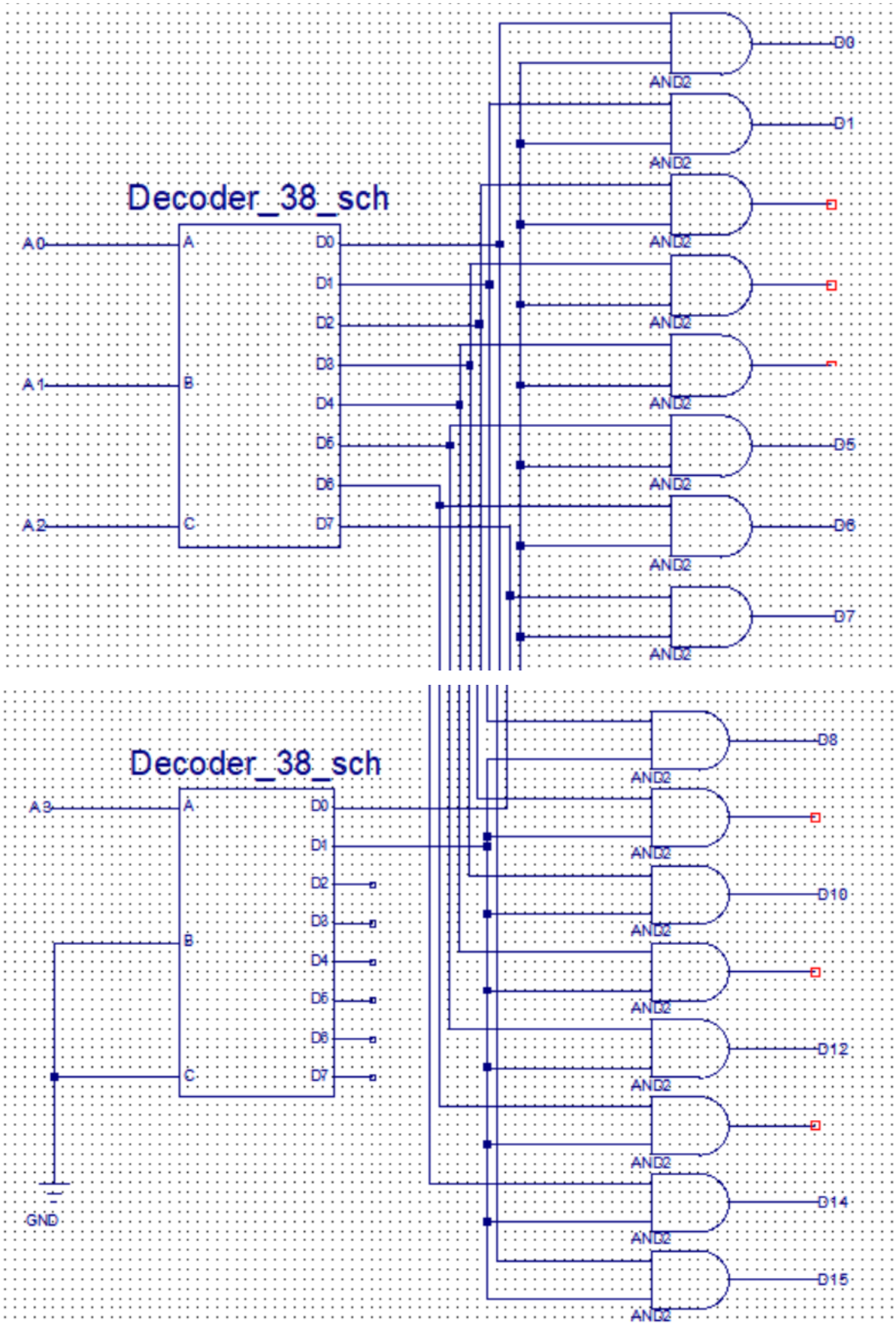

A
B
NAND2
C
D
NAND2
E
INV
NAND3
F
NAND2
INV
G

3-27

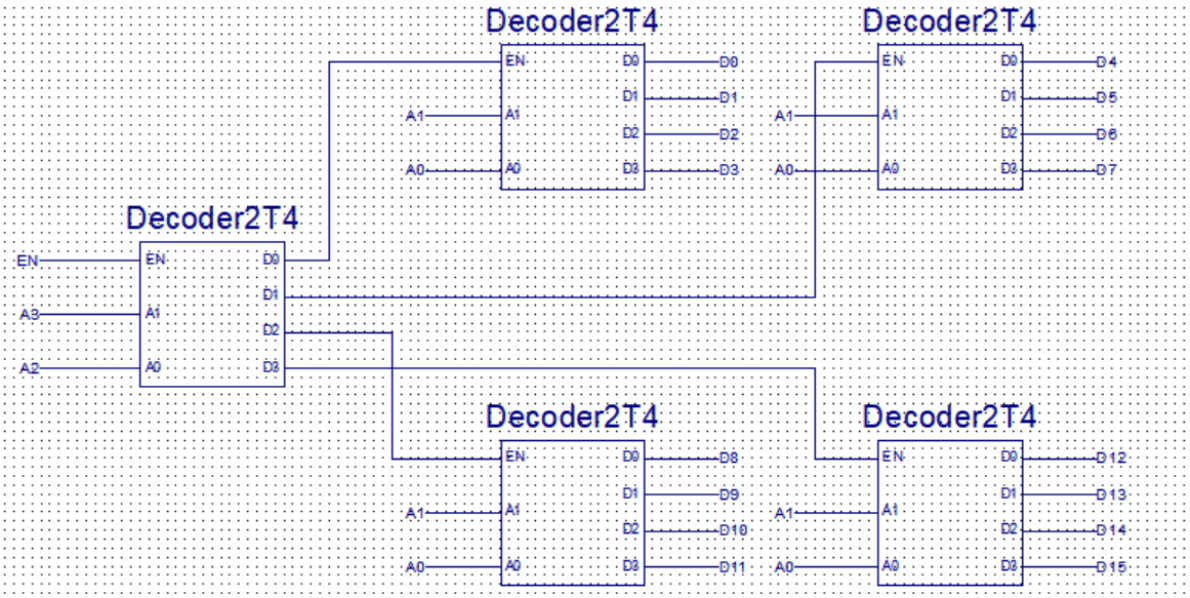

S0
S1
S2
S3
S4
S5
AND6
M
OR2
INV
C
V
A
L

3-28


Decoder_38_sch
A0
A1
A2
Decoder_38_sch
A3
D0
D1
D5
D6
D7
D10
D12
D14
D15

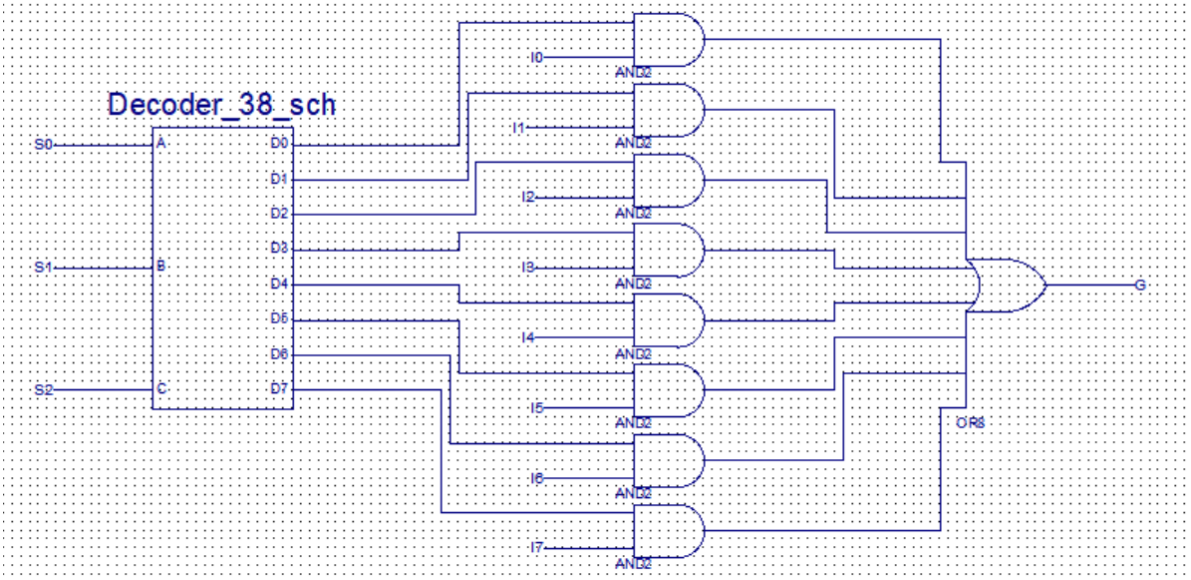

Decoder_38_sch
A0
A1
A2
A
B
C
D0
D1
D2
D3
D4
D5
D6
D7
AND2
D0
D1
D5
D6
D7
Decoder_38_sch
A3
GND
D8
D10
D12
D14
D15

3-29

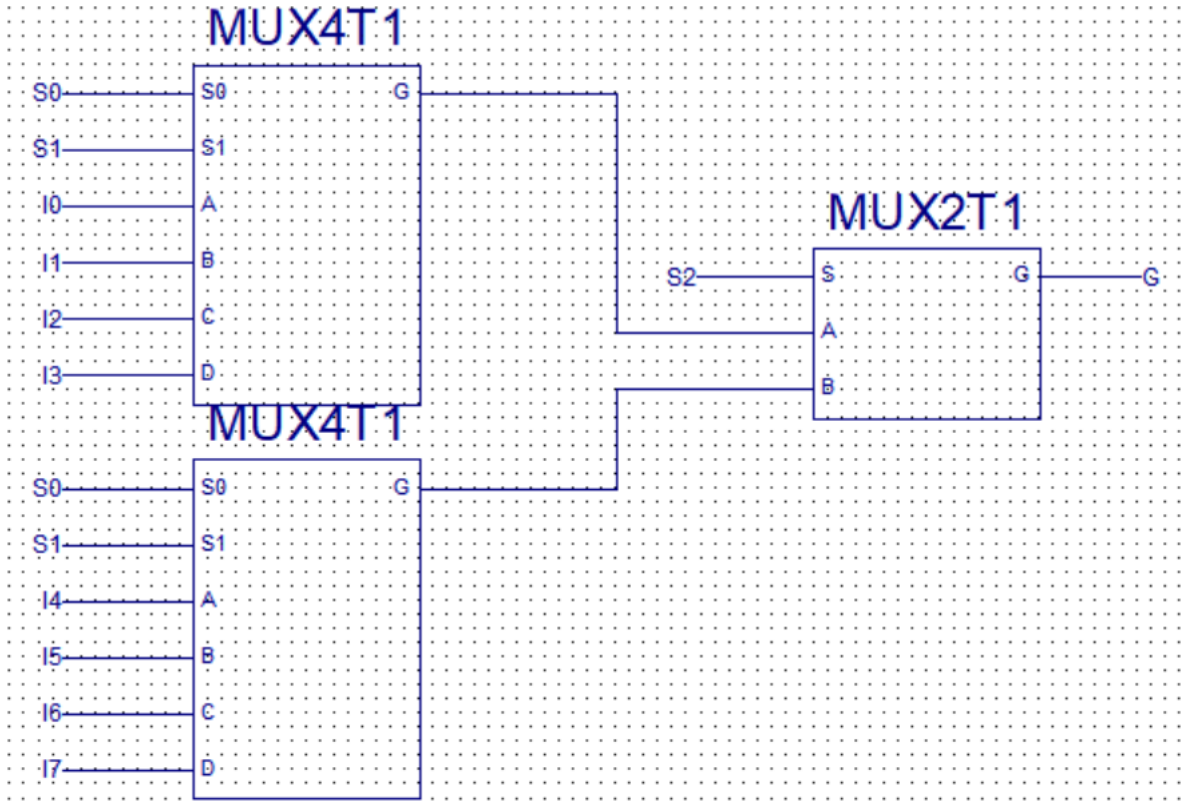

Decoder2T4
Decoder2T4
Decoder2T4
Decoder2T4
Decoder2T4
EN
A3
A2
A1
A0
D0
D1
D2
D3
D4
D5
D6
D7
D8
D9
D10
D11
D12
D13
D14
D15

3-37

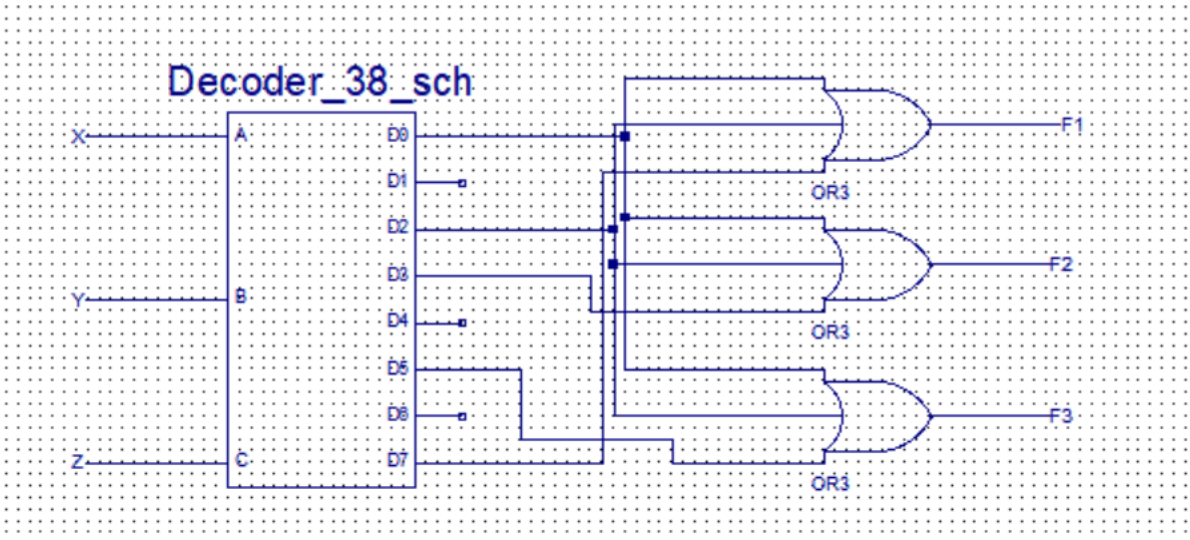
(a)


Decoder_38_sch
S0
S1
S2
A
B
C
D0
D1
D2
D3
D4
D5
D6
D7
I0
I1
I2
I3
I4
I5
I6
I7
AND3
OR8
G

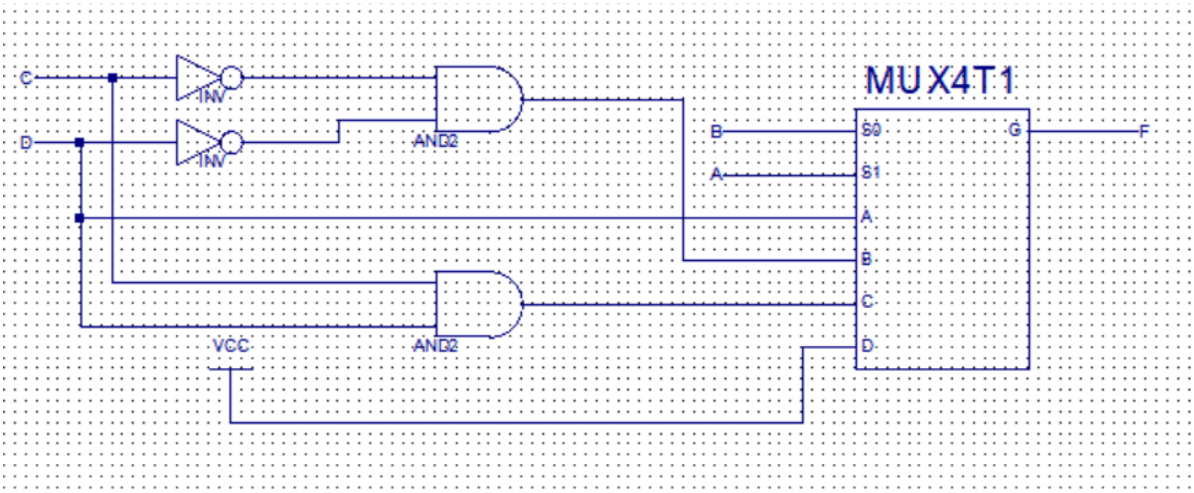
(b)


MUX4T1
S0
S1
I0
I1
I2
I3
S0
S1
A
B
C
D
G
MUX2T1
S2
S
A
B
G
G
MUX4T1
S0
S1
I4
I5
I6
I7
S0
S1
A
B
C
D
G

3-44


Decoder_38_sch
X
Y
Z
A
B
C
D0
D1
D2
D3
D4
D5
D6
D7
OR3
OR3
OR3
F1
F2
F3

3-47


C
D
INV
INV
AND2
AND2
VCC
MUX4T1
B
A
S0
S1
A
B
C
D
G
F