Project #1

Zachary Priddy
Abdulla Al Braiki
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Lab 1

Question 1:
The address of the last byte of the last instruction of this (the provided code) would be 0x2000_1B18.

Question 2:
The ident[EI_DATA]=2 means specifies 2's complement values, with the least significant byte occupying the lowest address.

AND Just because you made us look it up…
"In one of Gulliver's Travels he goes to a land where everyone is split into two warring factions; they are arguing about the correct way to eat a boiled egg. The Little-Endians eat the little end first, whereas the Big-Endians eat the big end first. (You would need to read the book, since I don't think that particular adventure ever made it into a movie.) The reference therefore is to which end of the number comes first."

Question 3:
The address pointed by entry is 0x20000BFE

Question 4:
The assembly language label is '__startup' with the instruction of 'move' and is in the source file startcf.c

Question 5:
The memory address of main is 0x20000C98 and is 4 bytes long.

Question 6:
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.vectors	0x0400	0x2000_0500 — 0x2000_08FF
.text	0x1218	0x2000_0900 — 0x2000_1B17
.data	0x0004	0x2000_1B18 — 0x2000_1B1B
.romp	0x0018	0x2000_1B1C — 0x2000_1B33
.bss	0x0004	0x2000_5004 — 0x2000_5007

Question 7:
Power – 7, 20, 30, 48, 59, 92, 100, 115, 125, 135
Ground – 8, 21, 31, 49, 60, 91, 99, 114, 124, 134

Question 8:
The frequency of Ref Clk would then be 12 MHz

Question 9:
To get 72 MHz MFD would have to be 001

Question 10:
It is not possible to get f(sys) = 50 MHz, the closest that you can get without going over is 48 MHz