

MULTILEVEL ALGORITHMS FOR ACYCLIC PARTITIONING OF
DIRECTED ACYCLIC GRAPHS*JULIEN HERRMANN[†], M. YUSUF ÖZKAYA[†], BORA UÇAR[‡],
KAMER KAYA[§], AND ÜMIT V. ÇATALYÜREK[†]

Abstract. We investigate the problem of partitioning the vertices of a directed acyclic graph into a given number of parts. The objective function is to minimize the number or the total weight of the edges having end points in different parts, which is also known as the edge cut. The standard load balancing constraint of having an equitable partition of the vertices among the parts should be met. Furthermore, the partition is required to be *acyclic*; i.e., the interpart edges between the vertices from different parts should preserve an acyclic dependency structure among the parts. In this work, we adopt the multilevel approach with coarsening, initial partitioning, and refinement phases for acyclic partitioning of directed acyclic graphs. We focus on two-way partitioning (sometimes called bisection), as this scheme can be used in a recursive way for multiway partitioning. To ensure the acyclicity of the partition at all times, we propose novel and efficient coarsening and refinement heuristics. The quality of the computed acyclic partitions is assessed by computing the edge cut. We also propose effective ways to use the standard undirected graph partitioning methods in our multilevel scheme. We perform a large set of experiments on a dataset consisting of (i) graphs coming from an application and (ii) some others corresponding to matrices from a public collection. We report significant improvements compared to the current state of the art.

Key words. directed graph, acyclic partitioning, multilevel partitioning

AMS subject classifications. 05C70, 05C85, 68R10, 68W05

DOI. 10.1137/18M1176865

1. Introduction. The standard graph partitioning (GP) problem asks for a partition of the vertices of an undirected graph into a number of parts. The objective and the constraint of this well-known problem are to minimize the number of edges having vertices in two different parts and to equitably partition the vertices among the parts. The GP problem is NP-complete [13, ND14]. We investigate a variant of this problem, called *acyclic partitioning*, for directed acyclic graphs. In this variant, we have one more constraint: the partition should be acyclic. In other words, for a suitable numbering of the parts, all edges should be directed from a vertex in a part p to another vertex in a part q where $p \leq q$.

The directed acyclic graph partitioning (DAGP) problem arises in many applications. The stated variant of the DAGP problem arises in exposing parallelism in automatic differentiation [6, Chap. 9], and particularly in the computation of the Newton step for solving nonlinear systems [4, 5]. The DAGP problem with some additional constraints is used to reason about the parallel data movement complexity and to dynamically analyze the data locality potential [10, 11]. Other important

*Submitted to the journal's Methods and Algorithms for Scientific Computing section March 23, 2018; accepted for publication (in revised form) April 3, 2019; published electronically July 3, 2019. A preliminary version of this paper appeared in *Proceedings of the 17th IEEE/ACM International Symposium on Cluster, Cloud and Grid Computing*, CCGRID'17, 2017, pp. 371–380.

<https://doi.org/10.1137/18M1176865>

[†]School of Computational Science and Engineering, Georgia Institute of Technology, Atlanta, GA 30332-0250 (julien.herrmann@cc.gatech.edu, myozka@gatech.edu, umit@gatech.edu).

[‡]CNRS and LIP (UMR5668 Université de Lyon - CNRS - ENS Lyon - Inria - UCBL 1), ENS Lyon, 69364, France (bora.ucar@ens-lyon.fr).

[§]Faculty of Engineering and Natural Sciences, Sabancı University, Istanbul 34956, Turkey (kaya@sabanciuniv.edu).

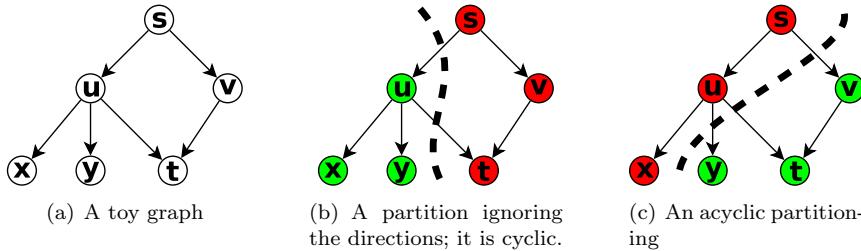


FIG. 1.1. (a) *A toy example with six tasks and six dependencies*, (b) *a nonacyclic partitioning when edges are oriented*, and (c) *an acyclic partitioning of the same directed graph*.

applications of the DAGP problem include (i) fusing loops for improving temporal locality, and enabling streaming and array contractions in runtime systems [19], such as Bohrium [20]; (ii) analysis of cache efficient execution of streaming applications on uniprocessors [1]; (iii) a number of circuit design applications in which the signal directions impose an acyclic partitioning requirement [7, 29].

Let us consider a toy example shown in Figure 1.1(a). A partition of the vertices of this graph is shown in Figure 1.1(b) with a dashed curve. Since there is a cut edge from s to u and another from u to t , the partition is cyclic and is not acceptable. An acyclic partition is shown in Figure 1.1(c), where all the cut edges are from one part to the other.

We adopt the multilevel partitioning approach [2, 14] with the coarsening, initial partitioning, and refinement phases for acyclic partitioning of DAGs. We propose heuristics for these three phases (subsections 4.1, 4.2, and 4.3, respectively) which guarantee acyclicity of the partitions at all phases and maintain a DAG at every level. We strived to have fast heuristics at the core. With these characterizations, the coarsening phase requires new algorithmic/theoretical reasoning, while the initial partitioning and refinement heuristics are direct adaptations of the standard methods used in undirected graph partitioning, with some differences worth mentioning. We discuss only the bisection case, as we were able to improve the direct k -way algorithms we proposed before [15] by using the bisection heuristics recursively—we give a brief comparison in subsection 5.4.

The acyclicity constraint on the partitions precludes the use of the state-of-the-art undirected graph partitioning tools. This has been recognized before, and those tools were put aside [15, 21]. While this is sensible, one can still try to make use of the existing undirected graph partitioning tools [14, 16, 25, 27], as they have been very well engineered. Let us assume that we have partitioned a DAG with an undirected graph partitioning tool into two parts by ignoring the directions. It is easy to detect whether the partition is cyclic since all the edges need to go from part one to part two. Furthermore, we can easily fix it as follows. Let v be a vertex in the second part; we can move all u vertices for which there is a path from v to u into the second part. This procedure breaks any cycle containing v , and hence the partition becomes acyclic. However, the edge cut may increase, and the partitions can be unbalanced. To solve the balance problem and reduce the cut, we can apply a restricted version of the move-based refinement algorithms in the literature. After this step, this final partition meets the acyclicity and balance conditions. Depending on the structure of the input graph, it could also be a good initial partition for reducing the edge cut. Indeed, one of our most effective schemes uses an undirected graph partitioning

algorithm to create a (potentially cyclic) partition, fixes the cycles in the partition, and refines the resulting acyclic partition with a novel heuristic to obtain an initial partition. We then integrate this partition within the proposed coarsening approaches to refine it at different granularities. We elaborate on this scheme in subsection 4.4.

The rest of the paper is organized as follows. Section 2 introduces the notation and background on DAGP, and section 3 briefly surveys the existing literature. We propose multilevel partitioning heuristics for acyclic partitioning of DAGs in section 4. Section 5 presents the experimental results, and section 6 concludes the paper.

2. Preliminaries and notation. A *directed graph* $G = (V, E)$ contains a set of vertices V and a set of directed edges E of the form $e = (u, v)$, where e is directed from u to v . A *path* is a sequence of edges $(u_1, v_1) \cdot (u_2, v_2), \dots$ with $v_i = u_{i+1}$. A path $((u_1, v_1) \cdot (u_2, v_2) \cdot (u_3, v_3) \cdots (u_\ell, v_\ell))$ is of length ℓ , where it connects a sequence of $\ell + 1$ vertices $(u_1, v_1 = u_2, \dots, v_{\ell-1} = u_\ell, v_\ell)$. A path is called *simple* if the connected vertices are distinct. Let $u \rightsquigarrow v$ denote a simple path that starts from u and ends at v . A path $((u_1, v_1) \cdot (u_2, v_2) \cdots (u_\ell, v_\ell))$ forms a (simple) *cycle* if all v_i for $1 \leq i \leq \ell$ are distinct and $u_1 = v_\ell$. A *directed acyclic graph*, DAG in short, is a directed graph with no cycles.

The path $u \rightsquigarrow v$ represents a dependency of v on u . We say that the edge (u, v) is *redundant* if there exists another $u \rightsquigarrow v$ path in the graph. That is, when we remove a redundant (u, v) edge, u remains connected to v , and hence the dependency information is preserved. We use $\text{Pred}[v] = \{u \mid (u, v) \in E\}$ to represent the (immediate) predecessors of a vertex v and $\text{Succ}[v] = \{u \mid (v, u) \in E\}$ to represent the (immediate) successors of v . We call the neighbors of a vertex v its immediate predecessors and immediate successors: $\text{Neigh}[u] = \text{Pred}[v] \cup \text{Succ}[v]$. For a vertex u , the set of vertices v such that $u \rightsquigarrow v$ are called the *descendants* of u . Similarly, the set of vertices v such that $v \rightsquigarrow u$ are called the *ancestors* of the vertex u . We will call vertices without any predecessors (and hence ancestors) the *sources* of G and vertices without any successors (and hence descendants) the *targets* of G . Every vertex u has a weight denoted by w_u , and every edge $(u, v) \in E$ has a cost denoted by $c_{u,v}$.

A k -way partitioning of a graph $G = (V, E)$ divides V into k disjoint subsets $\{V_1, \dots, V_k\}$. The weight of a part V_i denoted by $w(V_i)$ is equal to $\sum_{u \in V_i} w_u$, which is the total vertex weight in V_i . Given a partition, an edge is called a *cut edge* if its end points are in different parts. The *edge cut* of a partition is defined as the sum of the costs of the cut edges. Usually, a constraint on the part weights accompanies the problem. We are interested in acyclic partitions, which are defined below.

DEFINITION 2.1 (acyclic k -way partition). *A partition $\{V_1, \dots, V_k\}$ of $G = (V, E)$ is called an acyclic k -way partition if two paths $u \rightsquigarrow v$ and $v' \rightsquigarrow u'$ do not co-exist for $u, u' \in V_i$, $v, v' \in V_j$, and $1 \leq i \neq j \leq k$.*

There is a related definition in the literature [11], which is called a convex partition. A partition is convex if for all vertex pairs u, v in the same part, the vertices in any $u \rightsquigarrow v$ path are also in the same part. Hence, if a partition is acyclic, it is also convex. On the other hand, convexity does not imply acyclicity. Figure 2.1 shows that the definitions of an acyclic partition and a convex partition are not equivalent. For the toy graph in Figure 2.1(a), there are three possible balanced partitions shown in Figures 2.1(b), 2.1(c), and 2.1(d). They are all convex, but only the one in Figure 2.1(d) is acyclic.

Deciding on the existence of a k -way acyclic partition respecting an upper bound on the part weights and an upper bound on the cost of cut edges is NP-complete [13].

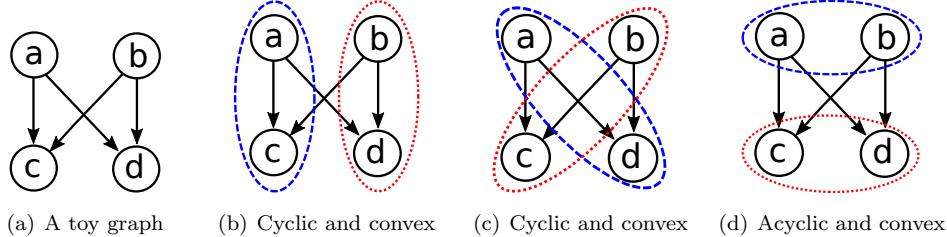


FIG. 2.1. *A toy graph (left), two cyclic and convex partitions (middle two), and an acyclic and convex partition (right).*

The formal problem treated in this paper is defined as follows.

DEFINITION 2.2 (DAGP problem). *Given a DAG $G = (V, E)$ and an imbalance parameter ε , find an acyclic k -way partition $P = \{V_1, \dots, V_k\}$ of V such that the balance constraints*

$$(2.1) \quad w(V_i) \leq (1 + \varepsilon) \frac{\sum_{v \in V} w_v}{k}$$

are satisfied for $1 \leq i \leq k$, and the edge cut is minimized.

3. Related work. Fauzia et al. [11] propose a heuristic for the acyclic partitioning problem to optimize data locality when analyzing DAGs. To create partitions, the heuristic categorizes a vertex as ready to be assigned to a partition when all of the vertices it depends on have already been assigned. Vertices are assigned to the current partition set until the maximum number of vertices that would be “active” during the computation of the part reaches a specified limit, which is the cache size in their application. This implies that part sizes are not limited by the sum of the total vertex weights but that it is a complex function that depends on an external schedule (order) of the vertices. This differs from our problem, as we limit the size of each part by the total sum of the weights of the vertices on that part.

Kernighan [17] proposes an algorithm to find a minimum edge-cut partition of the vertices of a graph into subsets of size greater than a lower bound and inferior to an upper bound. The partition needs to use a fixed vertex sequence that cannot be changed. Indeed, Kernighan’s algorithm takes a topological order of the vertices of the graph as an input and partitions the vertices such that all vertices in a subset constitute a continuous block in the given topological order. This procedure is optimal for a given, fixed topological order and has a runtime proportional to the number of edges in the graph if the part weights are taken as constant. We used a modified version of this algorithm as a heuristic in the earlier version of our work [15].

Cong, Li, and Bagrodia [7] describe two approaches for obtaining acyclic partitions of directed Boolean networks, modeling circuits. The first one is a single-level Fiduccia–Mattheyses (FM)-based approach. In this approach, Cong, Li, and Bagrodia generate an initial acyclic partition by splitting the list of the vertices (in a topological order) from left to right into k parts such that the weight of each part does not violate the bound. The quality of the results is then improved with a k -way variant of the FM heuristic [12] taking the acyclicity constraint into account. Our previous work [15] employs a similar refinement heuristic. The second approach of Cong, Li, and Bagrodia is a two-level heuristic; the initial graph is first clustered with a special decomposition, and then it is partitioned using the first heuristic.

In a recent paper [21], Moreira, Popp, and Schulz focus on an imaging and computer vision application on embedded systems and discuss acyclic partitioning heuristics. They propose a single-level approach in which an initial acyclic partitioning is obtained using a topological order. To refine the partitioning, they proposed four local search heuristics which respect the balance constraint and maintain the acyclicity of the partition. Three heuristics pick a vertex and move it to an eligible part if and only if the move improves the cut. These three heuristics differ in choosing the set of eligible parts for each vertex; some are very restrictive, and some allow arbitrary target parts as long as acyclicity is maintained. The fourth heuristic tentatively realizes the moves that increase the cut in order to escape from a possible local minimum. It has been reported that this heuristic delivers better results than the others. In a follow-up paper, Moreira, Popp, and Schulz [22] discuss a multilevel graph partitioner and an evolutionary algorithm based on this multilevel scheme. Their multilevel scheme starts with a given acyclic partition. Then, the coarsening phase contracts edges that are in the same part until there is no edge to contract. Here, matching-based heuristics from undirected graph partitioning tools are used without taking the directions of the edges into account. Therefore, the coarsening phase can create cycles in the graph; however, the induced partitions are never cyclic. Then, an initial partition is obtained, which is refined during the uncoarsening phase with move-based heuristics. In order to guarantee acyclic partitions, the vertices that lie in cycles are not moved. In a systematic evaluation of the proposed methods, Moreira, Popp, and Schulz note that there are many local minima and suggest using relaxed constraints in the multilevel setting. The proposed methods have high runtime, as the evolutionary method of Moreira, Popp, and Schulz is not concerned with this issue. Improvements with respect to the earlier work [21] are reported.

Previously, we had developed a multilevel partitioner [15]. In this paper, we propose methods to use an undirected graph partitioner to guide the multilevel partitioner. We focus on partitioning the graph in two parts since we can handle the general case with a recursive bisection scheme. We also propose new coarsening, initial partitioning, and refinement methods specifically designed for the 2-partitioning problem. Our multilevel scheme maintains acyclic partitions and graphs through all the levels.

Other related work on acyclic partitioning of directed graphs include an exact, branch-and-bound algorithm by Nossack and Pesch [23] which works on the integer programming formulation of the acyclic partitioning problem. This solution is, of course, too costly to be used in practice. Wong, Young, and Mak [29] present a modification of the decomposition of Cong, Li, and Bagrodia [7] for clustering and use this in a two-level scheme.

4. Directed multilevel graph partitioning. We propose a new multilevel tool for obtaining acyclic partitions of DAGs. Multilevel schemes [2, 14] form the de-facto standard for solving graph and hypergraph partitioning problems efficiently and are used by almost all current state-of-the-art partitioning tools [3, 14, 16, 25, 27]. Similar to other multilevel schemes, our tool has three phases: the coarsening phase, which reduces the number of vertices by clustering them; the initial partitioning phase, which finds a partition of the coarsest graph; and the uncoarsening phase, in which the initial partition is projected to the finer graphs and refined along the way, until a solution for the original graph is obtained.

4.1. Coarsening. In this phase, we obtain smaller DAGs by coalescing the vertices, level by level. This phase continues until the number of vertices becomes smaller

than a specified bound or the reduction on the number of vertices from one level to the next one is lower than a threshold. At each level ℓ , we start with a finer acyclic graph G_ℓ , compute a valid clustering \mathcal{C}_ℓ ensuring the acyclicity, and obtain a coarser acyclic graph $G_{\ell+1}$. While our previous work [15] discussed matching-based algorithms for coarsening, we present agglomerative, clustering-based variants here. The new variants supersede the matching-based ones. Unlike the standard undirected graph case, in DAGP, not all vertices can be safely combined. Consider a DAG with three vertices a, b, c and three edges $(a, b), (b, c), (a, c)$. Here, the vertices a and c cannot be combined since that would create a cycle. We say that a set of vertices is contractible (all its vertices are matchable) if unifying them does not create a cycle. We now present a general theory about finding clusters without forming cycles, after giving some definitions.

DEFINITION 4.1 (clustering). *A clustering of a DAG is a set of disjoint subsets of vertices. Note that we do not make any assumptions on whether the subsets are connected or not.*

DEFINITION 4.2 (coarse graph). *Given a DAG G and a clustering C of G , we let $G|_C$ denote the coarse graph created by contracting all sets of vertices of C .*

The vertices of the coarse graph are the clusters in C . If $(u, v) \in G$ for two vertices u and v that are located in different clusters of C , then $G|_C$ has an (directed) edge from the vertex corresponding to u 's cluster to the vertex corresponding to v 's cluster.

DEFINITION 4.3 (feasible clustering). *A feasible clustering C of a DAG G is a clustering such that $G|_C$ is acyclic.*

THEOREM 4.1. *Let $G = (V, E)$ be a DAG. For $u, v \in V$ and $(u, v) \in E$, the coarse graph $G|_{\{(u, v)\}}$ is acyclic if and only if there is no path from u to v in G avoiding the edge (u, v) .*

Proof. Let $G' = (V', E') = G|_{\{(u, v)\}}$ be the coarse graph and w be the merged, coarser vertex of G' corresponding to $\{u, v\}$.

If there is a path from u to v in G avoiding the edge (u, v) , then all the edges of this path are also in G' , and the corresponding path in G' goes from w to w , creating a cycle.

Assume that there is a cycle in the coarse graph G' . This cycle has to pass through w ; otherwise, it must be in G , which is impossible by the definition of G . Thus, there is a cycle from w to w in the coarse graph G' . Let $a \in V'$ be the first vertex visited by this cycle after w and $b \in V'$ be the last one, just before completing the cycle. Let \mathbf{p} be an $a \rightsquigarrow b$ path in G' such that $(w, a) \cdot \mathbf{p} \cdot (b, w)$ is the said $w \rightsquigarrow w$ cycle in G' . Note that a can be equal to b and in this case $\mathbf{p} = \emptyset$. By the definition of the coarse graph G' , $a, b \in V$ and all edges in the path \mathbf{p} are in $E \setminus \{(u, v)\}$. Since we have a cycle in G' , the following two items must hold:

- (i) either $(u, a) \in E$ or $(v, a) \in E$, or both; and
- (ii) either $(b, u) \in E$ or $(b, v) \in E$, or both.

Hence, overall we have nine (3×3) cases. Here, we investigate only four of them, as the “both” conditions in (i) and (ii) can be eliminated easily by the following statements:

- $(u, a) \in E$ and $(b, u) \in E$ is impossible because otherwise, $(u, a) \cdot \mathbf{p} \cdot (b, u)$ would be a $u \rightsquigarrow u$ cycle in the original graph G .
- $(v, a) \in E$ and $(b, v) \in E$ is impossible because otherwise, $(v, a) \cdot \mathbf{p} \cdot (b, v)$ would be a $v \rightsquigarrow v$ cycle in the original graph G .
- $(v, a) \in E$ and $(b, u) \in E$ is impossible because otherwise, $(u, v) \cdot (v, a) \cdot \mathbf{p} \cdot (b, u)$

would be a $u \rightsquigarrow u$ cycle in the original graph G .

Thus, $(u, a) \in E$ and $(b, v) \in E$. Therefore, $(u, a) \cdot \mathbf{p} \cdot (b, v)$ is a $u \rightsquigarrow v$ path in G avoiding the edge (u, v) , which concludes the proof. \square

Theorem 4.1 can be extended to a set of vertices by noting that this time all paths connecting two vertices of the set should contain only the vertices of the set. Neither the theorem nor its extension implies an efficient algorithm, as it requires at least one transitive reduction. Furthermore, it does not describe a condition about two clusters forming a cycle, even if both are individually contractible. In order to address both of these issues, we put a constraint on the vertices that can form a cluster, based on the following definition.

DEFINITION 4.4 (top-level value). *For a DAG $G = (V, E)$, the top-level value of a vertex $u \in V$ is the length of the longest path from a source of G to that vertex. The top-level values of all vertices can be computed in a single traversal of the graph with a complexity $O(|V| + |E|)$. We use $\text{top}[u]$ to denote the top level of the vertex u .*

The top-level value of a vertex is independent of the topological order used for computation. By restricting the set of edges considered in the clustering to the edges $(u, v) \in E$ such that $\text{top}[u] + 1 = \text{top}[v]$, we ensure that no cycles are formed by contracting a unique cluster (the condition identified in Theorem 4.1 is satisfied). Let C be a clustering of the vertices. Every edge in a cluster of C being contractible is a necessary condition for C to be feasible but not a sufficient one. More restrictions on the edges of vertices inside the clusters should be found to ensure that C is feasible. We propose three coarsening heuristics based on clustering sets of more than two vertices, whose pairwise top-level differences are always zero or one.

4.1.1. Acyclic clustering with forbidden edges. To have an efficient heuristic, we rely only on static information computable in linear time while searching for a feasible clustering. As stated in the introduction of this section, we rely on the top-level difference of one (or less) for all vertices in the same cluster, as well as an additional condition to ensure that there will be no cycles when a number of clusters are contracted simultaneously. In Theorem 4.2, we give two sufficient conditions for a clustering to be feasible (that is, the graphs at all levels are DAGs) and prove their correctness.

THEOREM 4.2 (correctness of the proposed clustering). *Let $G = (V, E)$ be a DAG and $C = \{C_1, \dots, C_k\}$ be a clustering. If C is such that*

- for any cluster C_i and for all $u, v \in C_i$, $|\text{top}[u] - \text{top}[v]| \leq 1$, and
- for two different clusters C_i and C_j and for all $u \in C_i$ and $v \in C_j$, either

$(u, v) \notin E$, or $\text{top}[u] \neq \text{top}[v] - 1$,

then the coarse graph $G|_C$ is acyclic.

Proof. Let us assume (for the sake of contradiction) that there is a clustering with the same properties above but the coarsened graph has a cycle. We pick one such clustering $C = \{C_1, \dots, C_k\}$ with the minimum number of clusters. Let $t_i = \min\{\text{top}[u], u \in C_i\}$ be the smallest top-level value of a vertex of C_i . According to the properties of C , for every vertex $u \in C_i$, either $\text{top}[u] = t_i$, or $\text{top}[u] = t_i + 1$. Let w_i be the coarse vertex in $G|_C$ obtained by contracting all vertices in C_i for $i = 1, \dots, k$. By the assumption, there is a cycle in $G|_C$, and let \mathbf{c} be one with the minimum length. This cycle passes through all the w_i vertices. Otherwise, there would be a smaller cardinality clustering with the properties above and creating a cycle in the coarsened graph, contradicting the minimal cardinality of C . Let us renumber, without loss of

generality, the w_i vertices such that \mathbf{c} is a $w_1 \rightsquigarrow w_1$ cycle which passes through all the w_i vertices in the nondecreasing order of the indices. This also renames the clusters accordingly.

After renaming the w_i vertices, for every $i \in \{1, \dots, k\}$, there is a path in $G|_C$ from w_i to w_{i+1} . Given the definition of the coarsened graph, for every $i \in \{1, \dots, k\}$, there exist a vertex $u_i \in C_i$ and a vertex $u_{i+1} \in C_{i+1}$ such that there exists a path $u_i \rightsquigarrow u_{i+1}$ in G . Thus, $\text{top}[u_i] + 1 \leq \text{top}[u_{i+1}]$. According to the second property, either there is at least one intermediate vertex between u_i and u_{i+1} and then $\text{top}[u_i] + 1 < \text{top}[u_{i+1}]$; or $\text{top}[u_i] + 1 \neq \text{top}[u_{i+1}]$ and then $\text{top}[u_i] + 1 < \text{top}[u_{i+1}]$. Thus, in any case, $\text{top}[u_i] + 1 < \text{top}[u_{i+1}]$, which can be rewritten as $\text{top}[u_i] < \text{top}[u_{i+1}] - 1$.

By definition, we know that $t_i \leq \text{top}[u_i]$ and $\text{top}[u_{i+1}] - 1 \leq t_{i+1}$. Thus, for every $i \in \{1, \dots, k\}$, we have $t_i < t_{i+1}$, which leads to the self-contradicting statement $t_1 < t_{k+1} = t_1$ and concludes the proof. \square

The main heuristic based on Theorem 4.2 is described in Algorithm 1. This heuristic visits all vertices in an order, and adds the visited vertex to a cluster, if certain criteria are met; if not, the vertex stays as a singleton. When visiting a singleton vertex, the clusters of its in-neighbors and out-neighbors are investigated, and the best (according to an objective value) among those meeting the criterion described in Theorem 4.2 is selected.

Algorithm 1 returns the `leader` array of each vertex for the current coarsening step. Vertices with the same leader form a cluster (and will form a single vertex in the coarsened graph). For each vertex $u \in V$, `leader`[u] is the id of the representative vertex for the cluster that will contain u after Algorithm 1. The `leader` table will be used to build the coarse graph. Any arbitrary vertex in a given cluster can be used as the leader of this cluster without impacting the rest of the algorithm. At the beginning, each vertex belongs to a singleton cluster, and `leader`[u] = u . To keep the track of trivial clusters (singleton vertices), we use an auxiliary `mark` array. The value `mark`[u] is *false* if u still belongs to a singleton cluster. Otherwise, the value is set to *true*.

For each singleton vertex u , we maintain an auxiliary array `nbbadneighbors` to keep the number of nontrivial *bad neighbor* clusters. That is to say, the number of clusters containing a neighbor of u that would violate the second condition of Theorem 4.2 in case u was put in another cluster. Hence, if u has only one *bad neighbor* cluster, it can only be put into this cluster. For instance, in Figure 4.1(a), at this point of the coarsening, vertex B can only be put in cluster 1. Otherwise, if vertex B was matched with one of its other neighbors, the second condition of the theorem would be violated. Thus, if a vertex has more than one *bad neighbor* in different clusters, it has to stay as a singleton. For instance, in Figure 4.1(b), vertex B has two *bad neighbor* clusters and cannot be put in any cluster without violating the second condition of Theorem 4.2. To check whether there exists another *bad neighbor* cluster previously formed, we maintain an array `leaderbadneighbor` that keeps the representative/leader of the first *bad neighbor* cluster for each vertex. Initially, this value is set to minus one.

In Algorithm 1, the function `ValidNeighbors` selects the *compatible neighbors* of vertex u , that is, the neighbors in clusters that vertex u can join. This selection is based on the top-level difference (to respect the first condition of Theorem 4.2), the number of *bad neighbors* of u and u 's neighbors (to respect the second condition of Theorem 4.2), and the size limitation (we do not want a cluster to be bigger than

10% of the total weight of the graph). Then, a best neighbor, *BestNeigh*, according to an objective value, such as the edge cost, is selected. After setting the leader of vertex u to the same value as the leader of *BestNeigh*, some bookkeeping is done for the arrays related to the second condition of Theorem 4.2. More precisely, at lines 16–22 of Algorithm 1, the neighbors of u are informed about u joining a new cluster, potentially becoming a bad neighbor. While doing that, the algorithm skips the vertices v such that $|\text{top}[u] - \text{top}[v]| > 1$ since u cannot form a bad neighbor cluster for such v . Similarly, if the best neighbor chosen for u was not in a cluster previously, i.e., was a singleton vertex, the number of *bad neighbors* of its neighbors are updated (lines 24–30).

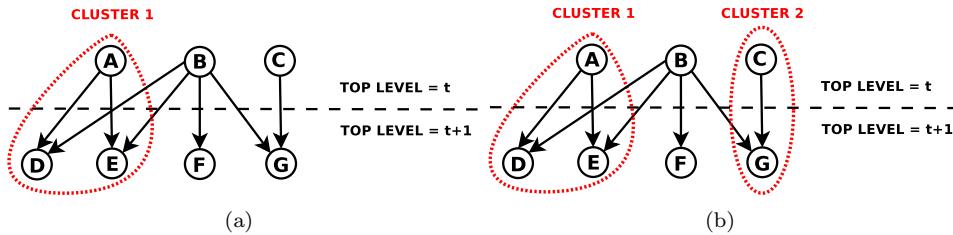


FIG. 4.1. Two examples of acyclic clustering.

In our framework, we also implemented the version in the preliminary study [15] where the size of the cluster is limited to two, meaning that it computes a matching of the vertices.

It can be easily seen that Algorithm 1 has a worst case time complexity of $O(|V| + |E|)$. The array *top* is constructed in $O(|V| + |E|)$ time, and the best, valid neighbor of a vertex u is found in $O(|\text{Neigh}[u]|)$ time. The neighbors of a vertex are visited at most once to keep the arrays related to the second condition of Theorem 4.2 up to date at lines 16 and 24.

4.1.2. Acyclic clustering with cycle detection. We now propose a less restrictive clustering algorithm to ensure that the acyclicity of the coarse graph is maintained. As in the previous section, we rely on the top-level difference of one (or less) for all vertices in the same cluster; i.e., for any cluster C_i and for all $u, v \in C_i$, $|\text{top}[u] - \text{top}[v]| \leq 1$. Knowing this invariant, when a new vertex is added to a cluster, a cycle-detection algorithm checks that no cycles are formed when all the clusters are contracted simultaneously. This algorithm does not traverse the entire graph by also using the fact that the top-level difference within a cluster is at most one.

From the proof of Theorem 4.2, we know that with a feasible clustering, if adding a vertex to a cluster whose vertices' top-level values are t and $t + 1$ creates a cycle in the contracted graph, then this cycle goes through only the vertices with top-level values t or $t + 1$. Thus, when considering the addition of a vertex u to a cluster C containing v , we check potential cycle formations by traversing the graph starting from u in a breadth-first manner in the *DetectCycle* function used in Algorithm 2. Let t denote the minimum top level in C . When at a vertex w , we normally add a successor y of w into the queue if $|\text{top}(y) - t| \leq 1$; if w is in the same cluster as one of its predecessors x , we also add x to the queue if $|\text{top}(x) - t| \leq 1$. This function uses markers to not visit the same vertex multiple times, returns *true* if at some point in the traversal a vertex from cluster C is reached, and returns *false* otherwise. In the worst case, this cycle detection algorithm completes a full graph traversal, but in

Algorithm 1: Clustering with forbidden edges.

Data: Directed graph $G = (V, E)$, a traversal order of the vertices in V , a priority on edges

Result: The leader array for the coarsening

```

1 top ← CompTopLevels( $G$ )
  /* Initialize all the auxiliary data to be used */ 
2 for  $u \in V$  do
3   mark[ $u$ ] ← false           // all vertices are marked as singleton
4   leader[ $u$ ] ←  $u$ 
5   weight[ $u$ ] ←  $w_u$         // keeps the total weight for each cluster
  /* nbbadneighbors[ $u$ ] stores the number of bad clusters for a vertex  $u$ . If it exceeds one,  $u$  is left alone (the second condition of Theorem 4.2). */
6   nbbadneighbors[ $u$ ] ← 0
7   leaderbadneighbors[ $u$ ] ← -1

8 for  $u \in V$  following the traversal order in input do
9  if mark[ $u$ ] then continue
  /* The function ValidNeighbors returns the set of valid match candidates for  $u$  based on Theorem 4.2. It also checks the threshold for the maximum cluster size and the number of bad neighbor clusters for  $u$ . */
10 N ← ValidNeighbors( $u$ ,  $G$ , nbbadneighbors, leaderbadneighbors, weight)
11 if  $N = \emptyset$  then continue
12 BestNeigh ← BestNeighbor( $N$ )
13 ℓ ← leader[BestNeigh]
14 leader[ $u$ ] ← ℓ           // assign  $u$  to BestNeigh's cluster
15 weight[ℓ] ← weight[ℓ] +  $w_u$ 
  /* Let the neighbors of  $u$  know that it is not a singleton anymore */
16 for  $v \in \text{Neigh}[u]$  do
17  if  $|\text{top}[u] - \text{top}[v]| > 1$  then continue //  $u$  cannot form a bad cluster
18  if nbbadneighbors[ $v$ ] = 0 then
19    nbbadneighbors[ $v$ ] ← 1
20    leaderbadneighbors[ $v$ ] ← ℓ
21  else if nbbadneighbors[ $v$ ] = 1 and leaderbadneighbors[ $v$ ] ≠ ℓ then
22    nbbadneighbors[ $v$ ] ← 2           // mark  $v$  as unmatchable

  /* If BestNeigh was forming a singleton cluster before  $u$ 's assignment */
23 if mark[BestNeigh] = false then
24  /* Let BestNeigh's neighbors know that it is not a singleton anymore */
25  for  $v \in \text{Neigh}[BestNeigh]$  do
26    if  $|\text{top}[BestNeigh] - \text{top}[v]| > 1$  then continue
27    if nbbadneighbors[ $v$ ] = 0 then
28      nbbadneighbors[ $v$ ] ← 1           // The first bad neighbor cluster for  $v$ 
      leaderbadneighbors[ $v$ ] ← ℓ
29    else if nbbadneighbors[ $v$ ] = 1 and leaderbadneighbors[ $v$ ] ≠ ℓ then
30      nbbadneighbors[ $v$ ] ← 2           // mark  $v$  as unmatchable
31  mark[BestNeigh] ← true           // BestNeigh is not a singleton anymore
32  mark[ $u$ ] ← true             //  $u$  is not a singleton anymore
33 return leader

```

practice, it stops quickly and does not introduce a significant overhead.

Here, we propose different clustering strategies. These algorithms consider all the vertices in the graph, one by one, and put them in a cluster if their top-level differences are at most one and if no cycles are introduced. The clustering algorithms depending on different vertex traversal orders and priority definitions on the adjacent edges are described in Algorithm 2. As with Algorithm 1, this algorithm also returns the *leader* array of each vertex for the current coarsening step. When a vertex is put in a cluster with top-level values t and $t + 1$, its *markup* (respectively, *markdown*) value is set to

true if its top-level value is t (respectively, $t + 1$). Since the worst-case complexity of the cycle detection is $O(|V| + |E|)$, the worst-case complexity of Algorithm 2 is $O(|V|(|V| + |E|))$. However, the cycle detection stops quickly in practice and the behavior of Algorithm 2 is closer to $O(|V| + |E|)$, as described in subsection 5.6.

Algorithm 2: Clustering with cycle detection.

```

Data: Directed graph  $G = (V, E)$ , a traversal order of the vertices in  $V$ , a priority
      on edges
Result: A feasible clustering  $C$  of  $G$ 
1  $\text{top} \leftarrow \text{CompTopLevels}(G)$ 
2 for  $u \in V$  do
3    $\text{markup}[u] \leftarrow \text{false}$  // if  $u$ 's cluster has a  $v$  with  $\text{top}[v] = \text{top}[u] + 1$ 
4    $\text{markdown}[u] \leftarrow \text{false}$  // if  $u$ 's cluster has a  $v$  with  $\text{top}[v] = \text{top}[u] - 1$ 
5    $\text{leader}[u] \leftarrow u$  // the leader vertex id for  $u$ 's cluster
6 for  $u \in V$  following the traversal order in input do
7   if  $\text{markup}[u]$  or  $\text{markdown}[u]$  then continue
8   for  $v \in \text{Neigh}[u]$  following given priority on edges do
9     if  $(|\text{top}[u] - \text{top}[v]| > 1)$  then continue // we use  $|\text{top}[u] - \text{top}[v]| = 1$ 
10    /* If this is a  $(u, v)$  edge */  

11    if  $v \in \text{Succ}[u]$  then
12      if  $\text{markup}[v]$  then continue
13      if  $\text{DetectCycle}(u, v, G, \text{leader})$  then continue
14       $\text{leader}[u] \leftarrow \text{leader}[v]$ 
15       $\text{markup}[u] \leftarrow \text{markup}[v] \leftarrow \text{true}$ 
16    /* If this is a  $(v, u)$  edge */  

17    if  $v \in \text{Pred}[u]$  then
18      if  $\text{markdown}[v]$  then continue
19      if  $\text{DetectCycle}(u, v, G, \text{leader})$  then continue
20       $\text{leader}[u] \leftarrow \text{leader}[v]$ 
21       $\text{markdown}[u] \leftarrow \text{markup}[v] \leftarrow \text{true}$ 
22
23 return  $\text{leader}$ 

```

4.1.3. Hybrid acyclic clustering. The cycle detection-based algorithm can suffer from quadratic runtime for vertices with large in-degrees or out-degrees. To avoid this, we design a hybrid acyclic clustering which uses the clustering strategy described in Algorithm 2 by default and switches to the clustering strategy in Algorithm 1 for *large degree* vertices. We define a limit on the degree of a vertex (typically $\sqrt{|V|}/10$) for calling it *large degree*. When considering an edge (u, v) where $\text{top}[u] + 1 = \text{top}[v]$, if the degrees of u and v do not exceed the limit, we use the cycle detection algorithm to determine whether we can contract the edge. Otherwise, if the out-degree of u or the in-degree of v is too large, the edge will be contracted if Algorithm 1 allows so. The complexity of this algorithm is in between those of Algorithms 1 and 2 and will likely avoid the quadratic behavior in practice (if not, the degree parameter can be adapted).

4.2. Initial partitioning. After the coarsening phase, we compute an initial acyclic partitioning of the coarsest graph. We present two heuristics. One of them is akin to the greedy graph growing method used in the standard graph/hypergraph partitioning methods. The second one uses an undirected partitioning and then fixes the acyclicity of the partitions. Throughout this section, we use (V_0, V_1) to denote

the bisection of the vertices of the coarsest graph G . The acyclic bisection (V_0, V_1) is such that there is no edge from the vertices in V_1 to those in V_0 .

4.2.1. Greedy directed graph growing. One approach to compute a bisection of a directed graph is to design a greedy algorithm that moves vertices from one part to another using local information. Greedy algorithms have shown to be effective for initial partitioning in multilevel schemes in the undirected case. We start with all vertices in V_1 and replace vertices towards V_0 by using heaps. At any time, the vertices that can be moved to V_0 are in the heap. These vertices are those whose all in-neighbors are in V_0 . Initially, only the sources are in the heap, and when all the in-neighbors of a vertex v are moved to the first part, v is inserted into the heap. We separate this process into two phases. In the first phase, the key values of the vertices in the heap are set to the weighted sum of their incoming edges, and the ties are broken in favor of the vertex closer to the first vertex moved. The first phase continues until the first part has more than 0.9 of the maximum allowed weight (modulo the maximum weight of a vertex). In the second phase, the actual gain of a vertex is used. This gain is equal to the sum of the weights of the incoming edges minus the sum of the weights of the outgoing edges. In this phase, the ties are broken in favor of the heavier vertices. The second phase stops as soon as the required balance is obtained. The reason that we separated this heuristic into two phases is that at the beginning, the gains are of no importance, and the more vertices become movable the more flexibility the heuristic has. Yet, towards the end, parts are fairly balanced, and using actual gains can help keep the cut small.

Since the order of the parts is important, we also reverse the roles of the parts and the directions of the edges. That is, we put all vertices in V_0 , and move the vertices one by one to V_1 , when all out-neighbors of a vertex have been moved to V_1 . The proposed greedy directed graph growing heuristic returns the better of these two alternatives.

4.2.2. Undirected bisection and fixing acyclicity. In this heuristic, we partition the coarsest graph as if it were undirected and then move the vertices from one part to another in case the partition was not acyclic. Let (P_0, P_1) denote the (not necessarily acyclic) bisection of the coarsest graph treated as if it were undirected.

The proposed approach designates arbitrarily P_0 as V_0 and P_1 as V_1 . One way to fix the cycle is to move all ancestors of the vertices in V_0 to V_0 , thereby guaranteeing that there is no edge from vertices in V_1 to vertices in V_0 , making the bisection (V_0, V_1) acyclic. We do these moves in a reverse topological order, as shown in Algorithm 3. Another way to fix the acyclicity is to move all descendants of the vertices in V_1 to V_1 , again guaranteeing an acyclic partition. We do these moves in a topological order, as shown in Algorithm 4. We then fix the possible unbalance with a refinement algorithm.

Note that we can also initially designate P_1 as V_0 and P_0 as V_1 and again use Algorithms 3 and 4 to fix a potential cycle in two different ways. We try all four of these choices and return the best partition (essentially returning the best of the four choices to fix the acyclicity of (P_0, P_1)).

4.3. Refinement. This phase projects the partition obtained for a coarse graph to the next, finer one and refines the partition by vertex moves. As in the standard refinement methods, the proposed heuristic is applied in a number of passes. Within a pass, we repeatedly select the vertex with the maximum move gain among those that can be moved. We tentatively realize this move if the move maintains or improves the

Algorithm 3: fixAcyclicityUp.

Data: Directed graph $G = (V, E)$ and a bisection $part$
Result: An acyclic bisection of G

```

1 for  $u \in G$  (in reverse topological order) do
2   if  $part[u] = 0$  then
3     for  $v \in \text{Pred}[u]$  do
4        $part[v] \leftarrow 0$ 
5 return  $part$ 
```

Algorithm 4: fixAcyclicityDown.

Data: Directed graph $G = (V, E)$ and a bisection $part$
Result: An acyclic bisection of G

```

1 for  $u \in G$  (in topological order) do
2   if  $part[u] = 1$  then
3     for  $v \in \text{Succ}[u]$  do
4        $part[v] \leftarrow 1$ 
5 return  $part$ 
```

balance. Then, the most profitable prefix of vertex moves is realized at the end of the pass. As usual, we allow the vertices to move only once in a pass; therefore, once a vertex is moved, it is not eligible to move again during the same pass. We use heaps with the gain of moves as the key value, where we keep only movable vertices. We call a vertex *movable* if moving it to the other part does not create a cyclic partition. As previously done, we use the notation (V_0, V_1) to designate the acyclic bisection with no edge from vertices in V_1 to vertices in V_0 . This means that for a vertex to move from part V_0 to part V_1 , one of the two conditions should be met: (i) either all its out-neighbors should be in V_1 ; (ii) or the vertex has no out-neighbors at all. Similarly, for a vertex to move from part V_1 to part V_0 , one of the two conditions should be met (i) either all its in-neighbors should be in V_0 ; (ii) or the vertex has no in-neighbors at all. This is in a sense the adaptation of the boundary Fiduccia–Mattheyses (FM) [12] to directed graphs, where the boundary corresponds to the movable vertices. The notion of movability being more restrictive results in an important simplification with respect to the undirected case. The gain of moving a vertex v from V_0 to V_1 is

$$(4.1) \quad \sum_{u \in \text{Succ}[v]} w(v, u) - \sum_{u \in \text{Pred}[v]} w(u, v)$$

and the negative of this value when moving it from V_1 to V_0 . This means that the gain of a vertex is static: once a vertex is inserted in the heap with the key value (4.1), it is never updated. A move could render some vertices unmovable; if they were in the heap, then they should be deleted. Therefore, the heap data structure needs to support insert, delete, and extract max operations only.

We have also implemented a swapping-based refinement heuristic akin to the boundary Kernighan–Lin (KL) [18] and another one moving vertices only from the maximum loaded part. For graphs with unit weight vertices, we suggest using the boundary FM, and for others we suggest using one pass of boundary KL followed by one pass of the boundary FM that moves vertices only from the maximum loaded part.

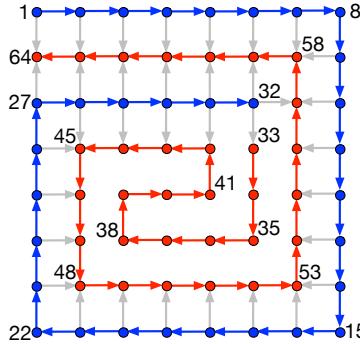


FIG. 4.2. 8×8 grid graph whose vertices are ordered in a spiral way; a few of the vertices are labeled with their number. All edges are oriented from a lower numbered vertex to a higher numbered one. There is a unique bipartition with 32 vertices in each side. The edges defining the total order are shown in red and blue (color is available online only), except the one from 32 to 33; the cut edges are shown in gray; other internal edges are not shown.

4.4. Constraint coarsening and initial partitioning. There are a number of highly successful, undirected graph partitioning libraries [16, 25, 27]. They are not directly usable for our purposes, as the partitions can be cyclic. Fixing such partitions, by moving vertices to break the cyclic dependencies among the parts, can increase the edge cut dramatically (with respect to the undirected cut). Consider, for example, the $n \times n$ grid graph, where the vertices are integer positions for $i = 1, \dots, n$ and $j = 1, \dots, n$ and a vertex at (i, j) is connected to (i', j') when $|i - i'| = 1$ or $|j - j'| = 1$, but not both. There is an acyclic orientation of this graph, called spiral ordering, as described in Figure 4.2 for $n = 8$. This spiral ordering defines a total order. When the directions of the edges are ignored, we can have a bisection with perfect balance by cutting only $n = 8$ edges with a vertical line. This partition is cyclic, and it can be made acyclic by putting all vertices numbered greater than 32 to the second part. This partition, which puts the vertices 1–32 to the first part and the rest to the second part, is the unique acyclic bisection with perfect balance for the associated DAG. The edge cut in the directed version is 35, as seen in the figure (gray edges). In general, one has to cut $n^2 - 4n + 3$ edges for $n \geq 8$: the blue vertices in the border (excluding the corners) have one edge directed to a red vertex; the interior blue vertices have two such edges; finally, the blue vertex labeled $n^2/2$ has three such edges.

Let us also investigate the quality of the partitions from a more practical standpoint. We used MeTiS [16] as the undirected graph partitioner on a dataset of 94 matrices (their details are in section 5). The results are given in Figure 4.3. For this preliminary experiment, we partitioned the graphs into two with the maximum allowed load imbalance $\varepsilon = 3\%$. In the experiment, for only two graphs, the output of MeTiS is acyclic, and the geometric mean of the normalized edge cut is 0.0012. Figure 4.3(a) shows the normalized edge cut and the load imbalance after fixing the cycles, while Figure 4.3(b) shows the two measurements after meeting the balance criteria. A normalized edge cut value is computed by normalizing the edge cut with respect to the number of edges.

In both figures, the horizontal lines mark the geometric mean of the normalized edge cuts, and the vertical lines mark the 3% imbalance ratio. In Figure 4.3(a), there are 37 instances in which the load balance after fixing the cycles is feasible. The

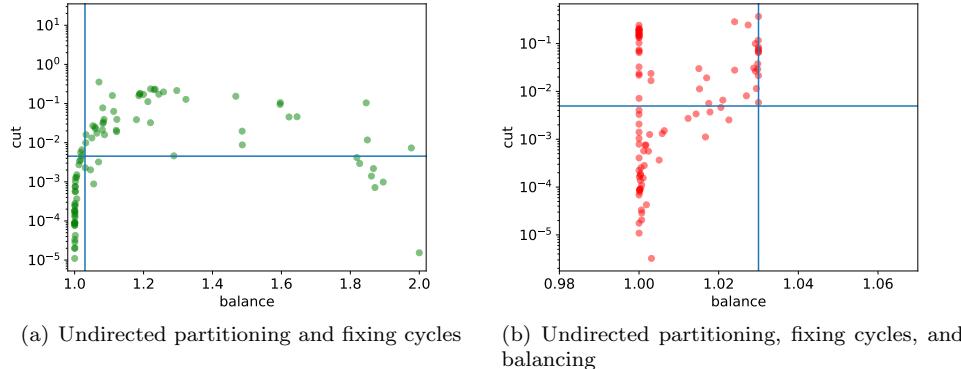


FIG. 4.3. Normalized edge cut (normalized with respect to the number of edges) and the balance obtained after using an undirected graph partitioner and fixing the cycles (left) and after ensuring balance with refinement (right).

geometric mean of the normalized edge cuts in this subfigure is 0.0045, while in the other subfigure, it is 0.0049. Fixing the cycles increases the edge cut with respect to an undirected partitioning, but not catastrophically (only by $0.0045/0.0012 = 3.75$ times in these experiments), and achieving balance after this step increases the cut only a little (goes to 0.0049 from 0.0045). That is why we suggest using an undirected graph partitioner, fixing the cycles among the parts, and performing a refinement-based method for load balancing as a good (initial) partitioner.

In order to refine the initial partition in a multilevel setting, we propose a scheme similar to the *iterated multilevel algorithm* used in the existing partitioners [3, 28]. In this scheme, first a partition P is obtained. Then, the coarsening phase is employed to match (or to agglomerate) the vertices that were in the same part in P . After the coarsening, an initial partitioning is freely available by using the partition P on the coarsest graph. The refinement phase then can work as before. Moreira, Popp, and Schulz [22] use this approach for the directed graph partitioning problem. To be more concrete, we first use an undirected graph partitioner, then fix the cycles as discussed in subsection 4.2.2, and then refine this acyclic partition for balance with the proposed refinement heuristics in subsection 4.3. We then use this acyclic partition for constraint coarsening and initial partitioning. We expect this scheme to be successful in graphs with many sources and targets where the sources and targets can lie in any of the parts while the overall partition is acyclic. On the other hand, if a graph is such that its balanced acyclic partitions need to put the sources in one part and the targets in another part, then fixing acyclicity may result in moving many vertices. This in turn will harm the edge cut found by the undirected graph partitioner.

5. Experimental evaluation. The partitioning tool presented (**dagP**) is implemented in C/C++ programming languages. The experiments are conducted on a computer equipped with dual 2.1 GHz, Xeon E5-2683 processors and 512GB memory. The source code and more information is available at <http://tda.gatech.edu/software/dagP/>.

We have performed an extensive evaluation of the proposed multilevel DAGP method on DAG instances coming from two sources. The first set of instances is from the Polyhedral Benchmark suite (PolyBench) [26], whose parameters are listed

TABLE 5.1
Instances from the Polyhedral Benchmark suite (PolyBench).

Graph	Parameters	#vertex	#edge	Max. deg.	Avg. deg.	#source	#target
2mm	P=10, Q=20, R=30, S=40	36,500	62,200	40	1.704	2100	400
3mm	P=10, Q=20, R=30, S=40, T=50	111,900	214,600	40	1.918	3900	400
adi	T=20, N=30	596,695	1,059,590	109,760	1.776	843	28
atax	M=210, N=230	241,730	385,960	230	1.597	48530	230
covariance	M=50, N=70	191,600	368,775	70	1.925	4775	1275
doitgen	P=10, Q=15, R=20	123,400	237,000	150	1.921	3400	3000
durbin	N=250	126,246	250,993	252	1.988	250	249
fstd-2d	T=20, X=30, Y=40	256,479	436,580	60	1.702	3579	1199
gemm	P=60, Q=70, R=80	1,026,800	1,684,200	70	1.640	14600	4200
gemver	N=120	159,480	259,440	120	1.627	15360	120
gesummv	N=250	376,000	500,500	500	1.331	125250	250
heat-3d	T=40, N=20	308,480	491,520	20	1.593	1280	512
jacobi-1d	T=100, N=400	239,202	398,000	100	1.664	402	398
jacobi-2d	T=20, N=30	157,808	282,240	20	1.789	1008	784
lu	N=80	344,520	676,240	79	1.963	6400	1
ludcmp	N=80	357,320	701,680	80	1.964	6480	1
mvt	N=200	200,800	320,000	200	1.594	40800	400
seidel-2d	M=20, N=40	261,520	490,960	60	1.877	1600	1
symm	M=40, N=60	254,020	440,400	120	1.734	5680	2400
syr2k	M=20, N=30	111,000	180,900	60	1.630	2100	900
syrk	M=60, N=80	594,480	975,240	81	1.640	8040	3240
trisolv	N=400	240,600	320,000	399	1.330	80600	1
trmm	M=60, N=80	294,570	571,200	80	1.939	6570	4800

in Table 5.1. The graphs in the Polyhedral Benchmark suite arise from various linear computation kernels. The parameters in the second column of Table 5.1 represent the size of these computation kernels. For more details, we refer the reader to the description of the Polyhedral Benchmark suite (PolyBench) [26]. The second set of instances is obtained from the matrices available in the SuiteSparse Matrix Collection (formerly known as the University of Florida Sparse Matrix Collection) [8]. From this collection, we pick all the matrices satisfying the following properties: listed as binary and square and has at least 100000 rows and at most 2^{26} nonzeros. There were a total of 95 matrices at the time of experimentation, with two matrices (ids 1514 and 2294) having the same pattern. We discard the duplicate and use the remaining 94 matrices for experiments. For each such matrix, we take the strict upper triangular part as the associated DAG instance whenever this part has more nonzeros than the lower triangular part; otherwise, we take the strict lower triangular part. All edges have unit cost, and all vertices have unit weight.

Since the proposed heuristics have a randomized behavior (the traversals used in the coarsening and refinement heuristics are randomized), we run them 10 times for each DAG instance and report the averages of these runs. We use performance profiles [9] to present the edge cut results. A performance profile plot shows the probability that a specific method gives results within a factor θ of the best edge cut obtained by any of the methods compared in the plot. Hence, the higher and closer a plot is to the y -axis, the better the method is.

We set the load imbalance parameter $\varepsilon = 0.03$ in (2.1) for all experiments. The vertices are unit weighted, and therefore the imbalance is rarely an issue for a move-based partitioner.

5.1. Coarsening evaluation. We first evaluate the proposed coarsening heuristics. The aim is to find an effective one to set as a default coarsening heuristic.

The performance profile chart given in Figure 5.1 shows the effect of the coarsen-

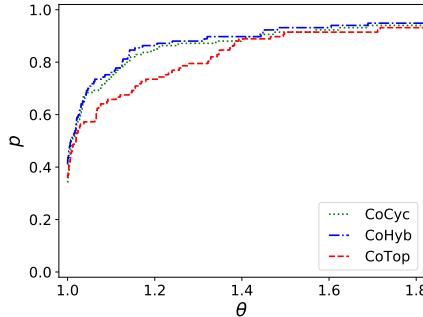


FIG. 5.1. Performance profiles of the proposed multilevel algorithm variants using three difference coarsening heuristics in terms of edge cut.

ing heuristics on the final edge cut for the whole dataset. The variants of the proposed multilevel algorithm which use different coarsening schemes are called **CoTop** (subsection 4.1.1), **CoCyc** (subsection 4.1.2), and **CoHyb** (subsection 4.1.3). Here, and in the rest of the paper, we used a randomized depth-first topological order for the node traversal in the coarsening heuristics since it performed better in practice. In Figure 5.1, we see that **CoCyc** and **CoHyb** behave similarly; this is expected, as not all graphs have vertices with large degrees. From this figure, we conclude that in general, the coarsening heuristics **CoHyb** and **CoCyc** are more helpful than **CoTop** in reducing the edge cut.

Another important characteristic to assess for a coarsening heuristic is its contraction efficiency. It is important that the coarsening phase does not stop too early and that the coarsest graph is small enough to be partitioned efficiently. Table 5.2 gives the maximum, the average, and the standard deviation of vertex and edge weight ratios and the average, the minimum, and the maximum number of coarsening levels observed for the two datasets. An effective coarsening heuristic should have small vertex and edge weight ratios. We see that **CoCyc** and **CoHyb** behave similarly and provide slightly better results than **CoTop** on both datasets. The graphs from the two datasets have different characteristics. All coarsening heuristics perform better on the PolyBench instances compared to the UFL instances: they obtain smaller ratios in the number of remaining vertices and yield smaller edge weights. Furthermore, the maximum vertex and edge weight ratios are smaller in PolyBench instances, again with all coarsening methods. To the best of our understanding, these happen for two reasons: (i) the average degree in the UFL instances is larger than that of the PolyBench instances (3.63 vs. 1.72); (ii) the ratio of the total number of source and target vertices to the total number of vertices is again larger in the UFL instances (0.13 vs. 0.03). Based on Figure 5.1 and Table 5.2, we set **CoHyb** as the default coarsening heuristic, as it performs better than **CoTop** in terms of final edge cut and is guaranteed to be more efficient than **CoCyc** in terms of runtime.

5.2. Constraint coarsening and initial partitioning. We now investigate the effect of using undirected graph partitioners to obtain a more effective coarsening and better initial partitions, as explained in subsection 4.4. We compare three variants of the proposed multilevel scheme. All of them use the refinement described in subsection 4.3 in the uncoarsening phase:

TABLE 5.2

The maximum, average, and standard deviation of vertex and edge weight ratios, and the average, the minimum, and the maximum number of coarsening levels for the UFL dataset on the upper half of the table, and for the PolyBench dataset on the lower half.

Algorithm	Vertex ratio (%)			Edge weight ratio (%)			Coarsening levels		
	avg	std. dev	max	avg	std. dev	max	avg	min	max
CoTop	1.29	6.34	46.72	26.07	24.95	87.00	12.45	2	17.0
CoCyc	1.06	6.31	47.29	25.97	24.86	87.90	12.74	2	17.6
CoHyb	1.08	6.27	46.70	26.00	24.80	87.00	12.69	2	17.7
CoTop	1.33	2.26	8.50	25.67	11.08	47.60	7.44	4	11.8
CoCyc	0.41	0.90	4.10	24.96	9.20	37.00	8.37	5	12.0
CoHyb	0.54	0.88	3.60	24.81	9.33	39.00	8.46	5	11.9

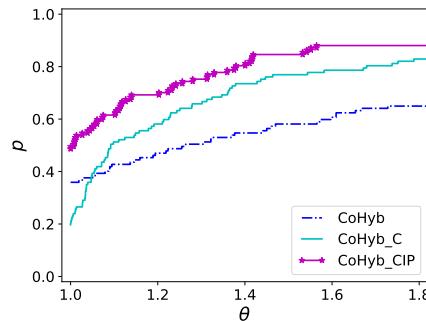


FIG. 5.2. Performance profiles for the edge cut obtained by the proposed multilevel algorithm using the constraint coarsening and partitioning (CoHyb_CIP), using the constraint coarsening and the greedy directed graph growing (CoHyb_C), and using the best identified approach without constraints (CoHyb).

- CoHyb: this variant uses the hybrid coarsening heuristic described in subsection 4.1.3 and the greedy directed graph growing heuristic described in subsection 4.2.1 in the initial partitioning phase. This method does not use constraint coarsening.
- CoHyb_C: this variant uses an acyclic partition of the finest graph, obtained as outlined in subsection 4.2.2 to guide the hybrid coarsening heuristic described in subsection 4.4, and uses the greedy directed graph growing heuristic in the initial partitioning phase.
- CoHyb_CIP: this variant uses the same constraint coarsening heuristic as the previous method but inherits the fixed acyclic partition of the finest graph as the initial partitioning.

The comparison of these three variants is given in Figure 5.2 for the whole dataset. From Figure 5.2, we see that using the constraint coarsening is always helpful with respect to not using them. This clearly separates CoHyb_C and CoHyb_CIP from CoHyb after $\theta = 1.1$. Furthermore, applying the constraint initial partitioning (on top of the constraint coarsening) brings tangible improvements.

In light of the experiments presented here, we suggest the variant CoHyb_CIP for general problem instances, as this has clear advantages over others in our dataset.

5.3. Evaluating CoHyb_CIP with respect to a single-level algorithm. We compare CoHyb_CIP (the variant of the proposed approach with constraint coarsening and initial partitioning) with a single-level algorithm that uses an undirected graph

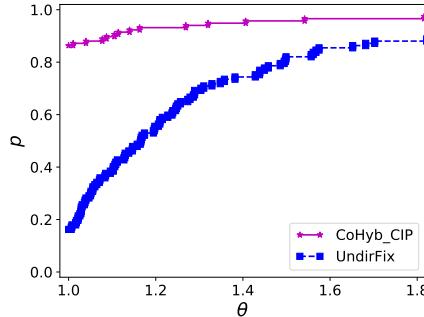


FIG. 5.3. Performance profiles for the edge cut obtained by the proposed multilevel approach using the constraint coarsening and partitioning (CoHyb_CIP) and using the same approach without coarsening (UndirFix).

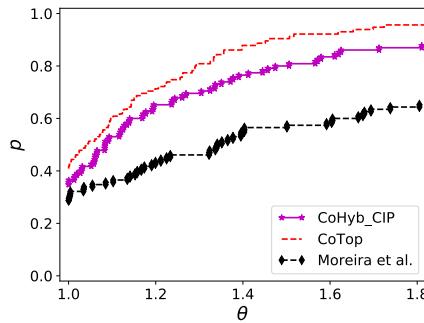


FIG. 5.4. Performance profiles for the edge cut obtained by CoHyb_CIP, CoTop, and Moreira, Popp, and Schulz's approach on the PolyBench dataset with $k \in \{2, 4, 8, 16, 32\}$.

partitioning, fixes the acyclicity, and refines the partitions. This last variant is denoted as **UndirFix**, and it is the algorithm described in subsection 4.2.2. Both variants use the same initial partitioning approach, which utilizes MeTiS [16] as the undirected partitioner. The difference between **UndirFix** and **CoHyb_CIP** is the latter's ability to refine the initial partition at multiple levels. Figure 5.3 presents this comparison. The plots show that the multilevel scheme **CoHyb_CIP** outperforms the single-level scheme **UndirFix** at all appropriate ranges of θ , attesting to the importance of the multilevel scheme.

5.4. Comparison with existing work. Here, we compare our approach with the evolutionary graph partitioning approach developed by Moreira, Popp, and Schulz [21] and briefly with our previous work [15].

Figure 5.4 shows how **CoHyb_CIP** and **CoTop** compare with the evolutionary approach in terms of the edge cut on the 23 graphs of the PolyBench dataset for the number of partitions $k \in \{2, 4, 8, 16, 32\}$. We use the average edge cut value of 10 runs for **CoTop** and **CoHyb_CIP** and the average values presented in [21] for the evolutionary algorithm. As seen in the figure, the **CoTop** variant of the proposed multilevel approach obtains the best results on this specific dataset (all variants of the proposed approach outperform the evolutionary approach).

Tables A.1 and A.2 show the average and best edge cuts found by CoHyb_CIP and CoTop variants of our partitioner and the evolutionary approach on the PolyBench dataset. The two tables just after them (Tables A.3 and A.4) give the associated balance factors. The variants CoHyb_CIP and CoTop of the proposed algorithm obtain strictly better results than the evolutionary approach in 78 and 75 instances (out of 115), respectively, when the average edge cuts are compared.

As seen in the last row of Table A.2, CoHyb_CIP obtains 26% less edge cut than the evolutionary approach on average (geometric mean) when the average cuts are compared (0.74 vs. 1.00 in the table); when the best cuts are compared, CoHyb_CIP obtains 48% less edge cut (0.50 vs. 0.96). Moreover, CoTop obtains 37% less edge cut than the evolutionary approach when the average cuts are compared (0.63 vs. 1.00 in the table); when the best cuts are compared, CoTop obtains 41% less cut (0.57 vs. 0.96). In some instances (for example, covariance and gemm in Table A.1 and syrk and trmm in Table A.2), we see large differences between the average and the best results of CoTop and CoHyb_CIP. Combined with the observation that CoHyb_CIP yields better results in general, this suggests that the neighborhood structure can be improved (see the notion of the strength of a neighborhood [24, section 19.6]). All partitions attain 3% balance.

The proposed approach with all the reported variants takes about 30 minutes to complete the whole set of experiments for this dataset, whereas the evolutionary approach is much more compute-intensive, as it has to run the multilevel partitioning algorithm numerous times to create and update the population of partitions for the evolutionary algorithm. The multilevel approach of Moreira, Popp, and Schulz [21] is more comparable in terms of characteristics with our multilevel scheme. When we compare CoTop with the results of the multilevel algorithm by Moreira, Popp, and Schulz, our approach provides results that are 37% better on average and the CoHyb_CIP approach provides results that are 26% better on average, highlighting the fact that keeping the acyclicity of the directed graph through the multilevel process is useful.

Finally, CoTop and CoHyb_CIP also outperform the previous version of our multilevel partitioner [15], which is based on a direct k -way partitioning scheme and matching heuristics for the coarsening phase, by 45% and 35% on average, respectively, on the same dataset.

5.5. Single commodity flow-like problem instances. In many of the instances of our dataset, graphs have many source and target vertices. We investigate how our algorithm performs on problems where all source vertices should be in a given part, and all target vertices should be in the other part, while also achieving balance. This is a problem close to the maximum flow problem, where we want to find the maximum flow (or minimum cut) from the sources to the targets with balance on part weights. Furthermore, addressing this problem also provides a setting for solving partitioning problems with fixed vertices.

For these experiments, we used the UFC dataset. We discarded all isolated vertices and added to each graph a source vertex S (with an edge from S to all source vertices of the original graph with a cost equal to the number of edges) and a target vertex T (with an edge from all target vertices of the original graph to T with a cost equal to the number of edges). A feasible partition should avoid cutting these edges and separate all sources from the targets.

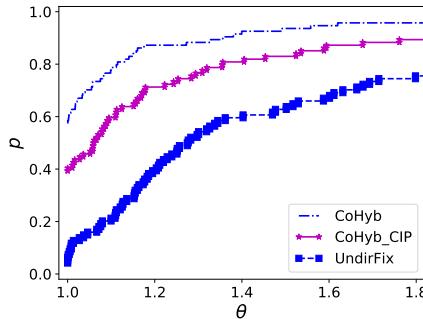


FIG. 5.5. Performance profiles of CoHyb, CoHyb_CIP, and UndirFix in terms of edge cut for the single source, single target graph dataset. The average of five runs is reported for each approach.

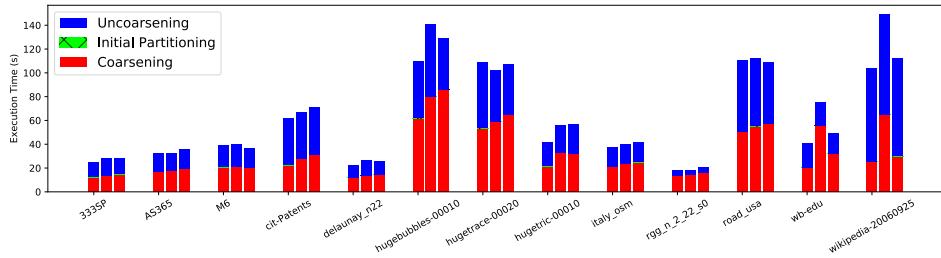


FIG. 5.6. Runtimes for CoTop, CoCyc, and CoHyb variants of the proposed multilevel scheme. For each bar group, the first, second, and third bars present the detailed runtimes of CoTop, CoCyc, and CoHyb, respectively.

The performance profiles of CoHyb, CoHyb_CIP, and UndirFix are given in Figure 5.5 with the edge cut as the evaluation criterion. As seen in this figure, CoHyb is the best performing variant, and UndirFix is the worst performing variant. This is interesting, as in the general setting, we saw a reverse relation. The variant CoHyb_CIP performs in the middle, as it combines the other two.

5.6. Runtime performance. We now assess the runtime performance of the proposed algorithms. Figure 5.6 shows the runtime comparison and distribution for 13 graphs with the longest coarsening time for the CoTop variant. A description of these 13 graphs can be found in Table 5.3. In Figure 5.6, each graph has three bars representing the runtime for the multilevel algorithm using the coarsening heuristics described in subsection 4.1: CoTop, CoCyc, and CoHyb. We can see that the runtime performances of the three coarsening heuristics are similar. This means that the cycle detection function in CoCyc does not introduce a large overhead, as stated in subsection 4.1.2. Most of the time, CoCyc has a bit longer runtime than CoTop, and CoHyb offers a good tradeoff. Note that in Figure 5.6, the computation time of the initial partitioning is negligible compared to that of the coarsening and uncoarsening phases, which means that the graphs have been efficiently contracted during the coarsening phase.

Figure 5.7 shows the comparison of the five variants of the proposed multilevel scheme and the single-level scheme on the whole dataset. Each algorithm is run 10 times on each graph. As expected, CoTop offers the best performance, and CoHyb

TABLE 5.3
13 instances from the UFL dataset with the longest coarsening times for CoTop.

Graph	#vertex	#edge	Max in	Max out	Avg deg	#source	#target
333SP	3,712,815	11,108,633	9	27	2.992	188,112	316,151
AS365	3,799,275	11,368,076	10	13	2.992	306,791	519,431
M6	3,501,776	10,501,936	10	10	2.999	280,784	472,230
cit-Patents	3,774,768	16,518,209	779	770	4.376	515,980	1,685,419
delaunay-n22	4,194,304	12,582,869	15	17	3	555,807	337,743
hugebubbles-00010	19,458,087	29,179,764	3	3	1.5	3,355,886	3,054,827
hugetrace-00020	16,002,413	23,998,813	3	3	1.5	2,514,461	2,407,017
hugetric-00010	6,592,765	9,885,854	3	3	1.5	1,085,866	1,006,163
italy-osm	6,686,493	7,013,978	5	8	1.049	155,509	458,561
rgg-n-2-22-s0	4,194,304	30,359,198	24	25	7.238	3,550	3,576
road-usa	23,947,347	28,854,312	8	8	1.205	6,392,288	8,010,032
wb-edu	9,845,725	29,494,732	17,489	3841	2.996	1,489,057	2,794,680
wikipedia-20060925	2,983,494	26,103,626	74,970	5,844	8.749	1,406,429	72,744

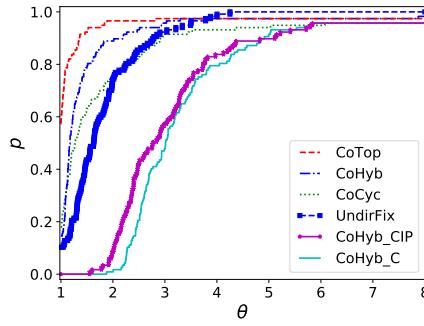


FIG. 5.7. Runtime performance profile of CoCyc, CoHyb, CoTop, CoHyb_C, CoHyb_CIP, and UndirFix on the whole dataset. The values are the averages of 10 runs.

offers a good tradeoff between CoTop and CoCyc. An interesting remark is that these three algorithms have a better runtime than the single-level algorithm UndirFix. For example, on average, CoTop is 1.44 times faster than UndirFix. This is mainly due to the cost of fixing acyclicity. Undirected partitioning accounts for roughly 25% of the execution time of UndirFix, and fixing the acyclicity constitutes the remaining 75%. Finally, the variants of the multilevel algorithm using constraint coarsening heuristics provide satisfying runtime performance with respect to the others.

6. Conclusion. We proposed a multilevel approach for acyclic partitioning of DAGs. This problem is close to the standard graph partitioning in that the aim is to partition the vertices into a number of parts while minimizing the edge cut and meeting a balance criterion on the part weights. Unlike the standard graph partitioning problem, the directions of the edges are important and the resulting partitions should have acyclic dependencies.

We proposed coarsening, initial partitioning, and refinement heuristics for the target problem. The proposed heuristics take the directions of the edges into account and maintain the acyclicity through all the multilevel hierarchy. We also proposed efficient and effective approaches to use the standard undirected graph partitioning tools in the multilevel scheme for coarsening and initial partitioning. We performed a large set of experiments on a dataset with graphs having different characteristics and evaluated different combinations of the proposed heuristics. Our experiments

suggested (i) the use of constraint coarsening and initial partitioning, where the main coarsening heuristic is a hybrid one which avoids the cycles and, in case it does not, performs a fast cycle detection (**CoHyb_CIP**) for the general case; (ii) a pure multilevel scheme without constraint coarsening, using the hybrid coarsening heuristic (**CoHyb**) for the cases where a number of sources need to be separated from a number of targets; (iii) a pure multilevel scheme without constraint coarsening, using the fast coarsening algorithm (**CoTop**) for the cases where the degrees of the vertices are small. All three approaches are shown to be more effective and efficient than the current state of the art.

An avenue for future work is applying the proposed multilevel scheme in real life applications that are based on task graphs. This requires a scheduling step to be applied after the proposed partitioning scheme, which needs further investigations. A recent work uses a multilevel algorithm for recombination and mutation [22]. Plugging in our multilevel scheme to that framework can yield significant improvements.

Appendix A. Detailed results on the PolyBench instances. We give in Tables A.1 and A.2 the detailed edge cut results of the proposed **CoTop** and **CoHyb_CIP** and Moreira, Popp, and Schulz's evolutionary algorithm [21]. Tables A.3 and A.4 give the balance attained in the partitions. In these two tables, the average balance of the 10 runs yielding the average edge cut of Tables A.1 and A.2 is reported per problem instance. The balance of the partition yielding the best edge cut of the previous tables is also given per problem instance.

Acknowledgment. We thank John Gilbert for his comments on an earlier version of this work presented at CSC'16. John suggested that we look at the spiral ordering of the grid graph.

TABLE A.1

Comparing the edge cuts obtained by CoHyb-CIP and CoTop with those obtained by the evolutionary algorithm of Moreira, Popp, and Schulz on the Polyhedral Benchmark Suite (first set of results).

Graph	k	Moreira, Popp, and Schulz [21]		CoHyb-CIP		CoTop	
		Average	Best	Average	Best	Average	Best
2mm	2	200	200	200	200	200	200
	4	947	930	6134	2686	2160	1900
	8	7181	6604	8713	6300	5361	4027
	16	13330	13092	12135	9380	11196	10698
	32	14583	14321	15911	14829	15932	14838
3mm	2	1000	1000	7399	800	1000	1000
	4	38722	37899	16771	7653	9264	8634
	8	58129	49559	24330	9832	28121	24270
	16	64384	60127	37041	31036	39683	37194
	32	62279	58190	46437	43062	48567	43210
adi	2	134945	134675	142719	142174	143067	139672
	4	284666	283892	212938	211939	215399	214945
	8	290823	290672	271949	266349	256302	255522
	16	326963	326923	300755	292351	282485	281511
	32	370876	370413	324494	316241	306075	305411
atax	2	47826	47424	44942	38679	39876	39876
	4	82397	76245	60187	47184	48645	48645
	8	113410	111051	63353	51580	51243	50419
	16	127687	125146	70723	62697	59208	57085
	32	132092	130854	78264	67401	69556	63166
covariance	2	66520	66445	27269	4775	55195	17183
	4	84626	84213	82125	61793	61991	34307
	8	103710	102425	136946	122656	74325	50680
	16	125816	123276	142177	123221	119284	106422
	32	142214	137905	121155	103751	133522	117431
doitgen	2	43807	42208	5035	3000	5947	5947
	4	72115	71072	37767	22290	37051	31157
	8	76977	75114	51283	43572	53244	50795
	16	84203	77436	62296	56650	66483	64488
	32	94135	92739	68350	62576	74786	70168
durbin	2	12997	12997	12997	12997	12997	12997
	4	21641	21641	21572	21572	21566	21566
	8	27571	27571	27519	27518	27520	27520
	16	32865	32865	32852	32848	32912	32912
	32	39726	39725	39738	39732	39826	39826
fdtd-2d	2	5494	5494	6264	6003	6024	5896
	4	15100	15099	15294	13199	16965	16674
	8	33087	32355	23699	21886	35711	34361
	16	35714	35239	32917	30725	44643	43608
	32	43961	42507	42515	41258	53658	52420
gemm	2	383084	382433	4200	4200	44549	44549
	4	507250	500526	168962	12600	59854	46677
	8	578951	575004	183228	36273	116990	96059
	16	615342	613373	294777	241136	263050	238125
	32	626472	623271	330937	307225	332946	299774
gemver	2	29349	29270	26368	22824	20913	20913
	4	49361	49229	45689	38663	40299	40185
	8	68163	67094	56930	49776	55266	53759
	16	78115	75596	62143	57779	59072	56598
	32	85331	84865	75425	68673	73131	71349
gesummv	2	1666	500	24762	500	500	500
	4	98542	94493	24613	1783	10316	8710
	8	101533	98982	25342	13522	9618	9397
	16	112064	104866	37819	21155	35686	30954
	32	117752	114812	48775	42523	45050	40671
heat-3d	2	8695	8684	10165	9648	9378	9225
	4	14592	14592	17093	16321	16700	16424
	8	20608	20608	28388	25862	25883	25470
	16	31615	31500	47612	46825	42137	41261
	32	51963	50758	64614	62894	70462	69439

TABLE A.2

Comparing the edge cuts obtained by CoHyb_CIP and CoTop with those obtained by the evolutionary algorithm of Moreira, Popp, and Schulz on the Polyhedral Benchmark Suite (second set of results). The last line (Geomean) is for the whole PolyBench dataset (i.e., computed by combining this table with the previous one), where the performance of the algorithms are normalized with respect to the average values shown under the column Moreira, Popp, and Schulz.

Graph	k	Moreira, Popp, and Schulz [21]		CoHyb_CIP		CoTop	
		Average	Best	Average	Best	Average	Best
jacobi-1d	2	596	596	646	472	682	660
	4	1493	1492	1617	1272	1789	1756
	8	3136	3136	2845	2560	3431	3216
	16	6340	6338	4519	3841	5089	4872
	32	8923	8750	6742	6026	6883	6634
jacobi-2d	2	2994	2991	4327	4002	3445	3342
	4	5701	5700	8405	7379	7370	7247
	8	9417	9416	14872	13802	13168	12895
	16	16274	16231	22626	21625	21565	21098
	32	22181	21758	30423	28911	29558	28979
lu	2	5210	5162	5351	4160	6085	6039
	4	13528	13510	21258	13141	22979	16959
	8	33307	33211	53643	44342	57437	49080
	16	74543	74006	105289	96617	108189	102868
	32	130674	129954	156187	147852	164737	158621
ludcmp	2	5380	5337	5731	5337	6942	5339
	4	14744	14744	25247	19339	22368	22065
	8	37228	37069	60298	50208	60255	50101
	16	78646	78467	106223	98324	109920	99798
	32	134758	134288	158619	151063	165018	155120
mvt	2	24528	23091	57216	33263	21281	19792
	4	74386	73035	55679	36564	38215	35788
	8	86525	82221	62453	47771	46776	43724
	16	99144	97941	71650	59399	54925	48385
	32	105066	104917	83635	79030	62584	60389
seidel-2d	2	4991	4969	4374	3401	4772	4638
	4	12197	12169	13177	12553	11784	11485
	8	21419	21400	24396	22452	21937	21619
	16	38222	38110	38065	35777	39747	38831
	32	52246	51531	58319	57012	59278	57885
symm	2	94357	94214	26374	24629	43597	43330
	4	127497	126207	59815	49450	85730	78379
	8	152984	151168	91892	75126	118259	111126
	16	167822	167512	105418	96322	135278	131127
	32	174938	174843	108950	99584	145903	141223
syr2k	2	11098	3894	4343	900	16124	14404
	4	49662	48021	12192	3121	22915	17959
	8	57584	57408	29194	24912	28787	27259
	16	59780	59594	29519	26327	31807	29132
	32	60502	60085	36111	34079	36689	35155
syrk	2	219263	218019	76767	3240	11740	9036
	4	289509	289088	72148	9995	56832	34893
	8	329466	327712	112236	66981	121664	109730
	16	354223	351824	179042	172076	184437	170781
	32	362016	359544	196173	186162	224330	213676
trisolv	2	6788	3549	367	280	336	336
	4	43927	43549	38148	1277	828	828
	8	66148	65662	20163	9364	2156	2156
	16	71838	71447	20421	12847	6240	5881
	32	79125	79071	25279	19949	13431	13172
trmm	2	138937	138725	50057	32720	13659	3440
	4	192752	191492	58477	16617	72276	35000
	8	225192	223529	92185	58957	134574	102693
	16	240788	238159	128838	122111	157277	145934
	32	246407	245173	153644	147551	171562	158113
Geomean		1.00	0.96	0.74	0.50	0.63	0.57

TABLE A.3
The partition balances for the edge cuts given in Table A.1.

Graph	k	CoHyb_CIP		CoTop	
		Average	Best	Average	Best
2mm	2	1.001	1.001	1.001	1.001
	4	1.028	1.030	1.024	1.001
	8	1.030	1.030	1.030	1.030
	16	1.029	1.030	1.030	1.030
	32	1.030	1.030	1.030	1.030
3mm	2	1.021	1.009	1.017	1.017
	4	1.027	1.030	1.030	1.030
	8	1.030	1.030	1.030	1.030
	16	1.030	1.030	1.030	1.030
	32	1.030	1.030	1.030	1.030
adi	2	1.000	1.000	1.030	1.030
	4	1.030	1.030	1.030	1.029
	8	1.030	1.030	1.030	1.030
	16	1.030	1.030	1.030	1.030
	32	1.030	1.030	1.030	1.030
atax	2	1.010	1.011	1.030	1.030
	4	1.020	1.030	1.030	1.030
	8	1.027	1.016	1.029	1.030
	16	1.029	1.030	1.030	1.030
	32	1.030	1.030	1.030	1.030
covariance	2	1.022	1.023	1.030	1.030
	4	1.026	1.021	1.030	1.030
	8	1.028	1.030	1.030	1.030
	16	1.029	1.030	1.030	1.030
	32	1.030	1.030	1.030	1.030
doitgen	2	1.003	1.000	1.030	1.030
	4	1.030	1.030	1.030	1.030
	8	1.030	1.030	1.030	1.030
	16	1.030	1.030	1.030	1.030
	32	1.030	1.030	1.030	1.030
durbin	2	1.024	1.024	1.024	1.024
	4	1.018	1.018	1.023	1.023
	8	1.020	1.020	1.028	1.028
	16	1.028	1.028	1.030	1.030
	32	1.030	1.029	1.030	1.030
fdtd-2d	2	1.007	1.000	1.006	1.000
	4	1.023	1.026	1.021	1.025
	8	1.026	1.028	1.027	1.024
	16	1.027	1.027	1.029	1.028
	32	1.029	1.030	1.028	1.029
gemm	2	1.010	1.008	1.029	1.029
	4	1.024	1.025	1.030	1.030
	8	1.029	1.028	1.029	1.027
	16	1.030	1.030	1.027	1.030
	32	1.030	1.030	1.030	1.030
gemver	2	1.008	1.000	1.000	1.000
	4	1.030	1.030	1.029	1.030
	8	1.029	1.025	1.030	1.029
	16	1.029	1.029	1.030	1.030
	32	1.030	1.030	1.030	1.030
gesummv	2	1.014	1.010	1.022	1.022
	4	1.026	1.013	1.030	1.030
	8	1.028	1.027	1.027	1.030
	16	1.029	1.029	1.030	1.030
	32	1.030	1.030	1.030	1.030
heat-3d	2	1.008	1.030	1.030	1.030
	4	1.030	1.030	1.030	1.030
	8	1.020	1.016	1.030	1.030
	16	1.024	1.022	1.030	1.030
	32	1.030	1.028	1.030	1.030

TABLE A.4

The partition balances for the edge cuts given in Table A.2. The last three lines (Min, Average, Max) are for the whole PolyBench dataset (i.e., computed by combining this table with the previous one).

Graph	k	CoHyb_CIP		CoTop	
		Average	Best	Average	Best
jacobi-1d	2	1.009	1.010	1.016	1.006
	4	1.019	1.027	1.016	1.022
	8	1.016	1.006	1.024	1.028
	16	1.025	1.024	1.024	1.024
	32	1.027	1.027	1.028	1.028
jacobi-2d	2	1.027	1.030	1.028	1.030
	4	1.017	1.012	1.029	1.030
	8	1.027	1.027	1.030	1.030
	16	1.027	1.028	1.030	1.030
	32	1.029	1.028	1.030	1.030
lu	2	1.023	1.003	1.030	1.030
	4	1.027	1.030	1.029	1.027
	8	1.030	1.030	1.030	1.030
	16	1.030	1.030	1.030	1.030
	32	1.030	1.030	1.030	1.030
ludcmp	2	1.020	1.020	1.022	1.020
	4	1.027	1.030	1.030	1.030
	8	1.030	1.030	1.030	1.030
	16	1.030	1.030	1.030	1.030
	32	1.030	1.030	1.030	1.030
mvt	2	1.020	1.028	1.024	1.030
	4	1.021	1.015	1.028	1.021
	8	1.025	1.030	1.029	1.021
	16	1.028	1.030	1.029	1.030
	32	1.029	1.030	1.030	1.030
seidel-2d	2	1.012	1.011	1.016	1.008
	4	1.024	1.022	1.028	1.025
	8	1.026	1.030	1.030	1.030
	16	1.029	1.029	1.030	1.030
	32	1.029	1.028	1.030	1.030
symm	2	1.016	1.030	1.030	1.030
	4	1.021	1.019	1.030	1.030
	8	1.027	1.029	1.030	1.030
	16	1.030	1.030	1.030	1.030
	32	1.030	1.030	1.030	1.030
syr2k	2	1.018	1.016	1.026	1.000
	4	1.029	1.030	1.020	1.029
	8	1.030	1.027	1.029	1.030
	16	1.030	1.030	1.027	1.021
	32	1.030	1.030	1.030	1.030
syrk	2	1.021	1.022	1.024	1.026
	4	1.030	1.030	1.028	1.030
	8	1.029	1.027	1.030	1.030
	16	1.030	1.030	1.030	1.030
	32	1.030	1.030	1.030	1.030
trisolv	2	1.012	1.021	1.027	1.027
	4	1.026	1.028	1.020	1.020
	8	1.028	1.030	1.026	1.026
	16	1.030	1.030	1.030	1.030
	32	1.030	1.030	1.030	1.030
trmm	2	1.028	1.024	1.016	1.010
	4	1.027	1.021	1.030	1.030
	8	1.030	1.030	1.030	1.030
	16	1.030	1.030	1.030	1.030
	32	1.030	1.030	1.030	1.030
		Min	1.000	1.000	1.000
		Average	1.025	1.025	1.027
		Max	1.030	1.030	1.030

REFERENCES

- [1] K. AGRAWAL, J. T. FINEMAN, J. KRAGE, C. E. LEISERSON, AND S. TOLEDO, *Cache-conscious scheduling of streaming applications*, in Proceedings of the Twenty-Fourth Annual ACM Symposium on Parallelism in Algorithms and Architectures, SPAA'12, 2012, pp. 236–245.
- [2] T. N. BUI AND C. JONES, *A heuristic for reducing fill-in in sparse matrix factorization*, in Proceedings of the Sixth SIAM Conference on Parallel Processing for Scientific Computing, SIAM, Philadelphia, 1993, pp. 445–452.
- [3] Ü. V. ÇATALYÜREK AND C. AYKANAT, *PaToH: A Multilevel Hypergraph Partitioning Tool, Version 3.0*, Department of Computer Engineering, Bilkent University, Ankara, Turkey, 1999; available online from <http://cc.gatech.edu/~umit/software.html>.
- [4] T. F. COLEMAN AND W. XU, *Parallelism in structured Newton computations*, in Parallel Computing: Architectures, Algorithms and Applications, ParCo'07, Forschungszentrum Jülich and RWTH Aachen University, Jülich and Aachen, Germany, 2007, pp. 295–302.
- [5] T. F. COLEMAN AND W. XU, *Fast (structured) Newton computations*, SIAM J. Sci. Comput., 31 (2008), pp. 1175–1191, <https://doi.org/10.1137/070701005>.
- [6] T. F. COLEMAN AND W. XU, *Automatic Differentiation in MATLAB Using ADMAT with Applications*, SIAM, Philadelphia, 2016, <https://doi.org/10.1137/1.9781611974362>.
- [7] J. CONG, Z. LI, AND R. BAGRODIA, *Acyclic multi-way partitioning of Boolean networks*, in Proceedings of the 31st ACM/IEEE Design Automation Conference, DAC'94, 1994, pp. 670–675.
- [8] T. A. DAVIS AND Y. HU, *The University of Florida sparse matrix collection*, ACM Trans. Math. Software, 38 (2011), 1.
- [9] E. D. DOLAN AND J. J. MORÉ, *Benchmarking optimization software with performance profiles*, Math. Program., 91 (2002), pp. 201–213.
- [10] V. ELANGO, F. RASTELLO, L.-N. POUCHET, J. RAMANUJAM, AND P. SADAYAPPAN, *On characterizing the data access complexity of programs*, in Proceedings of the 42nd Annual ACM SIGPLAN-SIGACT Symposium on Principles of Programming Languages, pp. 567–580.
- [11] N. FAUZIA, V. ELANGO, M. RAVISHANKAR, J. RAMANUJAM, F. RASTELLO, A. ROUNTEV, L.-N. POUCHET, AND P. SADAYAPPAN, *Beyond reuse distance analysis: Dynamic analysis for characterization of data locality potential*, ACM Trans. Archit. Code Optim., 10 (2013), 53.
- [12] C. M. FIDUCCIA AND R. M. MATTHEYES, *A linear-time heuristic for improving network partitions*, in Proceedings of the 19th ACM/IEEE Design Automation Conference, DAC'82, 1982, pp. 175–181.
- [13] M. R. GAREY AND D. S. JOHNSON, *Computers and Intractability: A Guide to the Theory of NP-Completeness*, W. H. Freeman, New York, 1979.
- [14] B. HENDRICKSON AND R. LELAND, *The Chaco User's Guide, Version 1.0*, Tech. Report SAND93-2339, Sandia National Laboratories, Albuquerque, NM, 1993.
- [15] J. HERRMANN, J. KHO, B. UÇAR, K. KAYA, AND Ü. V. ÇATALYÜREK, *Acyclic partitioning of large directed acyclic graphs*, in Proceedings of the 17th IEEE/ACM International Symposium on Cluster, Cloud and Grid Computing, CCGRID'17, 2017, pp. 371–380.
- [16] G. KARYPIS AND V. KUMAR, *MeTiS: A Software Package for Partitioning Unstructured Graphs, Partitioning Meshes, and Computing Fill-Reducing Orderings of Sparse Matrices Version 4.0*, Department of Computer Science and Engineering, Army HPC Research Center, University of Minnesota, Minneapolis, MN, 1998.
- [17] B. W. KERNIGHAN, *Optimal sequential partitions of graphs*, J. Assoc. Comput. Mach., 18 (1971), pp. 34–40.
- [18] B. W. KERNIGHAN AND S. LIN, *An efficient heuristic procedure for partitioning graphs*, Bell Syst. Tech. J., 49 (1970), pp. 291–307.
- [19] M. R. B. KRISTENSEN, S. A. F. LUND, T. BLUM, AND J. AVERY, *Fusion of parallel array operations*, in Proceedings of the 2016 ACM International Conference on Parallel Architectures and Compilation, 2016, pp. 71–85.
- [20] M. R. B. KRISTENSEN, S. A. F. LUND, T. BLUM, K. SKOVHEDE, AND B. VINTER, *Bohrium: A virtual machine approach to portable parallelism*, in Proceedings of the IEEE International Parallel & Distributed Processing Symposium Workshops, IPDPSW'14, 2014, pp. 312–321.
- [21] O. MOREIRA, M. POPP, AND C. SCHULZ, *Graph partitioning with acyclicity constraints*, in 16th International Symposium on Experimental Algorithms, Schloss Dagstuhl–Leibniz-Zentrum für Informatik, Wadern, Germany, 2017.
- [22] O. MOREIRA, M. POPP, AND C. SCHULZ, *Evolutionary multi-level acyclic graph partitioning*, in Proceedings of the ACM Genetic and Evolutionary Computation Conference, GECCO'18, 2018, pp. 332–339.

- [23] J. NOSSACK AND E. PESCH, *A branch-and-bound algorithm for the acyclic partitioning problem*, Comput. Oper. Res., 41 (2014), pp. 174–184.
- [24] C. H. PAPADIMITRIOU AND K. STEIGLITZ, *Combinatorial Optimization: Algorithms and Complexity*, Dover, New York, 1998.
- [25] F. PELLEGRINI, *SCOTCH 5.1 User's Guide*, Laboratoire Bordelais de Recherche en Informatique (LaBRI), Talence, France, 2008.
- [26] L.-N. POUCHET, *PolyBench/C: The Polyhedral Benchmark Suite*, <http://web.cse.ohio-state.edu/~pouchet/software/polybench/>, 2012.
- [27] P. SANDERS AND C. SCHULZ, *Engineering multilevel graph partitioning algorithms*, in Algorithms—ESA 2011, Springer, Berlin, Heidelberg, pp. 469–480.
- [28] C. WALSHAW, *Multilevel refinement for combinatorial optimisation problems*, Ann. Oper. Res., 131 (2004), pp. 325–372.
- [29] E. S. H. WONG, E. F. Y. YOUNG, AND W. K. MAK, *Clustering based acyclic multi-way partitioning*, in Proceedings of the 13th ACM Great Lakes Symposium on VLSI, GLSVLSI '03, 2003, pp. 203–206.