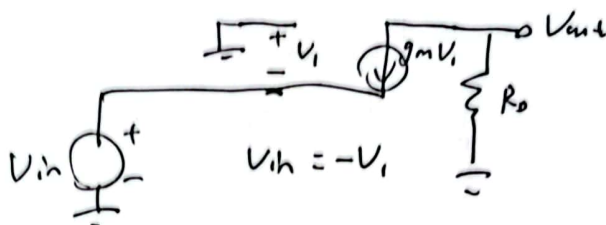
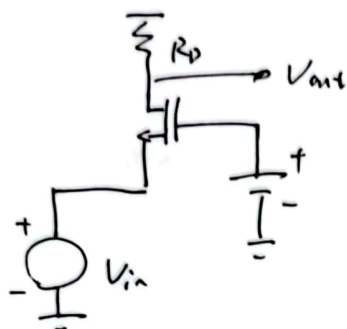
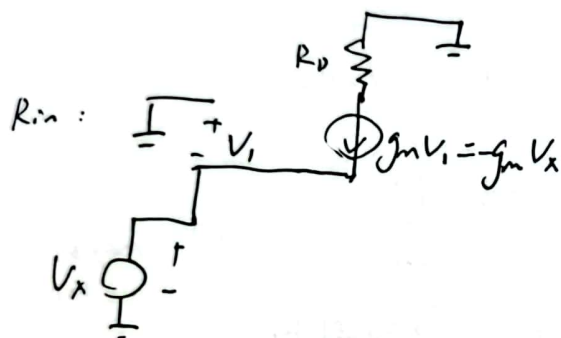




Small-Signal Properties ($\lambda=0$)

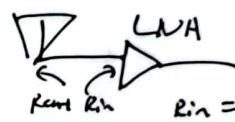


$$\Rightarrow \frac{V_{out}}{R_D} = -g_m V_i = g_m V_{in} \Rightarrow A_v = g_m R_D$$

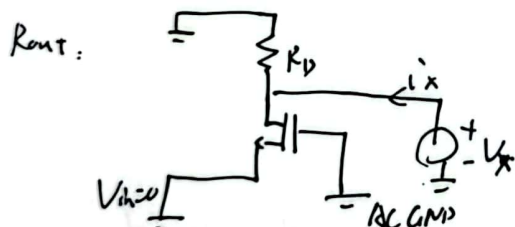


$$i_x - g_m V_x = 0 \Rightarrow R_{in} = \frac{1}{g_m} \text{ 低阻抗}$$

Application:



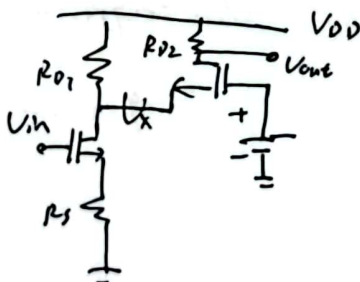
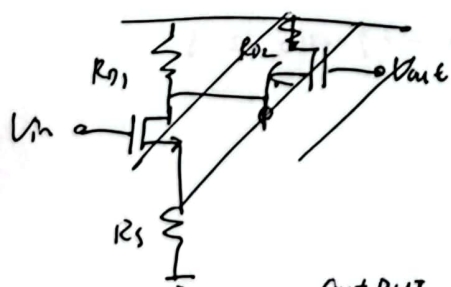
可用于阻抗匹配。如负载
电阻为 50Ω 时， $R_{in} = 50\Omega$ 。



$$R_{out} = R_D$$

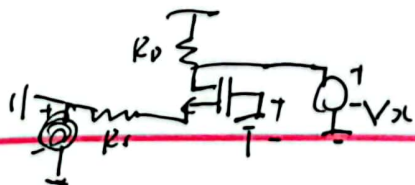
R fixed set AC ground and R_{in}
 $\frac{1}{g_m} + 5 \text{ and } ACG$

EXAMPLE ($\lambda=0$)



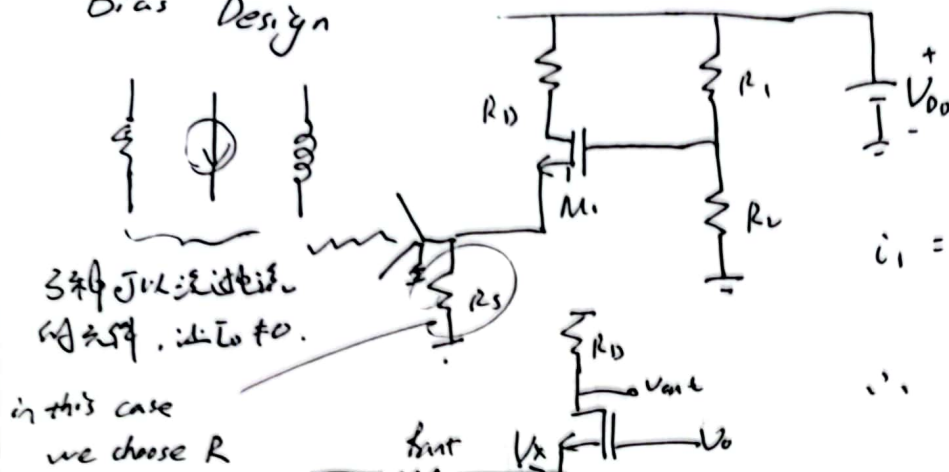
$$\begin{aligned} \frac{V_{out}}{V_{in}} &= \left(\frac{V_x}{V_{in}} \right) \cdot \frac{V_{out}}{V_x} \\ &= - \frac{R_{G1} \parallel \frac{1}{g_{m1}}}{\frac{1}{g_{m1}} + R_S} g_{m2} R_{D2} \end{aligned}$$

Output resistance of CG Stage in a special case $\lambda \neq 0$



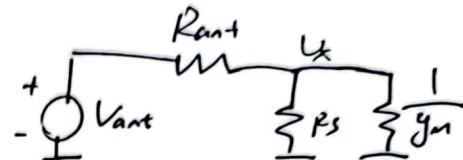
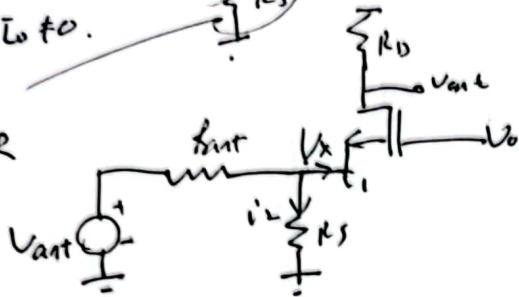
$$R_{out} = R_D \parallel \left[(1 + g_m r_o) R_S + r_o \right]$$

Bias Design



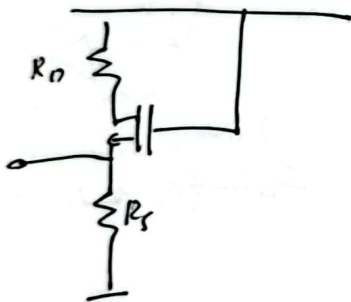
$$i_1 = \frac{R_2}{R_2 + \frac{1}{g_m}} i_{ant}$$

$\therefore R_2 \gg \frac{1}{g_m}$ is better



$$\frac{V_{out}}{V_{ant}} = \frac{V_x}{V_{ant}} \cdot \frac{V_{out}}{V_x} = \frac{R_2 \parallel \frac{1}{g_m}}{R_{ant} + R_2 \parallel \frac{1}{g_m}} \cdot g_m R_D$$

Example



$$R_{smax} = \frac{V_{DD} - V_{GS}}{I_D} = 370 \Omega. \text{ maybe not sat. fy } R_S \gg \frac{1}{g_m}$$

To ensure sat. $I_D R_D \leq V_{TH}$.

choose $R_D = 500 \Omega$

$$A_v = g_m R_D = 1.6$$

if double $\frac{W}{L}$, $A_v \uparrow$, but the speed \downarrow

$$V_{GS} = 0.95V \Rightarrow R_S = \frac{V_{DD} - V_{GS}}{I_D} = 55 \Omega$$

$$g_m = 4.5 \text{ mS} \Rightarrow g_m R_D = 2.3$$