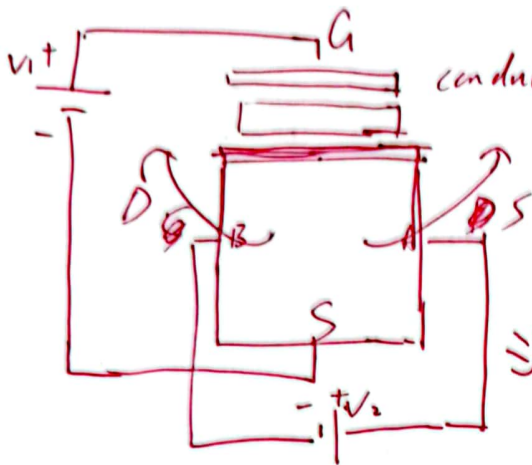


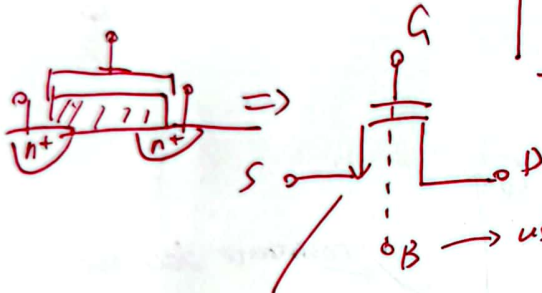
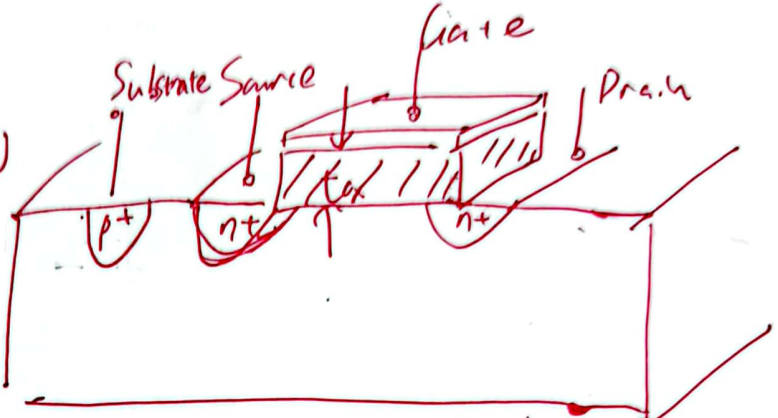


## Introduction



conductive plate

在图中上不可见的由导线引出故连到顶部



indicates the source

usually hide,

because its usually determined.

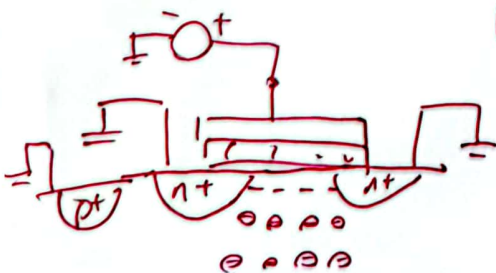
对S.S.的, substrate区可互连

$t_{ox} \downarrow \Rightarrow C \uparrow \Rightarrow O \uparrow \Rightarrow \text{elec loss}$

## Mos Operation

$V_{TH}$  ~~是~~ The voltage at which we introduce electrons instead of the negative ions.

Depletion: 耗尽 空穴



$V_G = V_{TH}$  时出现 channel

$V_G > V_{TH}$  时 S — G — D

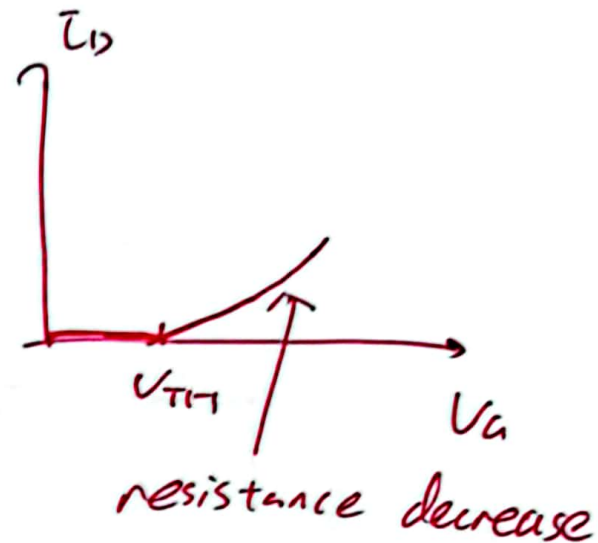
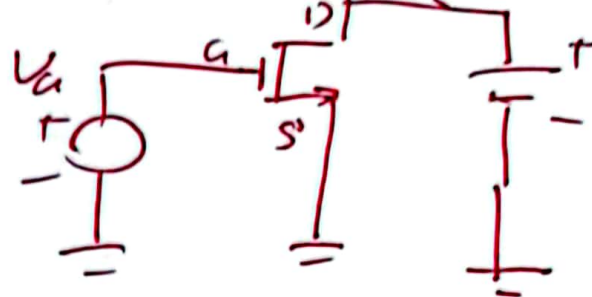
电压控制电阻  
Voltage controlled resistor

Assume  $V_D > V_S = 0$



两个变量  
控制变量法

①  $V_D = \text{constant}$  eg. 0.3V



②  $V_G = \text{constant}$  eg. 1V

