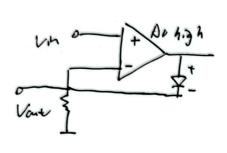
The but.

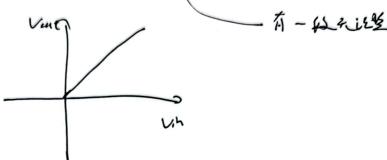


· Precision Reclifier

simple Rodifier

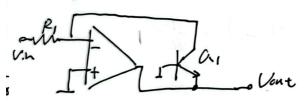
Vo. on Uh



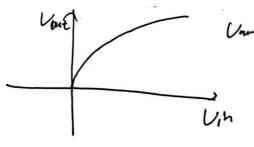


onsure un & Vant => lan Vih still can be reclifiered

· Logerithmic Amplufier



Un = Ici = Is exp UBE UBE = - Vont



Unic = VIla Vila

If Vinco.

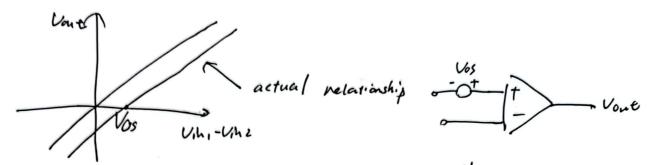
In Optomp sat.

not defined.

LO AMP.

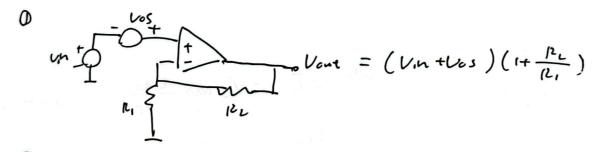
·Dc offets.

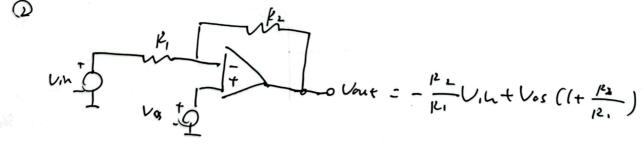
some imperfections of Op Amp.



Observations:

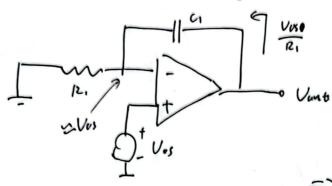
- 1 Vos is random
- 12) Vos can be placed in series with either input
- SEffect of Offset on Amplifiers







Effect of DC uttsets on Liregrators



To fix the chait:

Vart = (1+ 12) Vos

· Vant Vos prefers to

Vant — supply voltage

flow throwke nather than ((at t= x)

I deal Integrator Fransfer Function: - 1

New Integrator:

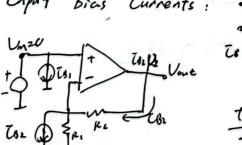
$$\frac{V_{\text{out}}}{V_{\text{in}}} \simeq -\frac{\frac{P_2}{P_2C_1S+1}}{P_1} = -\frac{P_2}{1^{2_1}(|2_2C_1S+1|)}$$

To be a good Integrator, the circuit

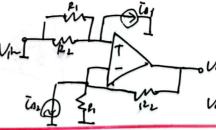
requires that (REGS)>>10

这的未没好走,如也以得在流流过,但是又想让爱无概念,扩 西山 在流文之区台明整一点,所以共享通信。

Enpar Bias Currents:



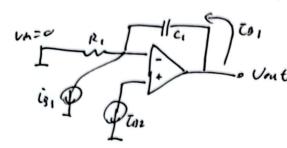
四大的这处十一丝心惊丹也没



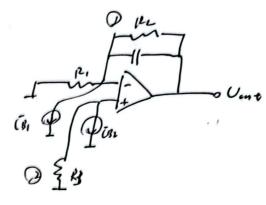
Vent = - [s, (HIPE)(HE)

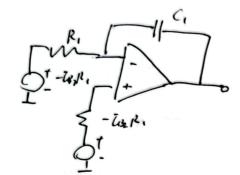
中国·杭州 HANGZHOU CHINA

Bias Cameros



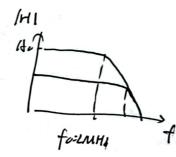






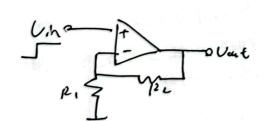
- Add & for sia cuments.
- Add R3 , + have the same bias.

Speed Lihitations.

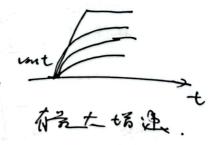


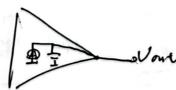
Vant
$$V_{ih}(s) = \frac{Ac}{(+ \frac{S}{4a})}$$

$$\frac{Ab}{(+ \frac{S}{4a})} \frac{R_{i}}{R_{i}+R_{i}} + 1$$









· 一 作州 HANGZHOU CHINA