

# Common-Source Stage with Degeneration

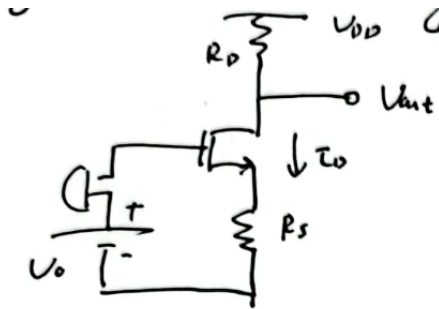
zrrraa

2023.11.22

## Degenerated CS Stage

We assume the  $\lambda = 0$ .

### Step I: Bias Conditions



$$V_D = V_{GS} + I_D R_S$$

For saturation zone,  $V_{DS} \geq V_{GS} - V_{TH}$ .

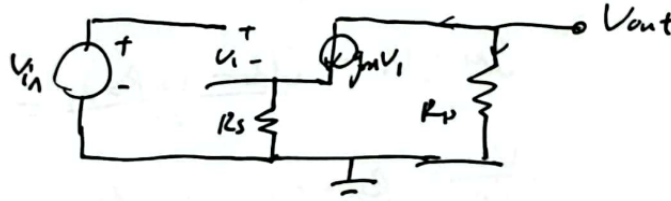
$$I_D = \frac{1}{2} \mu_n C_{ox} \frac{W}{L} (V_{GS} - V_{TH})^2$$

Drain voltage =  $V_{DD} - I_D R_D$

### Step II: Gain, I/O Impedances

$$g_m v_1 = -\frac{v_{out}}{R_D}$$

$$v_{in} = V_1 + g_m v_1 * R_S$$

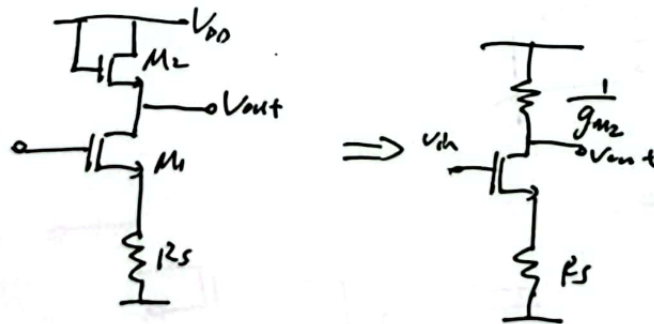


$$\Rightarrow \frac{v_{out}}{v_{in}} = -\frac{R_D}{\frac{1}{g_m} + R_s}$$

Therefore:

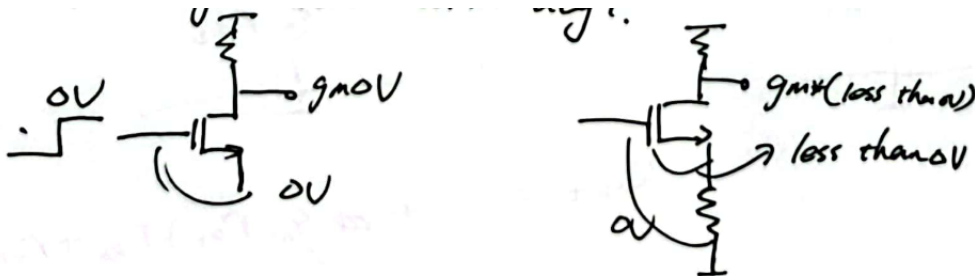
$$A_v = -\frac{\text{Resistance tied between Drain and AC Ground}}{\frac{1}{g_m} + \text{Resistance tied between Source and AC Ground}}$$

**Example**



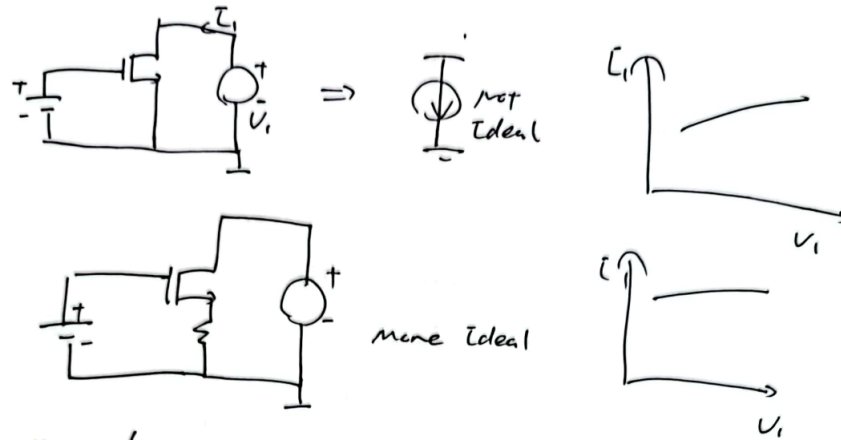
$$A_v = -\frac{\frac{1}{g_{m2}}}{\frac{1}{g_{m1}} + R_s}$$

So why is gain less with degenerated CS stage?



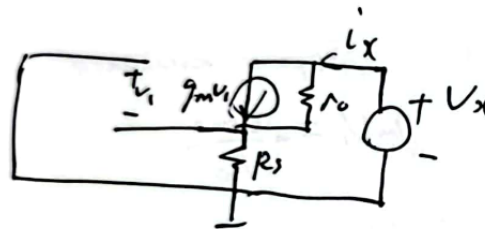
From the picture we can easily see the change of voltage between Gate and Source is less with deg.  
So the gain is less.

## Let's build a current source



We draw the I/V relationship between Drain and Source, it's clear that the CS stage with degeneration is more smooth. Or we can call it more ideal, as a current source. That's the meaning of degeneration.

## Small-Signal Imp



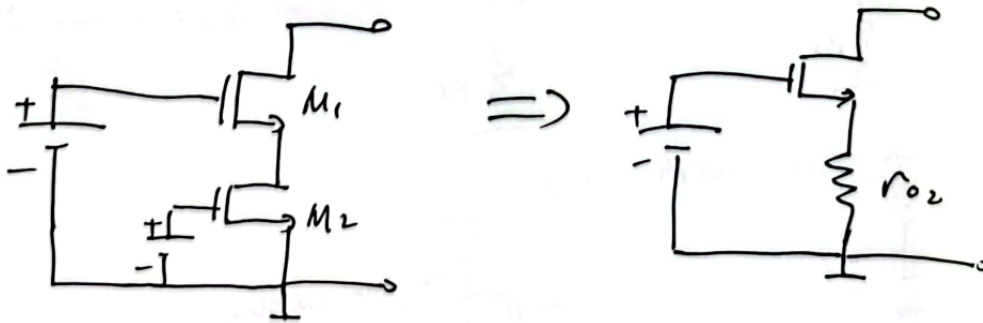
Calculate the output impedance.

$$i_x R_S = -v_1$$

$$g_m v_1 + \frac{v_x - i_x R_S}{r_o} = i_x$$

$$\Rightarrow \frac{v_x}{i_x} = R_S + r_o + g_m R_S r_o = (1 + g_m r_o) R_S + r_o$$

## Example



$$R_{out} = (1 + g_m r_{o1}) r_{o2} + r_{o1}$$

This structure has a special name: Cascode. We will learn it after.

## Link

[Razavi Electronics Circuits 1: lecture 38](#)