

Cluster State Quantum Computing

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Abstract— Any quantum computation can be performed via sequences of one-qubit measurements on a specific type of initially entangled state – the *cluster state*. Each computational step is a projective measurement that destroys a quantum state, leaving a final state that relies on the outcomes of earlier computations. The model of interest is the one-way quantum computer which is based on this measurement scheme. This paper will present background regarding computation using only measurements, a brief introduction into the preparation of cluster states, a discussion of one way quantum computers (1WQC), and the computational power of various configurations of a 1WQC.

I. BACKGROUND

Over the past few decades, advances in science and technology have greatly contributed to the development of modern computers. While these computers are efficient and convenient for everyday needs, they fail at certain computational tasks. Instead, quantum computers promise faster large scale factorization and database searches that are intractable for their classical counterparts. The first quantum computer designs were based off of classical models; sequences of one- and multi-qubit gate operations are performed on chosen quantum bits and a final measurement would convert quantum information into classical bits. A new model, proposed by Briegel and Raussendorf [1], demonstrates that quantum computation can be achieved by using single qubit measurements as computational steps. This so-called cluster model or *one-way quantum computer (1WQC)* relies on an entangled state of a large number of qubits or *cluster state* as the resource. The fascinating feature about 1WQC is that they have no classical analogues and probe into new territory in regards to entanglement and measurements.

A. Cluster States

Consider a set of qubits \mathcal{C} labeled by an integer index, that are distributed in some lattice such that every qubit can be said to have adjacent neighbors. For these to collectively form a cluster state, their quantum mechanical state would be characterized by the set of eigenvalue equations [2],

$$K_a |\Phi\rangle_{\mathcal{C}} = \kappa |\Phi\rangle_{\mathcal{C}} \quad (1)$$

for a family of operators $K_a = X^{(a)} \bigotimes_{\gamma \in \Gamma(a)} Z^{(\gamma)}$, $a \in \mathcal{C}$, where $\Gamma(a)$ is the set of indices of all qubits in the “adjacent neighborhood” of a . The matrix $X^{(a)}$ is used to denote an X operation on qubit- a , and so on. The eigenvalue $\kappa = \pm 1$ is determined by the specific occupation pattern of the neighboring sites.

A.1 Preparation of Linear Cluster State

Intuitively, a cluster state can be thought of as a graph where every vertex represents a qubit, and every edge represents the application of a C_z gate to both adjacent vertices.

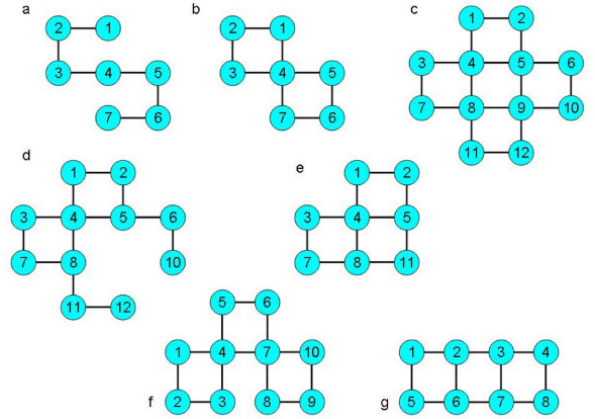
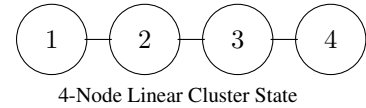


Fig. 1. Figure from [5], showing representative 2-D cluster shapes. The vertices are qubits with integer indices, and the edges indicate entanglement connectivity between select neighbors.

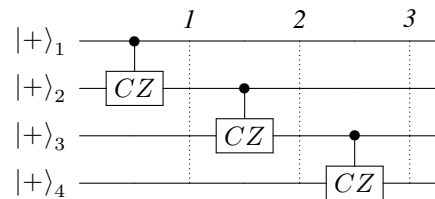
A cluster state can be represented as a graph $G = (N, E)$, where the $n \in N$ is a qubit and $e \in E$ is the application of a Controlled-Z (C_z) gate, where:

$$C_z = \begin{pmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & -1 \end{pmatrix}$$

A *linear* cluster state is one where $\text{degree}(n) \leq 2 \forall n \in N$.



A method to prepare such a cluster state is given in [3], consisting of “cascading” C_z gates on n qubits as follows:



We can then analyze the state of the qubits at each of the dotted lines:

1:

$$\begin{aligned} & \left(\frac{|0\rangle_1 |+\rangle_2 + |1\rangle_1 |-\rangle_2}{\sqrt{2}} \right) |+\rangle_3 |+\rangle_4 \\ & \equiv \left(\frac{|+\rangle_1 |0\rangle_2 + |-\rangle_1 |1\rangle_2}{\sqrt{2}} \right) |+\rangle_3 |+\rangle_4 \end{aligned}$$

2:

$$\begin{aligned} & \left(\frac{|+\rangle_1 |0\rangle_2 |+\rangle_3 + |-\rangle_1 |1\rangle_2 |-\rangle_3}{\sqrt{2}} \right) |+\rangle_4 \\ & \equiv \frac{1}{\sqrt{2}} \left(\frac{|0\rangle_1 |+\rangle_2 + |1\rangle_1 |-\rangle_2}{\sqrt{2}} |0\rangle_3 + \frac{|0\rangle_1 |-\rangle_2 + |1\rangle_1 |+\rangle_2}{\sqrt{2}} |1\rangle_3 \right) |+\rangle_4 \end{aligned}$$

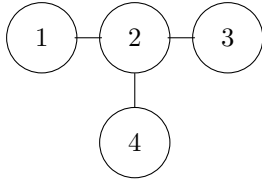
3:

$$\begin{aligned} & \left(\frac{|+\rangle_1 |0\rangle_2 + |-\rangle_1 |1\rangle_2}{\sqrt{2}} |0\rangle_3 + \frac{|+\rangle_1 |0\rangle_2 - |-\rangle_1 |1\rangle_2}{\sqrt{2}} |1\rangle_3 \right) |-\rangle_4 \\ & \equiv \frac{[(|+\rangle_1 |0\rangle_2 + |-\rangle_1 |1\rangle_2) |0\rangle_3 |+\rangle_4 + (|+\rangle_1 |0\rangle_2 - |-\rangle_1 |1\rangle_2) |1\rangle_3 |-\rangle_4]}{2} \end{aligned}$$

The action of the C_z gate in the computational basis can be seen to be $|x, y\rangle \rightarrow (-1)^{xy} |x, y\rangle$. Cluster states of arbitrary shape and connectivity can similarly be prepared via the recursive use of the Hadamard gate and two-qubit fusion operations [4], [5].

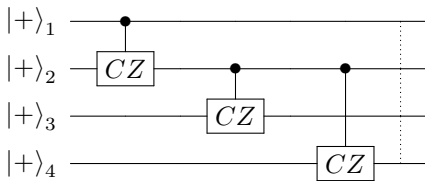
A.2 Preparation of T-Shaped Cluster State

A cluster state without the limitation on the degree of a node allows us to build *nonlinear* cluster states:



4-Node T-Shaped Cluster State

The circuit creating this cluster state will look as follows:



The state of the qubits after the application of the first two C_z gates is identical to the linear case, and the state after the last C_z (at the dotted line) is given by:

$$\begin{aligned} & \frac{|+\rangle_1 |0\rangle_2 |+\rangle_3 |+\rangle_4 + |-\rangle_1 |1\rangle_2 |-\rangle_3 |-\rangle_4}{\sqrt{2}} \\ & \equiv \frac{1}{\sqrt{2}} \left[\left(\frac{|+\rangle_1 |0\rangle_2 |+\rangle_3 + |-\rangle_1 |1\rangle_2 |-\rangle_3}{\sqrt{2}} \right) |0\rangle_4 \right. \\ & \quad \left. + \left(\frac{|+\rangle_1 |0\rangle_2 |+\rangle_3 - |-\rangle_1 |1\rangle_2 |-\rangle_3}{\sqrt{2}} \right) |1\rangle_4 \right] \end{aligned}$$

It is important to emphasize that the order in which the C_z gates are applied to grow the cluster state is irrelevant, as all of these pair-wise operations commute. This feature will be exploited later when discussing parallelizability.

B. The Effects of Measurement on a Cluster State

As is clear from the form of the expressions of all cluster states illustrated thus far, measuring any node in the computational basis severs it from the remaining graph by cutting all of its edges with its neighboring nodes. Should the outcome of said measurement be 1, then a Z gate/transform gets applied to all of its erstwhile neighbors in the leftover cluster state. Thus, a large cluster state can be arbitrarily trimmed, split, and/or reshaped by removing qubits from the cluster. This is accomplished by measuring the target qubit in the computational basis, and performing appropriate unitary rotations on its former neighbors based on the measurement outcome.

The effect of an X -measurement (*i.e.*, a computational basis measurement following a Hadamard transformation) on any node of the cluster state is much more involved. This will be explored in detail in the following subsection. I could explain how a linear CL can function as a wire. Don't know if it should go here or elsewhere.

Briegel and Raussendorf show that any quantum logic circuit can be implemented on a cluster state, which demonstrates universality of the proposed scheme [1]. Nielsen [6] extended this result to no longer require coherent dynamics, instead relying on a method to teleport quantum gates, and he provided a concise algorithm to accomplish this.

C. One-Way Quantum Computation

All quantum computation schemes may be characterized by some combination of state preparation, unitary transformation of said states, and measurements on the same. Human-usable computational tasks necessarily require both input and final output to be classical information. The classical input information can influence the quantum computation in choice of initial states, the choice of unitary transforms (*i.e.*, algorithm), and the choice of measurement bases. The output is always a classical function of the measurement outcomes. In typical models for quantum computation, entire algorithms are implemented as a sequence of unitary transformations on a prepared quantum state (stored in qubits) of size appropriate to the problem, with a round of measurements as the final step. In such models, the unitary transformation stage is completely reversible. The splitting of the effective unitary matrix into sequential steps can be arbitrary and entirely dependent on physical hardware limitations. There is no correspondence with "computational steps" or "clock cycles" in the classical sense, as the quantum state of the computer in the midst of the unitary stages is inaccessible for diagnosis or debugging purposes. Any leakage of information into computer memory or environment constitutes decoher-

ence, and will introduce errors in the computation.

One-way quantum computation, on the other hand, revolves around single qubit measurements as a progression of computational steps. Measurements are a crucial component to quantum information processing because they irreversibly destroy a quantum state. Entanglement, on the other hand, will ensure that the state of the final qubit relies on the outcomes of preceding measurements. Given a cluster state, a series one-qubit measurements can be performed at each qubit to implement a quantum gate [3]. The unidirectionality of cluster state computation is inherent, due to the fact that quantum information cannot be accurately recovered once a measurement has been made. Consider a two-dimensional array of entangled qubits, information propagates horizontally through a row of qubits while vertical qubit neighbors are used for two-qubit gates. Similarly, three-dimensional clusters can be used to implement topologically protected gates [7], where the gate function only depends upon the way “connected defects” are wound around one another, but not on the details of their shape. This degree of freedom affords the design some fault tolerance.

Need picture and text showing some quantum circuit in traditional gate-array form. And a 2-D cluster state below it that looks identical in shape. Must mention how the “input qubits” will be coupled into the cluster.

C.1 Linear cluster state as a wire

Dileep will fill this

C.2 Gates through teleportation

Xiaodi mentioned that gate application via teleportation was the motivation for cluster states to begin with. This is basically Maira’s segment of the talk. Can she turn it into motivational prose?

C.3 Applying a 2-qubit gate via 2-D cluster state

Dileep will fill this

C.4 Commutations and parallelizability

Dileep has some idea here. It’s basically extending what is above.

D. Computational Power and Complexity

The spacial layout of the graph representation of the cluster state plays a role in the computational power of that state. If a cluster state can be prepared linearly via the cascading C_z technique mentioned above, it can be represented as a “one-dimensional” graph (i.e., some graph $G = (V, E)$, $\forall v \in V$, $\deg(v) \leq 2$). Operations on a(?) linearly prepared cluster state can be efficiently simulated on a classical computer in $O(n \log^c(1/n))$, where n is the initial number of qubits, and c is the cost of floating point multiplication [8]. Though the author consequently dismisses linearly prepared cluster states

as a substrate for quantum computation, it would be interesting to know which class of problems they would be able to solve. Zach, there may be something worth digging deep for here. Let’s talk about this.

In general, measurement based models can be polynomial time reduced to the gate array model, and thus have the same power, but they are more easily parallelizable [9].

The gate teleportation algorithm [6] has a time complexity of $O(\log(1/\epsilon))$, where ϵ is the failure probability.

To pad this out to ten pages, we can work on physical systems, limitations of linear optics, 3-D fault tolerant model, and complexity theory. If we REALLY have time, there may or may not be something to be done with qudits.

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