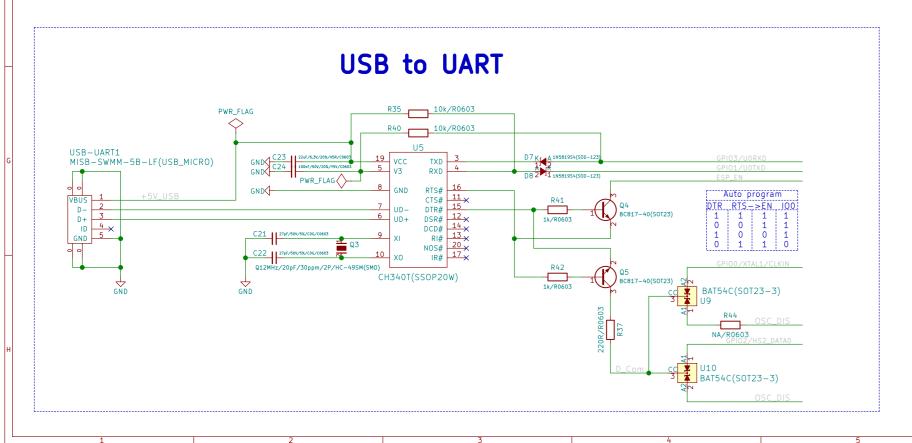
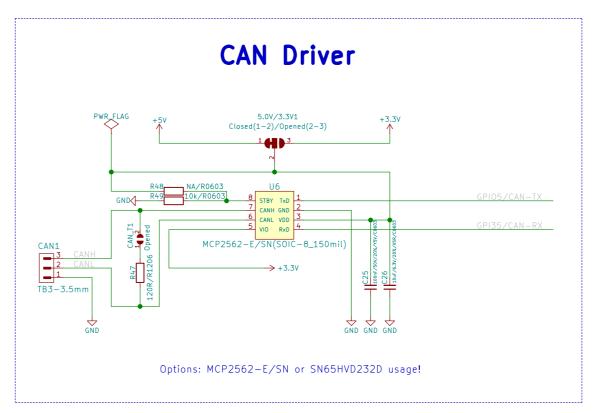


Mounting Holes





Interface	Signal	Pin
EMAC	EMAC_MDC_out EMAC_MDI_in EMAC_MDO_out EMAC_CRS_out EMAC_COL_out	Any GPIO
I2C	I2CEXTO_SCL_in I2CEXTO_SDA_in I2CEXT1_SCL_in I2CEXT1_SDA_in I2CEXT0_SCL_out I2CEXT0_SDA_out I2CEXT1_SCL_out I2CEXT1_SCL_out	Any GPIO
General Purpose SPI	HSPIQ_in/_out HSPIQ_in/_out HSPICLK_in/_out HSPI_CSO_in/_out HSPI_CSO_out HSPI_CS2_out VSPIQ_in/_out VSPIC_in/_out VSPICLK_in/_out VSPIC_SO_in/_out VSPI_CSS_out VSPI_CSS_out	Any GPIO

GND ← MH2 GND ← MH3		GF	P102: Pu	Pull-Down Pull-Down		
		GF	P104: Pu			
		MTDO/GPI	015: Pu	Pull-Up		
		GF	P105: Pu	Pull-Up		
			+			
	,	Voltage of Inter	nal LDO(VDD_SDIO)			
Pin	Default	3.3V	1.8V			
GPI012/MTDI	Pull-Down	0	1	Bootstrapping Pins		
		Bootin	g Mode			
Pin	Default	SPI Flash Boot	Download Boot	Settings		
GPI00	Pull-Up	1	0			
GPI02	Pull-Down	Don't-care	0			
		Debugging Log on I	JOTXD During Booting			
Pin	Default	UOTXD Toggling	UOTXD Silent	UOTXD Silent		
GPI015/MTD0	Pull-Up	11	0			
		Timing of	SDIO Slave			
Pin	Default	Falling-edge:Output	Falling-edge:Input Rising-edge:Output	Rising—edge:Input Falling—edge:Output	Rising—edge:Input Rising—edge:Outpu	
GPI015/MTD0	Pull-Up	0	0	1	1	
GPI05	Pull-Up	0	1	0	1	

GPI00:

Pull-Up

