A Semantics-Based Approach to Concept Assignment in Assembly Code

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ICCWS 2017, Dayton, Ohio, USA

March 3, 2017

Overview

- What is Concept Assignment? Why?
- Semantics for Concept Assignment
- Data Model to extend and compose concepts

The Concept Assignment Problem

"Recognizing concepts and assigning them to locations within a program in order to build an understanding of that program"

- Programming constructs, program features, behaviors
- ► Extensively researched in source code but not binary code

The Concept Assignment Problem in Assembly

Why?

- Reverse engineers read assembly code to understand binaries
- Cyber analysts use tools to understand behaviors of vulnerable or malicious software
- Enable automated approaches to describe and discover classes of vulnerabilities in software

Specify Formal Language and Semantics

Lift assembly to an Intermediate Language

```
program ::= stmt^*
stmt s ::= var := exp \mid store(exp, exp) \mid goto
                     exp | assert exp | if exp then goto
                     exp else goto exp | halt
                    load(exp) \mid exp \diamond_b exp \mid \diamond_u exp \mid var
             ::=
exp e
                     \mid get input(src) \mid v
                    + | - | * | / | \lor | \land | < | \le | > | \ge
\Diamond_h
         ::= - (unary minus) | \neg (logical negation)
\Diamond_n
value v
                    32-bit unsigned integer \mid \perp
            ::=
src
              ::=
                    string
```

Figure 1: Grammar of a simple intermediate language

Example - Dynamic Memory Allocation

C Source Code

```
int main() {
    int *heap_array = (int*)malloc(3 * sizeof(int));
    int index = 2;
    heap_array[0] = 10;
    heap_array[1] = 20;
    heap_array[index] = 30;
}
```

Assembly

0000000000400506 <main > : 400506: push rbp 400507: rbp, rsp mov 40050a: sub rsp .0 x10 40050e: edi.0xc mov 400513: call 400400 <malloc@plt> 400518: mov QWORD PTR [rbp-0x8], rax 40051c: DWORD PTR [rbp-0xcl.0x2 mov 400523: eax QWORD PTR [rbp-0x8] mov 400527: DWORD PTR [rax],0xa mov 40052d: mov rax.QWORD PTR [rbp-0x8] 400531: add rax,0x4 400535: DWORD PTR [rax].0x14 mov 40053b: mov eax . DWORD PTR [rbp-0xc] cdae 40053e: 400540 lea rdx .[rax*4+0x0] rax QWORD PTR [rbp-0x8] 400548: mov 40054c: hhs rax . rdx 40054f: mov DWORD PTR [rax],0x1e 400555: leave 400556: ret

Intermediate Language

```
esp := esp - 1
   store (esp, ebp)
   ebp := esp
   esp := esp - 2
   edi := 3
   eax := get_input(malloc)
   store (ebp - 1, eax)
   store (ebp - 2, 2)
   eax := load(ebp - 1)
   store (eax. 10)
11
   eax := load(ebp - 1)
12
   eax := eax + 1
13
   store (eax. 20)
14
   eax := load(ebp - 2)
15
   edx :=
          eax * 1 + 0
16
   eax := load(ebp - 1)
17
   eax := eax + edx
18
   store (eax. 30)
19
   esp := ebp
20
   ebp := load(esp)
21
   halt
```

Specify Formal Language and Semantics

Use operational semantics to prove the existence of a concept in a program

$$\begin{split} v \text{ is input from } src \\ \frac{H'_{\mu} = H_{\mu}[v \leftarrow P_{\text{malloc}}(src)] \quad H'_{\rho} = H_{\rho}[v \leftarrow \rho_{\text{malloc}}(src, edi)]}{H_{\mu}, H_{\rho}, \mu, \Delta \vdash \text{get_input}(src) \Downarrow v} \\ \frac{\mu, \Delta \vdash e \Downarrow v \quad v' = \mu[v] \quad \varphi = \Phi(v, H_{\mu}[v'])}{H_{\mu}, \mu, \Delta \vdash \text{load } e \Downarrow v'} \text{H-LOAD} \\ \frac{P_{\text{heapcheck}}(v_1, \mu[\varphi], H_{\rho}[\mu[\varphi]], H_{\mu}[\mu[\varphi]]) = \mathbf{T}}{\mu, \Delta \vdash e_1 \Downarrow v_1 \quad \mu, \Delta \vdash e_2 \Downarrow v_2 \quad \mu' = \mu[v_1 \leftarrow v_2] \quad \iota = \Sigma[pc+1]}{H_{\mu}, H_{\rho}, \Sigma, \mu, \Delta, pc, \text{store}(e_1, e_2) \leadsto H_{\mu}, H_{\rho}, \Sigma, \mu', \Delta, pc + 1, \iota} \text{H-STORE} \end{split}$$

Figure 2: Operational semantics that trace dynamic allocation of memory

Formal Semantics

Extract information about the feature.

- How much memory was allocated to the program heap;
- The pointer in memory to the data structure;
- ► The total number of elements possible to allocate;
- The data stored in the array;
- If any access to the heap array is out-of-bounds (possible buffer overflow).

Integrate with Data Model

Knowledge Representation Data Model

- Organize concepts
- "Back-end" for the formal language
- Compose basic analyses together to map more complex behaviors to locations in program

Integrate with Data Model

Category-Theoretic Data Model

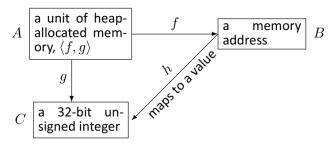


Figure 3: Data model representing a block of heap memory with address and value

Conclusions

- ▶ Re-frame and formalize concept assignment for binaries
- Method is limited by the richness of the knowledge base
- ► Goal: automate comprehension of binary code to support cyber analysts and reverse engineers

Thank You

Questions?