CC1120 notes  
Referenced from the **C1120** **User Guide**

Overview:

* SPI interface used to configure chip for different applications. (p 5)

Configuration:

* Configure using SmartRF Studio Software (p 6):
  + <http://www.ti.com/tool/smartrftm-studio>
  + Necessary to reconfigure after every chip reset through SPI interface. SmartRF provides a code export function making it easy to implement in firmware.

Microcontroller Interface:

* Interface with 4wire SPI to:
  + Configure CC1120
  + Program into Rx, Tx, Sleep, Idle
  + Read and write data RX FIFO, TX FIFO
  + Read status info
* SPI: CC1120 is slave pins are (SI, SO, SCLK, CSn)
* Some GPIO pins are shared with the SPI pins, check user maual

System Considerations:

* Internal voltage regulator for the digital core requires one external decoupling capacitor.
* Up to 200 kbps in data streaming continuous transmission applications.
* SLEEP state should be used for low power applications (switch off internal oscillator)
* Also consider eWOR functionality (Enhanced Wake On Radio)

Mostly seems to be firmware stuff on this doc…