

# 1. Description

## 1.1. Project

Project Name	StepMotorCan
Board Name	custom
Generated with:	STM32CubeMX 6.11.0
Date	04/18/2024

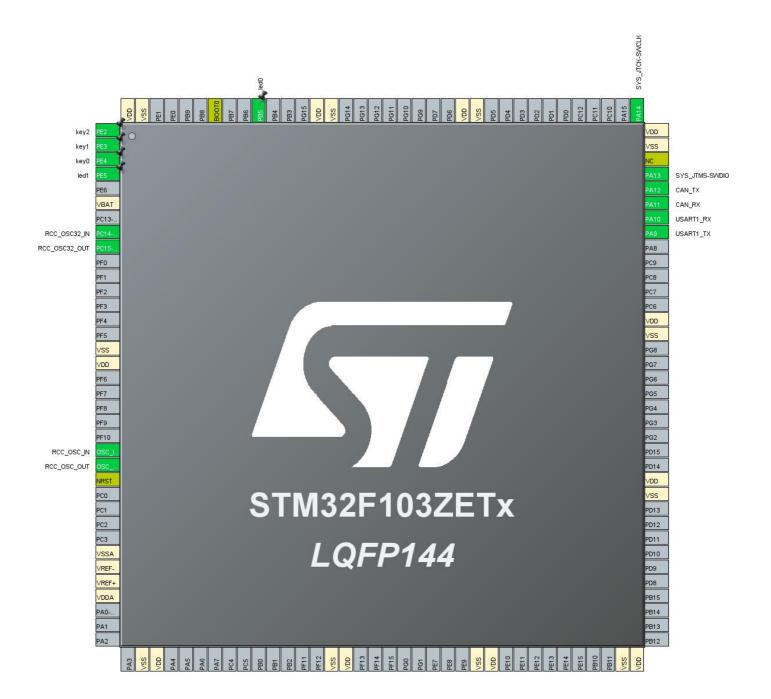
### 1.2. MCU

MCU Series	STM32F1
MCU Line	STM32F103
MCU name	STM32F103ZETx
MCU Package	LQFP144
MCU Pin number	144

## 1.3. Core(s) information

Core(s)	Arm Cortex-M3

# 2. Pinout Configuration



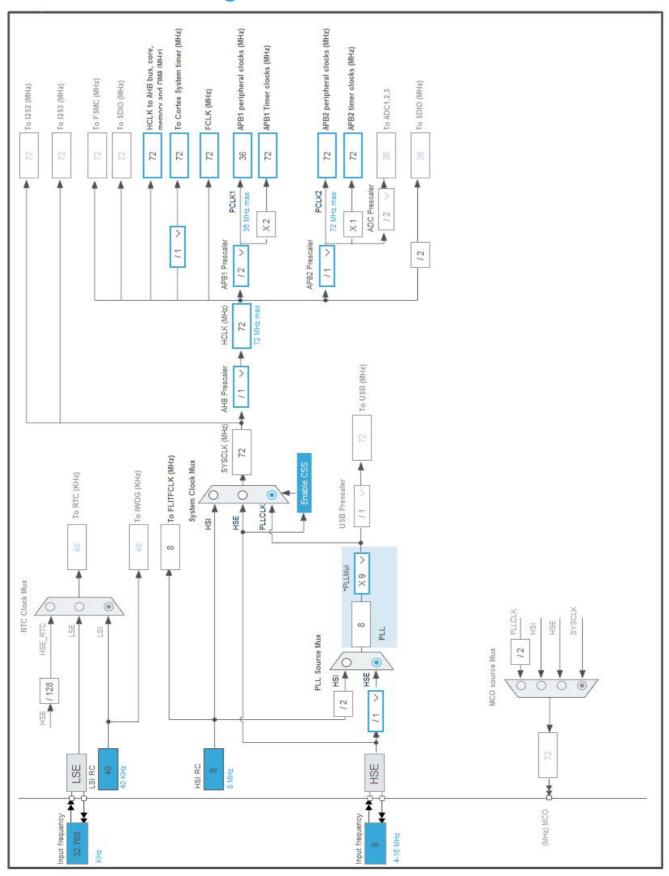
# 3. Pins Configuration

Pin Number	Pin Name	Pin Type	Alternate	Label
LQFP144	(function after		Function(s)	
	reset)		,	
1	PE2 *	I/O	GPIO_Input	key2
2	PE3 *	I/O	GPIO_Input	key1
3	PE4 *	I/O	GPIO_Input	key0
4	PE5 *	I/O	GPIO_Output	led1
6	VBAT	Power	51 10 <u></u> 50 10 10	
8	PC14-OSC32_IN	I/O	RCC_OSC32_IN	
9	PC15-OSC32_OUT	I/O	RCC_OSC32_OUT	
16	VSS	Power		
17	VDD	Power		
23	OSC_IN	MonolO	RCC_OSC_IN	
24	OSC_OUT	MonolO	RCC_OSC_OUT	
25	NRST	Reset		
30	VSSA	Power		
31	VREF-	Power		
32	VREF+	Power		
33	VDDA	Power		
38	VSS	Power		
39	VDD	Power		
51	VSS	Power		
52	VDD	Power		
61	VSS	Power		
62	VDD	Power		
71	VSS	Power		
72	VDD	Power		
83	VSS	Power		
84	VDD	Power		
94	VSS	Power		
95	VDD	Power		
101	PA9	I/O	USART1_TX	
102	PA10	I/O	USART1_RX	
103	PA11	I/O	CAN_RX	
104	PA12	I/O	CAN_TX	
105	PA13	I/O	SYS_JTMS-SWDIO	
106	NC	NC		
107	VSS	Power		
108	VDD	Power		

Pin Number LQFP144	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
109	PA14	I/O	SYS_JTCK-SWCLK	
120	VSS	Power		
121	VDD	Power		
130	VSS	Power		
131	VDD	Power		
135	PB5 *	I/O	GPIO_Output	led0
138	воото	Boot		
143	VSS	Power		
144	VDD	Power		

<sup>\*</sup> The pin is affected with an I/O function

# 4. Clock Tree Configuration



# 5. Software Project

## 5.1. Project Settings

Name	Value
Project Name	StepMotorCan
Project Folder	D:\work\Motor\repo\stm32-sample-
Toolchain / IDE	STM32CubeIDE
Firmware Package Name and Version	STM32Cube FW_F1 V1.8.4
Application Structure	Advanced
Generate Under Root	Yes
Do not generate the main()	No
Minimum Heap Size	0x200
Minimum Stack Size	0x400

## 5.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	Yes
Backup previously generated files when re-generating	No
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	No
Enable Full Assert	No

## 5.3. Advanced Settings - Generated Function Calls

Rank	Function Name	Peripheral Instance Name
1	SystemClock_Config	RCC
2	MX_GPIO_Init	GPIO
3	MX_DMA_Init	DMA
4	MX_CAN_Init	CAN
5	MX_USART1_UART_Init	USART1

# 1. Power Consumption Calculator report

### 1.1. Microcontroller Selection

Series	STM32F1
Line	STM32F103
MCU	STM32F103ZETx
Datasheet	DS5792_Rev12

### 1.2. Parameter Selection

Temperature	25
Vdd	3.3

### 1.3. Battery Selection

Battery	Li-SOCL2(A3400)
Capacity	3400.0 mAh
Self Discharge	0.08 %/month
Nominal Voltage	3.6 V
Max Cont Current	100.0 mA
Max Pulse Current	200.0 mA
Cells in series	1
Cells in parallel	1

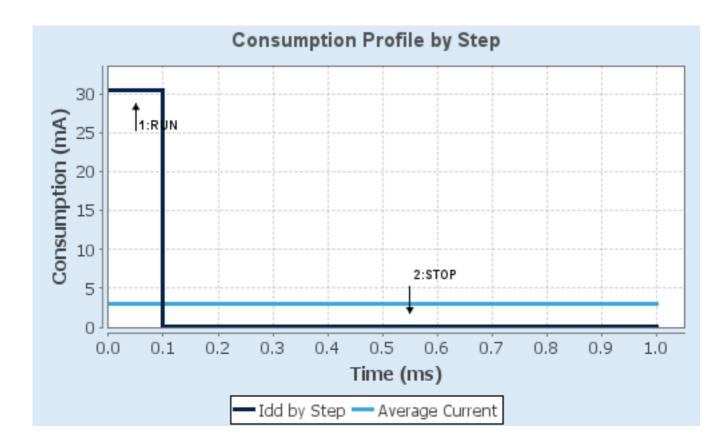
## 1.4. Sequence

Step	Step1	Step2
Mode	RUN	STOP
Vdd	3.3	3.3
Voltage Source	Battery	Battery
Range	No Scale	No Scale
Fetch Type	FLASH	n/a
CPU Frequency	72 MHz	0 Hz
Clock Configuration	HSE PLL	Regulator LP
Clock Source Frequency	8 MHz	0 Hz
Peripherals		
Additional Cons.	0 mA	0 mA
Average Current	30.5 mA	25 μΑ
Duration	0.1 ms	0.9 ms
DMIPS	90.0	0.0
Та Мах	101.98	105
Category	In DS Table	In DS Table

### 1.5. Results

Sequence Time	1 ms	Average Current	3.07 mA
Battery Life	1 month, 15 days,	Average DMIPS	61.0 DMIPS
	15 hours		

### 1.6. Chart



# 2. Peripherals and Middlewares Configuration

#### 2.1. CAN

mode: Activated

#### 2.1.1. Parameter Settings:

#### **Bit Timings Parameters:**

Prescaler (for Time Quantum) 4 \*

Time Quantum 111.1111111111111 \*

Time Quanta in Bit Segment 1 9 Times \*

Time Quanta in Bit Segment 2 8 Times \*

Time for one Bit 2000 \*

Baud Rate 500000 \*

ReSynchronization Jump Width 1 Time

#### **Basic Parameters:**

Time Triggered Communication Mode

Automatic Bus-Off Management

Disable

Automatic Wake-Up Mode

Disable

Automatic Retransmission

Disable

Receive Fifo Locked Mode

Transmit Fifo Priority

Disable

#### **Advanced Parameters:**

Test Mode Normal

#### 2.2. RCC

High Speed Clock (HSE): Crystal/Ceramic Resonator Low Speed Clock (LSE): Crystal/Ceramic Resonator

#### 2.2.1. Parameter Settings:

#### **System Parameters:**

VDD voltage (V) 3.3
Prefetch Buffer Enabled

Flash Latency(WS) 2 WS (3 CPU cycle)

#### **RCC Parameters:**

HSI Calibration Value 16
HSE Startup Timout Value (ms) 100
LSE Startup Timout Value (ms) 5000

#### 2.3. SYS

**Debug: Serial Wire** 

Timebase Source: SysTick

#### 2.4. USART1

**Mode: Asynchronous** 

#### 2.4.1. Parameter Settings:

#### **Basic Parameters:**

Baud Rate 115200

Word Length 8 Bits (including Parity)

Parity None Stop Bits 1

**Advanced Parameters:** 

Data Direction Receive and Transmit

Over Sampling 16 Samples

#### 2.5. FREERTOS

Interface: CMSIS\_V2

### 2.5.1. Config parameters:

API:

FreeRTOS API CMSIS v2

Versions:

FreeRTOS version 10.0.1 CMSIS-RTOS version 2.00

Kernel settings:

USE\_PREEMPTION Enabled

CPU\_CLOCK\_HZ SystemCoreClock

 TICK\_RATE\_HZ
 1000

 MAX\_PRIORITIES
 56

 MINIMAL\_STACK\_SIZE
 128

 MAX\_TASK\_NAME\_LEN
 16

 USE\_16\_BIT\_TICKS
 Disabled

IDLE\_SHOULD\_YIELD Enabled
USE\_MUTEXES Enabled

USE\_RECURSIVE\_MUTEXES Enabled Enabled USE\_COUNTING\_SEMAPHORES QUEUE\_REGISTRY\_SIZE 8 USE\_APPLICATION\_TASK\_TAG Disabled Enabled ENABLE\_BACKWARD\_COMPATIBILITY USE\_PORT\_OPTIMISED\_TASK\_SELECTION Disabled Disabled USE\_TICKLESS\_IDLE Enabled USE\_TASK\_NOTIFICATIONS Disabled RECORD\_STACK\_HIGH\_ADDRESS

#### Memory management settings:

Memory Allocation Dynamic / Static

TOTAL\_HEAP\_SIZE 4096 \*
Memory Management scheme heap\_4

#### **Hook function related definitions:**

USE\_IDLE\_HOOK Disabled
USE\_TICK\_HOOK Disabled
USE\_MALLOC\_FAILED\_HOOK Disabled
USE\_DAEMON\_TASK\_STARTUP\_HOOK Disabled
CHECK\_FOR\_STACK\_OVERFLOW Disabled

#### Run time and task stats gathering related definitions:

GENERATE\_RUN\_TIME\_STATS Disabled
USE\_TRACE\_FACILITY Enabled
USE\_STATS\_FORMATTING\_FUNCTIONS Disabled

#### Co-routine related definitions:

USE\_CO\_ROUTINES Disabled MAX\_CO\_ROUTINE\_PRIORITIES 2

#### Software timer definitions:

USE\_TIMERS Enabled
TIMER\_TASK\_PRIORITY 2
TIMER\_QUEUE\_LENGTH 10
TIMER\_TASK\_STACK\_DEPTH 256

#### Interrupt nesting behaviour configuration:

LIBRARY\_LOWEST\_INTERRUPT\_PRIORITY 15
LIBRARY\_MAX\_SYSCALL\_INTERRUPT\_PRIORITY 5

#### 2.5.2. Include parameters:

#### Include definitions:

vTaskPrioritySet Enabled uxTaskPriorityGet Enabled vTaskDelete Enabled

vTaskCleanUpResources Disabled Enabled vTaskSuspend vTaskDelayUntil Enabled vTaskDelay Enabled Enabled xTaskGetSchedulerState xTaskResumeFromISR Enabled xQueueGetMutexHolder Enabled Disabled xSemaphoreGetMutexHolder Disabled pcTaskGetTaskName Enabled uxTaskGetStackHighWaterMark Disabled xTaskGetCurrentTaskHandle Enabled eTaskGetState Disabled  $x \\ Event Group Set Bit From ISR$ Enabled xTimerPendFunctionCall Disabled xTaskAbortDelay Disabled xTaskGetHandle

#### 2.5.3. Advanced settings:

Newlib settings (see parameter description first):

USE\_NEWLIB\_REENTRANT Enabled \*

Project settings (see parameter description first):

Use FW pack heap file Enabled

<sup>\*</sup> User modified value

# 3. System Configuration

## 3.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
CAN	N PA11 CAN_RX Input mode No pull-up and no pull-down		No pull-up and no pull-down	n/a		
	PA12	CAN_TX	Alternate Function Push Pull	n/a	High *	
RCC	PC14- OSC32_IN	RCC_OSC32_IN	n/a	n/a	n/a	
	PC15- OSC32_OU T	RCC_OSC32_O UT	n/a	n/a	n/a	
	OSC_IN	RCC_OSC_IN	n/a	n/a	n/a	
	OSC_OUT	RCC_OSC_OUT	n/a	n/a	n/a	
SYS	PA13	SYS_JTMS- SWDIO	n/a	n/a	n/a	
	PA14	SYS_JTCK- SWCLK	n/a	n/a	n/a	
USART1	PA9	USART1_TX	Alternate Function Push Pull	n/a	High *	
	PA10	USART1_RX	Input mode	No pull-up and no pull-down	n/a	
GPIO	PE2	GPIO_Input	Input mode	Pull-up *	n/a	key2
	PE3	GPIO_Input	Input mode	Pull-up *	n/a	key1
	PE4	GPIO_Input	Input mode	Pull-up *	n/a	key0
	PE5	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	led1
	PB5	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	led0

### 3.2. DMA configuration

DMA request	Stream	Direction	Priority
USART1_TX	DMA1_Channel4	Memory To Peripheral	Low

### USART1\_TX: DMA1\_Channel4 DMA request Settings:

Mode: Normal
Peripheral Increment: Disable
Memory Increment: Enable \*

Peripheral Data Width: Byte
Memory Data Width: Byte

## 3.3. NVIC configuration

## 3.3.1. NVIC

Interrupt Table	Enable	Preenmption Priority	SubPriority		
Non maskable interrupt	true	0	0		
Hard fault interrupt	true	0	0		
Memory management fault	true	0	0		
Prefetch fault, memory access fault	true	0	0		
Undefined instruction or illegal state	true	0	0		
System service call via SWI instruction	true	0	0		
Debug monitor	true	0	0		
Pendable request for system service	true	15	0		
System tick timer	true	15	0		
DMA1 channel4 global interrupt	true 5		0		
USB low priority or CAN RX0 interrupts	true	5	0		
USART1 global interrupt	true	5	0		
PVD interrupt through EXTI line 16	unused				
Flash global interrupt	unused				
RCC global interrupt	unused				
USB high priority or CAN TX interrupts	unused				
CAN RX1 interrupt	unused				
CAN SCE interrupt	unused				

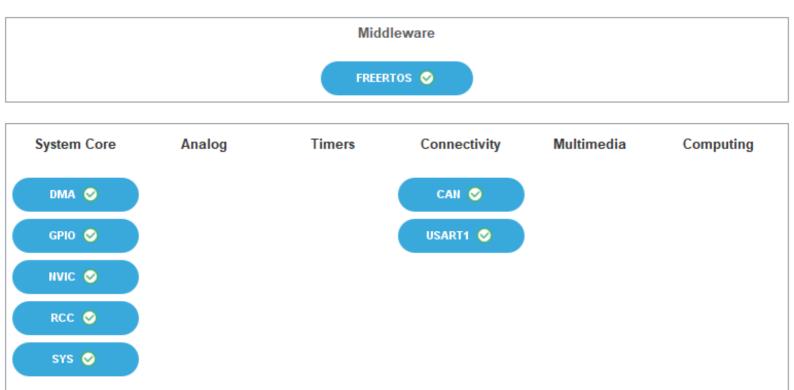
## 3.3.2. NVIC Code generation

Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
Non maskable interrupt	false	true	false
Hard fault interrupt	false	true	false
Memory management fault	false	true	false
Prefetch fault, memory access fault	false	true	false
Undefined instruction or illegal state	false	true	false
System service call via SWI instruction	false	false	false
Debug monitor	false	true	false
Pendable request for system service	false	false	false
System tick timer	false	true	true
DMA1 channel4 global interrupt	false	true	true
USB low priority or CAN RX0 interrupts	false	true	true
USART1 global interrupt	false	true	true

* User modified value		
Osei modined value		

# 4. System Views

- 4.1. Category view
- 4.1.1. Current



## 5. Docs & Resources

Type Link

BSDL files https://www.st.com/resource/en/bsdl\_model/stm32f1\_bsdl.zip

IBIS models https://www.st.com/resource/en/ibis\_model/stm32ibis.zip

System View https://www.st.com/resource/en/svd/stm32f1\_svd.zip

Description

Presentations https://www.st.com/resource/en/product\_presentation/stm32-

stm8\_embedded\_software\_solutions.pdf

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guide-for-stm8-stm32-and-legacy-mcus-stmicroelectronics.pdf

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with-stm32f10xxx-hardware-development-stmicroelectronics.pdf

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