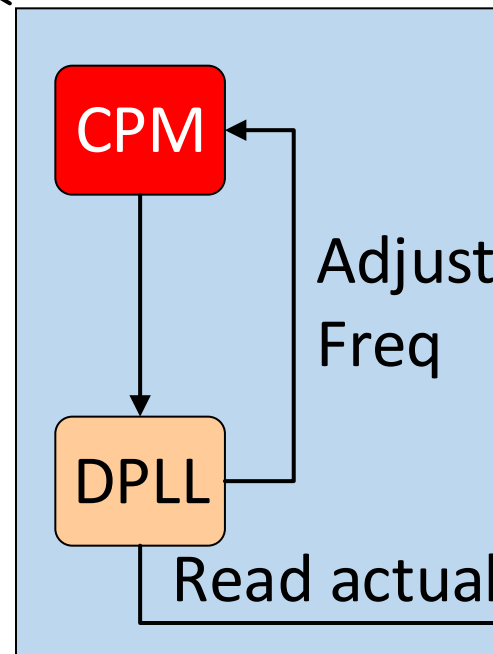
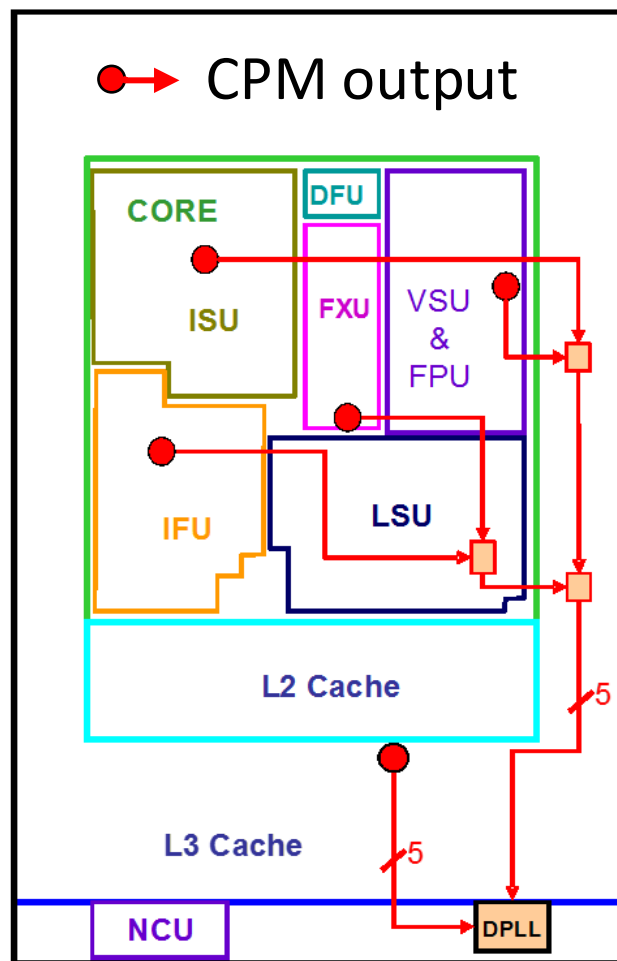


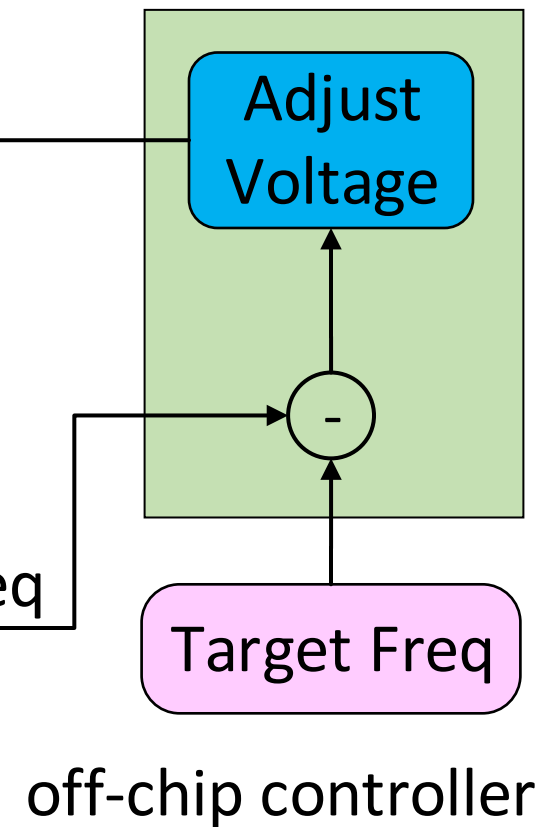
- sense timing margin

- per-core adaptive frequency control

- power/perf policy control



POWER7+
(one core showing)



off-chip controller