a. Design a foult-tolerant storage using digital standards that requires the message to be split into  $k+e \le n \le k+2e$  data blocks, where k is the minimum no. of data blocks needed and e is the allowed no. of bloch corruptions.

A. In order to ensure fault-tolerant storage, we can define a k-1 degree polynomial defined over a sufficiently large field.

Consider a finite field Fp (integers modulo p)

s.t. p is a prime  $p > 2^{b}$  |Fp| > n

## Encoding the message

Split the message into k blocks,  $m = m_0 m_1 m_2 ... m_{K-1}$   $st. m_i \in \mathbb{F}_p \ \forall i$ We know that a polynomial of degree K-1 can uniquely

be defined by 'k' evaluations. Consider polynomial in field given:  $M(x) = M_0 + M_1 x + M_2 x^2 + ... + M_{N_1} x^{N_1-1}$ 

Now we defined n such that  $k+e \le n \le k+2e$ Evaluating M(n) at n points  $\alpha_{0...n-1}$  ( $M(a_0)$ ,  $M(a_1)$ , ...  $M(\alpha_{n-1})$ ), we can then digitally sign each of the results (say  $C_i$ ). We now have a scheme that encodes a message in  $k+e \le n \le k+2e$  blocks.

$$C(m) = \begin{bmatrix} C_0 \\ C_1 \\ C_2 \end{bmatrix} = \begin{bmatrix} 1 & a_1 & a_2^2 & ... & a_0^{n-1} \\ 1 & a_1 & a_1^2 & ... & a_1^{n-1} \\ 1 & a_2 & a_2^2 & ... & a_2^{n-1} \\ \vdots & \vdots & \ddots & \ddots & \vdots \\ 1 & a_{N-1} & a_{N-1} & a_{N-1} \end{bmatrix} \begin{bmatrix} M_0 \\ M_1 \\ M_2 \\ \vdots \\ M_N \end{bmatrix}$$

Digitally sign

## Verifying Tamper Detection with N < k+2e

A PPTM adversary can temper with message blocks, with regligible error in detection permitted. Let #

be the set of tempered blocks, with  $|T| \le e$  (by problem definition). As all blocks are digitally signed, we can use verify to checkif a block has been tempered or not. For all t & T verify(t) with return true (not tempered) with regl1) probability (as we have a PPTM adversary).

## Recovering from k blocks

As  $|T| \leq e$ ,  $n-e \geq k$  untempered blocks remain. Each block  $C_i$  is an evaluation of the unique message polynomial (of degree k-1), allowing us to recover the message by using polynomial takerpolation to recover the coefficients.

We use Genesian Elimination for this

The goal is to invert the original transform:  $M = \begin{bmatrix} 1 & a_0 & a_0^2 & \dots & a_0^{n-1} \\ \vdots & & & & & & \\ 1 & a_n & a_{n-1}^{n-1} & & & & \\ & & & & & & \\ \end{bmatrix}^{-1} \begin{bmatrix} c_0 \\ \vdots \\ c_{n-1} \end{bmatrix}$ 

As Gaucsian elimination is done on finik field Fp, with operations done modulo P, we consider division to be multiplication with the modular inverse.

Consider a rough algorithm for such. A = [c | b] for cx = b.

for c = 0 to k  $r = row \quad with \quad A[r][c] \neq 0$ Swap rows r and c

$$F = C$$

$$F_{DC} = 0 + D \times K$$

$$i \neq 1 \neq C$$

$$W = - A[i][C]$$

$$A[i][C] + C \times K$$

$$A[i][C] + C \times K$$

$$M[i] = A[i][K]$$

$$A[i][C]$$

$$A[i][C]$$

$$TAMA MA$$