CSE350 Ahmad Zubair Sec: 07

10 TH As Servery

OR Gate

VA	VB	181	102	Je,	$I_{\mathbb{P}_2}$	16=1
O	0	O	0	0	0	O
0	5	0	0,0065	0	4.41908×10	4,41508
5	0	0.0065	0	4,41908×10	0	4.41908
5	5	0.00327	0.00327	2.22006×10-5	2.22006×10	4.44012
				Valle Harasay	1/	

AND Gate

أستنس						
VA	1B	1 _{R1}	1R2	IP	I_{P_2}	V12 = Y
0	0	0.00326	0.00326	2.22006 XID	2.22006×10	0, 55988
0	5	0.00649	0	4.41908×10-5	0	0.53092
5	0	0	0.00649	0	4.41908810	0.58092
5	5	0	0	0	0	5

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Vi	VRI	VR2	VPC.	1,	±2	IB	Le	Y (
1				4.34782×10	1 1 1		0	Ive down	
5	4.29662	5.70334	4.89188	0,0002864	5.70331×10	0,0002294	0.0022235	0.108117	

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Report: Strong by I vonvo

1. If we set VA=OV, VB=OV the output is logical

low.

IP we set $V_A = 0V$, $V_B = 5V$ the output is also

logical low.

If we set VA = 5V, VB = OV the output remains

the same as case 2.

However, if we set 1/=51, 1/2=51 the ontput is logical high.

A 3 38 8 8 8 8 0 0 0 - , t

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for or gate,
there won't be any difference as for
both the diodes the value of anode
will be greater than that of the
outhode. So, D, & D2 will work.

for AND gate,

the diodes D, or D2 won't work as the

value of the crode (VR = 5V) will be

value of the value of any of the

less than the value of any of the

diodes which is GV. As a result, D, &

D2 won't work.

3. The function of 12=100k at the base of an inverter is to control the current from of the base terminal. To illustrate, the looka resistor is connected to the base terminal so that the base terminal could channel the excessive amount of electory from the base terminal to a lover voltage

4. when vi= ov,

VB = - 0.652178V

NE = 01

:: VBE = -0.652178V

BE = -0.652178V -: VBE < 0.5 : autoff

when vi=54

Ic = 0.00222358 A

to=0.000229409 A

We assume PF=30 is in between 40 % 120 $\frac{I_c}{I_B} = \frac{0.00222358}{0.000229409}$ = 9-6926 -: 9.6026 | < PF : saturation 5. La i will ave to the B C