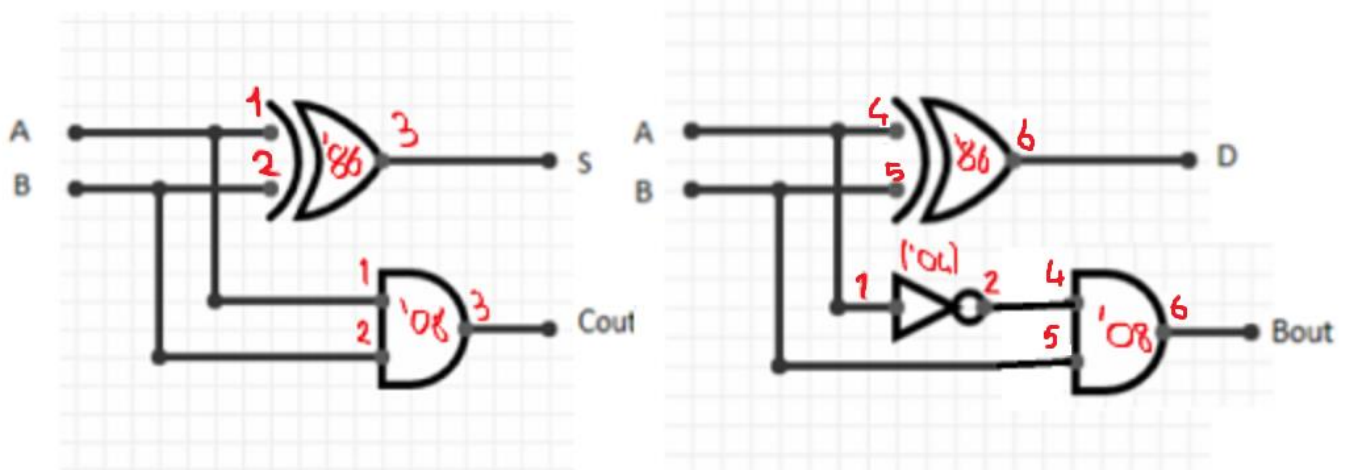CS-223-02

Lab 1

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PRELIMINARY REPORT

4 IC's are used;

1. One 7404 HEXInverting Gate

2. One 7408 Quad-2 Input AND Gate

3. One 7432 Quad-2 Input OR Gate

4. One 7486 Quad-2 Input XOR Gate

and for all IC's, their pin number for GND is 7 and for V_{cc}(+5V) is 14.

Below, there are the circuit schematics for half-adder and half-substractor in figure 2.
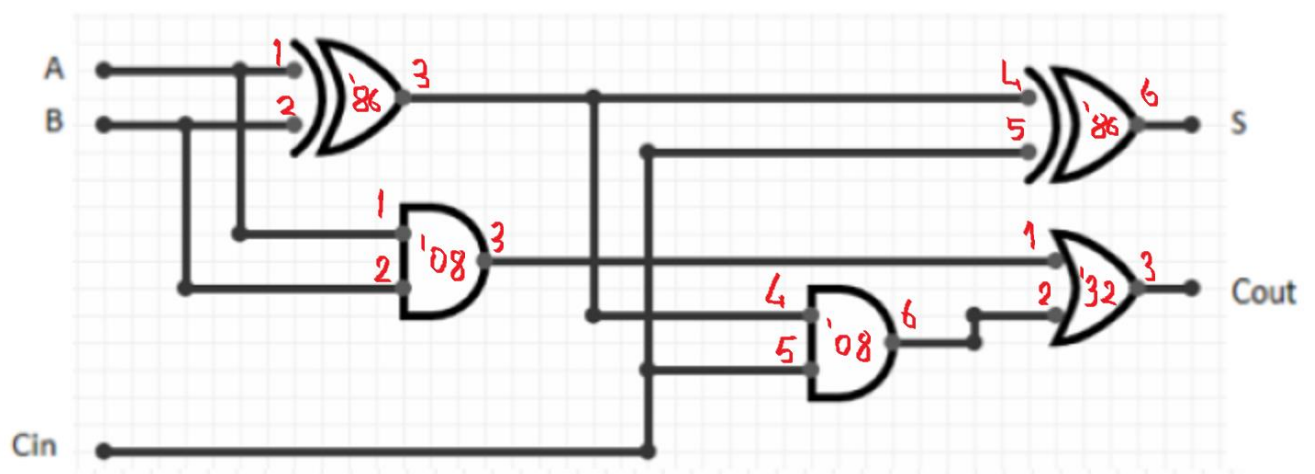
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Circuit schematics for full-adder (which was in figure 3) is also below.

Finally, this is circuit schematics for the full-substractor in figure 3.