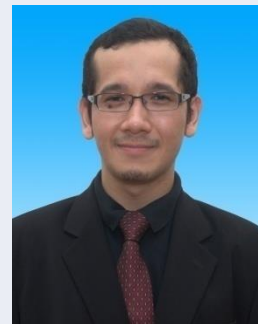


# Muhamad Musa bin Adam Lim

Citizenship : Malaysian [890729-04-5025] - Date of birth : 29 July 1989



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No : 19, Jalan Emas 1, Taman Emas, Telok Mas, 75460, Melaka	Home : 06 - 2610309 H/P : 0126430750 e-mail : musa.adamlim@gmail.com

## Profile

**Objective** I am seeking a position within the Electronic and Computer Engineering department or any related department possibly with an international perspective.

**Availability** Available (Studied at Universiti Teknikal Malaysia Melaka)

**Expected Salary** RM 3600(negotiable)

## Key Skills

(**Proficiency: Advanced** - Highly experienced; **Intermediate** - Familiar with all the basic functionalities; **Beginner** - Just started using or learning the skill):

Skill	Proficiency	Skill	Proficiency
Microsoft OS	Advanced	MPLab IDE	Intermediate
Multisim	Advanced	(Microcontroller)	
Microsoft Office	Advanced	Xilinx ISE (Verilog)	Intermediate
Computer	Advanced	C++	Intermediate
Troubleshooting		Mentor Graphics	Beginner
Network Fundamental	Intermediate	Eclipse JAVA	Beginner
C Language	Intermediate	Ubuntu/Backtrack	Beginner
(Microcontroller)			
Code Composer Studio	Intermediate		
Texas Instruments			

## Education

<b>2008 to 2012</b>	<b>Bachelor in Electronic Engineering (Computer Engineering)</b> Universiti Teknikal Malaysia Melaka, Malaysia <b>Current CGPA : 2.59</b>
<b>2007 to 2008</b>	<b>Foundation in Physical Science</b> Kolej Matrikulasi Melaka
<b>2002 to 2006</b>	<b>Malaysian Certificate of Education (SPM)</b> Sekolah Menengah Kebangsaan Telok Mas (Pure Science) Result = 9A 1C

## Training and Professional Certifications

- ✓ CompTIA A+(2010)
  - CompTIA A+ ID = COMP001020202812
- ✓ CIDB Electrical Wiring GL MYS-CGW0
- ✓ Certified IPC Specialist (CIS)
- ✓ Texas Instruments C55x Training using Code Composer Studio
- ✓ Challenge to Excel: Effective Personal Leadership : Dr. Sarjit Singh PhD (Master Trainer)
- ✓ Challenge to Excel: Take Charge: Dr. Sarjit Singh PhD (Master Trainer)
- ✓ Character Wise Leadership Training Program
- ✓ Design For Six Sigma For Product Development (Green Belt Program)

## Internship Experience

### Kolej Poly-Tech MARA Bangi

#### Internship (Computer technician trainee)

**May 2011 to July 2011**

- ✓ Working field including basic networking, formatting computer/notebook and diagnose & solve basic computer problems and laser printers.

## Working Experience

### MIMOS BHD Contract Electronic Engineer (Product Development)

- **Apprentice Code 8 Junior Engineer (1 year contract : October 2012 – October 2013)**
- **Engineer 1 - October 2013 till present (2 years contract : October 2013 – October 2015)**
- ✓ Involve in the team of product development in designing one of the electronic components of the main sensor device by using Texas Instruments IC to sense chemical reaction which producing electric current. Task including characterizing the main parameters of the IC which to be controlled by a Microcontroller as well as identifying the output window produced from the sensor itself. In charge to work together with the firmware team by giving input parameters to the firmware team as well as testing the sensor prototype for every firmware updates.
- ✓ Responsible to develop a product together with the team in a way of verifying the main parameters of the sensor prototype after the design process, as well as identifying the faulty units which not meeting the spec by inspecting and root causing the problems.
- ✓ Responsible to verify and modify the Sensor Board Module Test Jig which to be used for the manufacturing.
- ✓ Develop Test Procedure of the Sensor Board Module for the manufacturing.
- ✓ Conducted RF radiation test in order to observe the radiation pattern from the wireless device connected to the sensor.
- ✓ Person in charge to conduct rework or modifying on the Sensor Board Module for any modifications or design change.
- ✓ Supporting Reliability Test of the prototype with the Reliability Group in order to test of the robustness of the product.

- ✓ In charge of the Bill of Materials (BOM) for the products as well as related documents for product releasing, such as Test Procedure and Technical Architecture Document.

### University Projects

- Year 4** ✓ Final Year Project – Design and Implementation of 8-Bit Multiplier on FPGA(Field Programmable Gate Array)
- Year 3** ✓ Line following robot (PIC microcontroller)  
✓ Electromagnetic Dynamo Windmill
- Year 2** ✓ Simple Running Light(based on 68000 Microprocessor)  
✓ Basic DC Ammeter  
✓ Matlab software implementation

### Final Year Projects

#### Design And Implementation of 8-Bit Multiplier on FPGA(Field Programmable Gate Array)

- ✓ The final year project is about a multiplier operation is implemented by using FPGA. Multiplication is basically a shift add operation. Most computers use a "shift and add" algorithm to multiply small integers. To implement the idea of multiplier, FPGA is used. FPGA, shorts for Field Programmable Gate Array is a device that can be programmed after manufacturing, which means we can easily modify and reprogrammed without having major modifications.

### Languages

**Malay** (native)      **English** (fluent, Cambridge Level B3, MUET Band 3)

### References

#### Wan Hasmi bin Wan Kamal

- ✓ Staff Engineer (MIMOS BHD – Product Development : Analog Electronics Design)
- ✓ Email : whasmi.wkamal@mimos.my
- ✓ Contact Number : 0126280206