SIMULATING RECURRENT NEURAL NETWORKS ON GPUS

SUMMER PROJECT

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Introduction

Introduction

Definition

A *feedforward neural network* is an artificial neural network wherein connections between the units do not form a cycle.

Definition

A recurrent neural network is a class of artificial neural network where connections between units form a directed cycle.

FEEDFORWARD NEURAL NETWORK

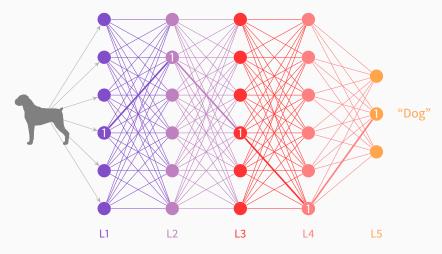


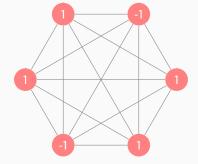
Figure 1: Feedforward Neural Network

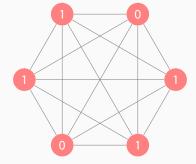


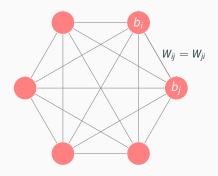
RECURRENT NEURAL NETWORK

HOPFIELD NETWORKS AND

BOLTZMANN MACHINES

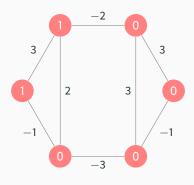




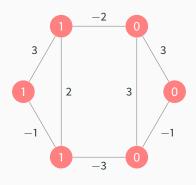


Energy configuration,
$$E = -\sum_{i < j} W_{ij} x_i x_j - \sum_i b_i x_i$$

Energy gap, $\Delta E_i = E(x_i = 0) - E(x_i = 1) = \sum_j W_{ij} x_j + b_i$
Update rule, $x_i := \begin{cases} +1 & \sum_j W_{ij} x_j + b_i \geq 0 \\ -1 & \text{otherwise} \end{cases}$

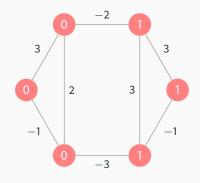


(1,0,0,0,0,1)

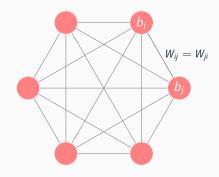


$$(1,0,0,0,0,1)$$

 $(1,1,0,0,0,1)$



BOLTZMANN MACHINES



$$E = -\sum_{i < j} W_{ij} x_i x_j - \sum_i b_i x_i$$

$$\Delta E_i = E(x_i = 0) - E(x_i = 1) = \sum_j W_{ij} x_j + b_i$$

$$\mathbb{P}(x_i = 1) = \frac{1}{1 + e^{-\Delta E_i / \tau}}$$

BOLTZMANN MACHINES

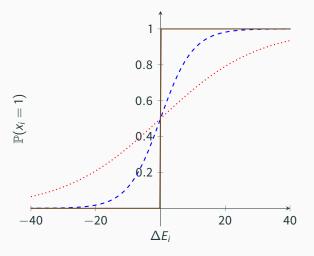
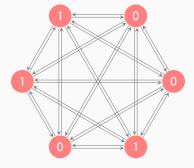
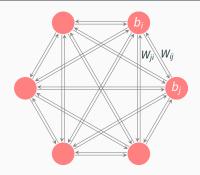


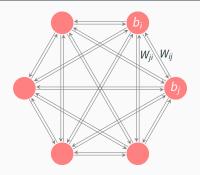
Figure 2: au= 0 (solid), au= 5 (dashed), au= 15 (dotted)





Transition Energy,
$$E(y, x|\theta) = -\sum_{ji \in E} W_{ji}y_jx_i - \sum_{j \in V} b_js_j - \sum_{i \in V} b_is_i$$

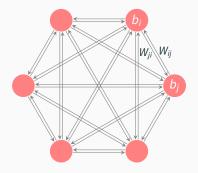
$$\Gamma_{yx} = \exp\left(-\frac{1}{2\tau}E(y, x|\theta) + \frac{1}{2\tau}E(x, x|\theta)\right)$$



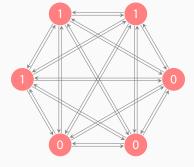
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$$E(y, x|\theta) = -\sum_{ji \in E} W_{ji}y_jx_i - \sum_{j \in V} b_js_j - \sum_{i \in V} b_is_i$$

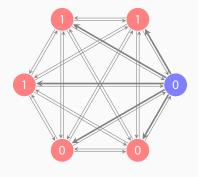
$$\Gamma_{yx} := \exp\left(\frac{1}{2\tau}s_jz_j\right)$$

where $s_j = 1 - 2x_j$, $z_j = \sum_j W_{ji}x_i + b_j$ and x, y differ by the jth unit.

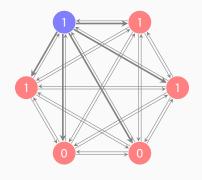


Transition probability from
$$x$$
 to y , $p_{yx} = \frac{\lambda_j}{\sum_{j'} \lambda_{j'}}$



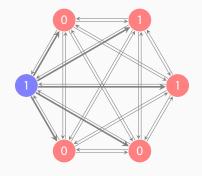


$$(T_0, (1, 0, 0, 0, 1, 1))$$



$$(T_0, (1, 0, 0, 0, 1, 1))$$

 $(T_1, (1, 0, 0, 1, 1, 1))$



$$(T_0, (1, 0, 0, 0, 1, 1))$$

$$(T_1, (1, 0, 0, 1, 1, 1))$$

$$(T_2, (1, 0, 0, 1, 1, 0))$$







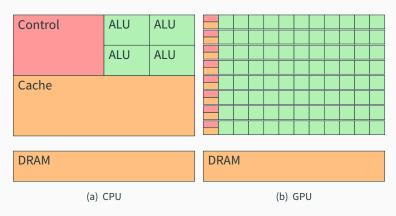
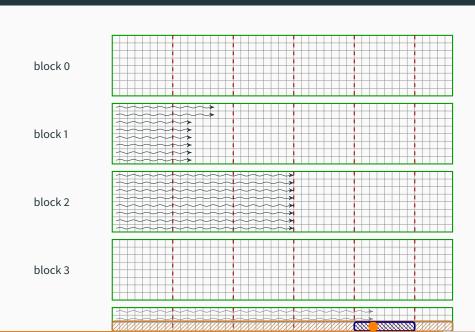


Figure 3: Comparison between the amount of transistors devoted to different functions inside a CPU and a GPU.



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The n-Category Café

From Set Theory to Type Theory

https://golem.ph.utexas.edu/category/2013/01/from_set_theory_to_type_theory.html



The nLab

Function Type

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