

## SUMMER PROJECT

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## INTRODUCTION

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## INTRODUCTION

### Definition

A **feedforward neural network** is an artificial neural network where connections between the units do not form a cycle.

### Definition

A **recurrent neural network** is an artificial neural network where connections between units form a directed cycle.

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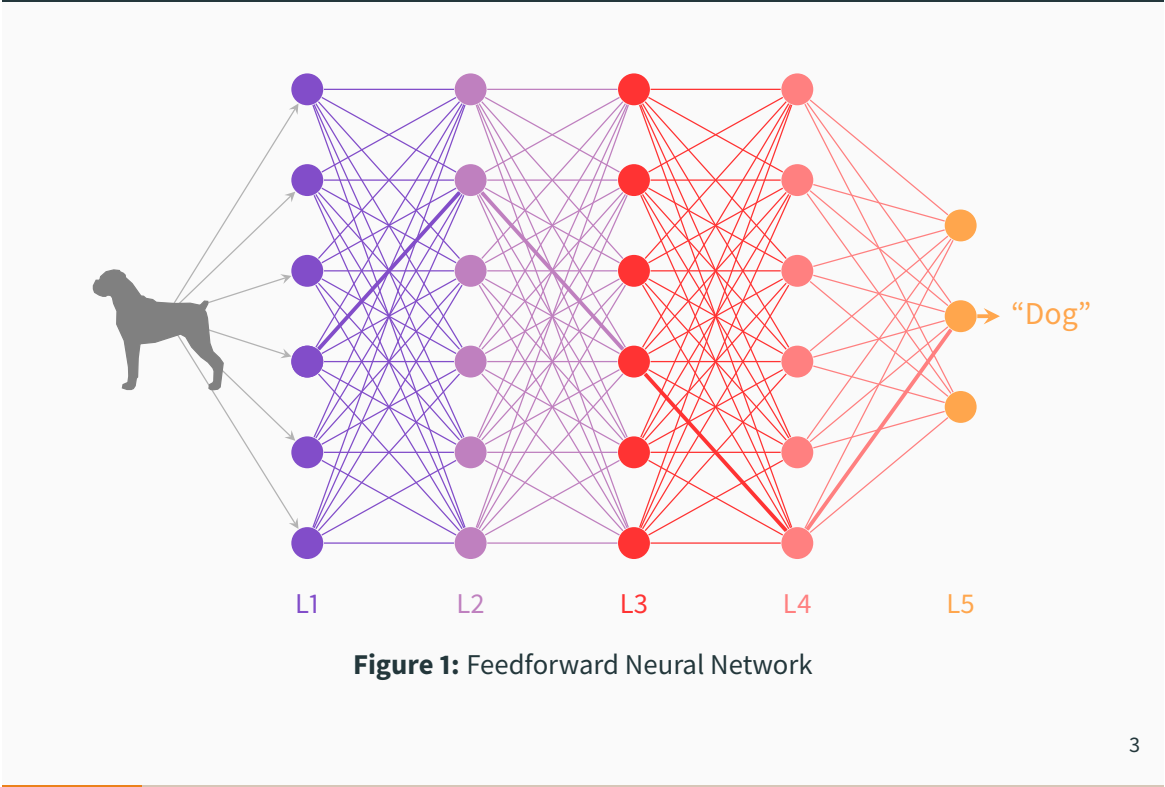
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## FEEDFORWARD NEURAL NETWORK



### Figure 1: Feedforward Neural Network

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## Notes

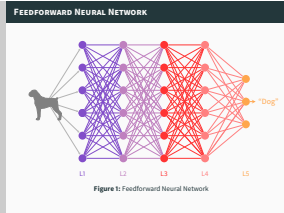
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2017-09-27

## Simulating Recurrent Neural Networks on Graphic Processing Units

## Introduction

## Feedforward Neural Network



- for feedforward neural networks, we can have supervised or unsupervised
- supervised: MNIST digits classification, unsupervised: autoencoders
- these neural networks are the more popular and mainstream ones, but today we are going to look at RNNs and how to simulate them.
- I will begin with a brief introduction of some RNNs and then introduce a RNN that does not

## Notes

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# RECURRENT NEURAL NETWORK

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## Simulating Recurrent Neural Networks on Graphic Processing Units

- Recurrent Neural Network

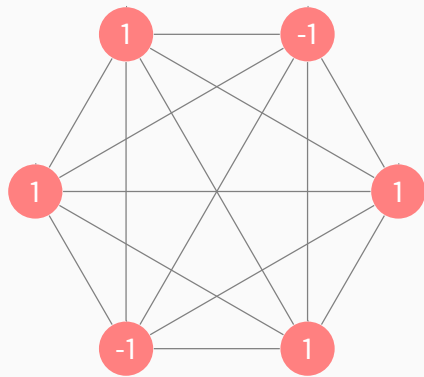
## RECURRENT NEURAL NETWORK

- Long short term memory (LSTM) is a RNN architecture that remembers values over arbitrary intervals
- LSTMs able to connect previous information to the present task, such as using previous video frames might inform the understanding of the present frame

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## HOPFIELD NETWORKS



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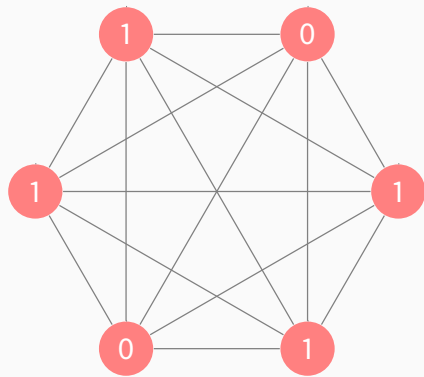
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## HOPFIELD NETWORKS



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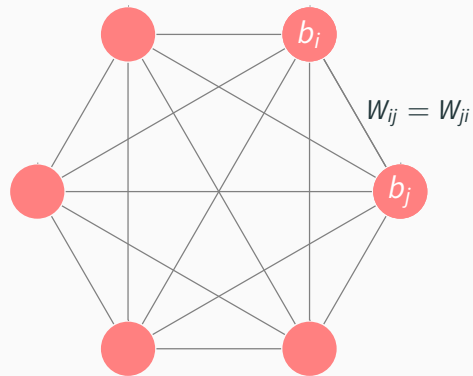
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## HOPFIELD NETWORKS



$$\text{Energy configuration, } E = - \sum_{i < j} W_{ij} x_i x_j - \sum_i b_i x_i$$

$$\text{Energy gap, } \Delta E_i = E(x_i = 0) - E(x_i = 1) = \sum_j W_{ij} x_j + b_i$$

$$\text{Update rule, } x_i := \begin{cases} +1 & \sum_j W_{ij} x_j + b_i \geq 0 \\ -1 & \text{otherwise} \end{cases}$$

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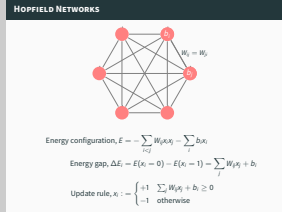
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## Simulating Recurrent Neural Networks on Graphic Processing Units

— Recurrent Neural Network

## └ Hopfield Networks

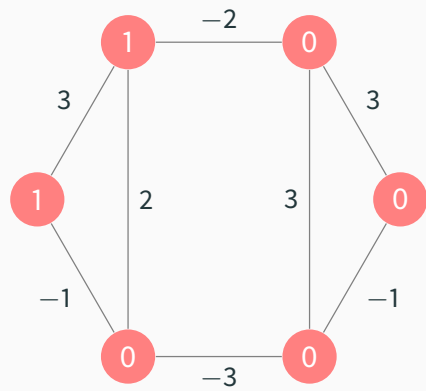


- composed of primitive computing elements called units
- units has two states, on or off, represented by  $\{1, -1\}$  or  $\{1, 0\}$
- connected to each other by bi-directional links
- adopts these states as a function of the states of its neighbouring units and weights of its links to them, it is a probabilistic function for a Boltzmann machine.
- weights can take on any real value
- a unit being on or off is taken to mean that the system currently accepts or rejects some elemental hypothesis of the domain
- weight on a link represents a weak pairwise constrain between two hypothesis
- positive (negative) weights indicate that two hypothesis support (contradict) one another with other things being equal
- link weights are symmetric, having the same strength in both directions

## Notes

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## HOPFIELD NETWORKS



(1, 0, 0, 0, 0, 1)

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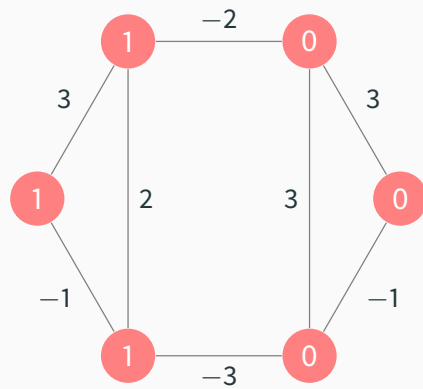
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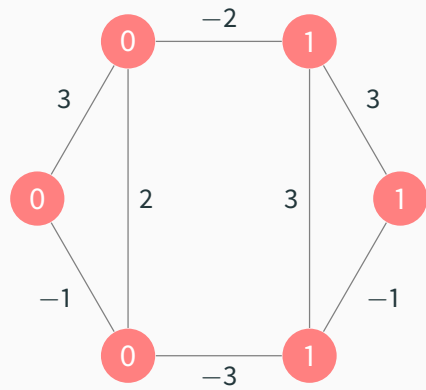
## HOPFIELD NETWORKS

 $(1, 0, 0, 0, 0, 1)$  $(1, 1, 0, 0, 0, 1)$ 

## Notes

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## HOPFIELD NETWORKS



(1, 0, 0, 0, 0, 1)

(1, 1, 0, 0, 0, 1)

(0, 0, 1, 1, 1, 0)

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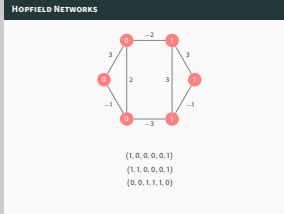
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## Simulating Recurrent Neural Networks on Graphic Processing Units

- Recurrent Neural Network

## — Hopfield Networks

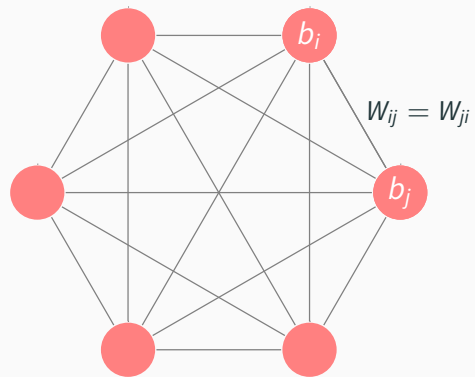


- Updating of Hopfield networks is done sequentially usually in a randomized order. Parallel updating might increase the energy instead.
- Hopfield networks always make decisions to reduce the energy and makes it impossible to escape from local minima.
- random noise can help us escape from poor minima, by starting with lots of noise so its easy to cross energy barriers and gradually decrease the noise so the system ends in a deep minimum. This is called simulated annealing.

## Notes

[illegible]

## BOLTZMANN MACHINES



$$E = - \sum_{i < j} w_{ij} x_i x_j - \sum_i b_i x_i$$

$$\Delta E_i = E(x_i = 0) - E(x_i = 1) = \sum_j w_{ij} x_j + b_i$$

$$\mathbb{P}(x_i = 1) = \frac{1}{1 + e^{-\Delta E_i / \tau}}$$

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## Simulating Recurrent Neural Networks on Graphic Processing Units

- Recurrent Neural Network

## Boltzmann Machines

**BOLTZMANN MACHINES**



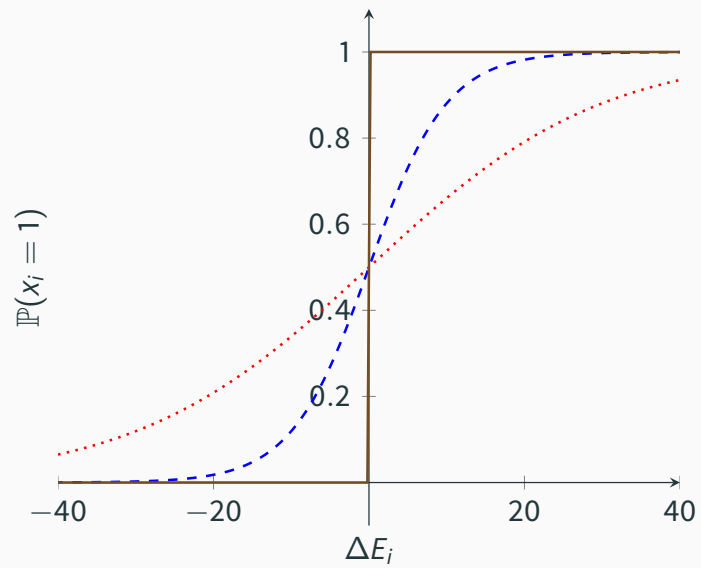
$$\begin{aligned} E &= -\sum_{i,j} W_{ij} x_i x_j - \sum_i b_i x_i \\ \Delta E_i &= E(x_i = 0) - E(x_i = 1) = -\sum_j W_{ij} x_j + b_i \\ P(x_i = 1) &= \frac{1}{1 + e^{-\Delta E_i / \tau}} \end{aligned}$$

- replace the binary threshold units by binary stochastic units that make biased random decisions
- temperature variable controls the amount of noise
- when  $\tau \rightarrow 0$  we get back the Hopfield network
- for  $\tau_1 > \tau_2$ , we are less likely to go to a lower energy state compared to in  $\tau_1$  compared to  $\tau_2$ , i.e. more likely to go to a higher energy state when the temperature is higher. This allows us to escape from local minimum and arrive at the global minimum
- we simulate the BM in the similar way to how it was in the Hopfield networks, but replace the binary threshold units with binary stochastic units

## Notes

[illegible]

## BOLTZMANN MACHINES



**Figure 2:**  $\tau = 0$  (solid),  $\tau = 5$  (dashed),  $\tau = 15$  (dotted)

## Notes

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# McCulloch-Pitts Machines

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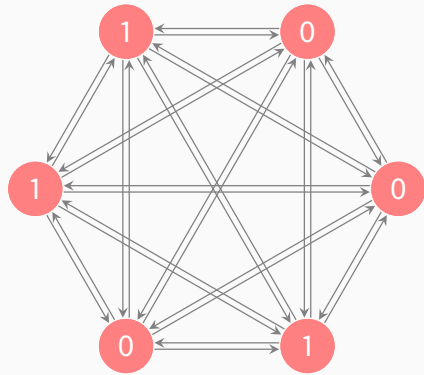
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## McCULLOCH-PITTS MACHINES



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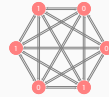
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## Simulating Recurrent Neural Networks on Graphic Processing Units

## └─ McCulloch-Pitts Machines

## └ McCulloch-Pitts Machines

**McCULLOCH-PITTS MACHINES**

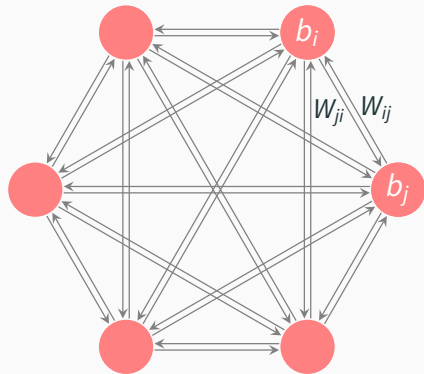


- state 1 is the refractory state, the neuron just fired and is unable to fire till it recovers
- state 0 is the armed state, the neuron just recovered and is waiting to fire
- here we model the units with the Nossenson-Messer neuron model, which explains biological firing rates in response to external stimuli

## Notes

[illegible]

## McCulloch-PITTS MACHINES



$$\text{Transition Energy, } E(y, x|\theta) = - \sum_{ji \in E} w_{ji} y_j x_i - \sum_{j \in V} b_j s_j - \sum_{i \in V} b_i s_i$$

$$\Gamma_{yx} = \exp \left( -\frac{1}{2\tau} E(y, x|\theta) + \frac{1}{2\tau} E(x, x|\theta) \right)$$

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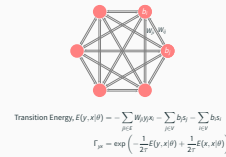
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## Simulating Recurrent Neural Networks on Graphic Processing Units

## └ McCulloch-Pitts Machines

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**McCULLOCH-PITTS MACHINES**

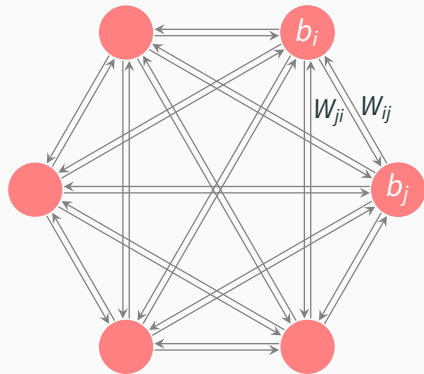


- digraph,  $G = (V, E)$ , weights  $W : V \rightarrow \mathbb{R}$ , biases  $b : E \rightarrow \mathbb{R}$ , binary states  $\mathbb{B} = \{0, 1\}$  with an initial distribution  $\mathbb{B}^{|V|} \rightarrow \delta$  and a temperature  $\tau$
- allow transitions where  $y$  and  $x$  differ by only one bit
- here the  $W$  matrix need not be symmetrical with zero diagonals like what we had in the Hopfield network and Boltzmann machine models
- for each  $y \neq x$ , start a Poisson process with rate  $\Gamma_{yx}$
- as such, we can talk about the interarrival timings of the Poisson process and our simulation of the McCulloch-Pitts machine not only gives us a binary tuple, but also the time taken from it to transit from its earlier state

## Notes

[illegible]

## McCULLOCH-PITTS MACHINES



$$\text{Transition Energy, } E(y, x|\theta) = -\sum_{ji \in E} W_{ji} y_j x_i - \sum_{j \in V} b_j s_j - \sum_{i \in V} b_i s_i$$

$$\Gamma_{yx} := \exp \left( \frac{1}{2\tau} s_j z_j \right)$$

where  $s_j = 1 - 2x_j$ ,  $z_j = \sum_i W_{ji}x_i + b_j$  and  $x, y$  differ by the  $j$ th unit.

## Notes

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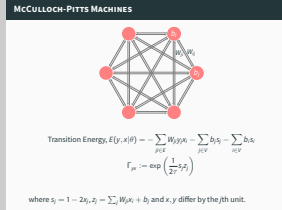


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## Simulating Recurrent Neural Networks on Graphic Processing Units

## └ McCulloch-Pitts Machines

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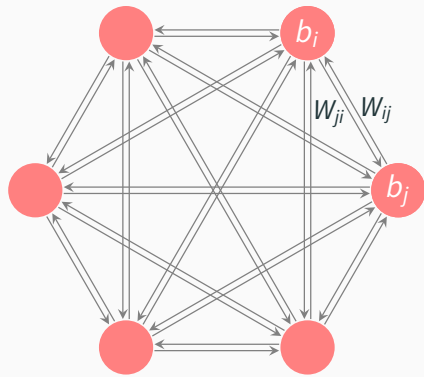


- when doing the updates we can just update the linear responses  $z_j$  and apply softmax on the  $\lambda_j$ 's to get the probability distribution of the transitions.
- it seems counter-intuitive to think of 0 as armed and 1 as refractory, but it is in fact the most natural thinking
- a transition from  $0 \rightarrow 1$  is a act of firing and a transition from  $1 \rightarrow 0$  is the act of recovery
- when a neuron transit from  $0 \rightarrow 1$ , it changes the value of the linear response; for a transiting neuron  $i$ , if  $W_{ji} > 0$ , then such a transition increases the linear response of neuron  $j$  and if  $W_{ji} < 0$  it decreases the linear response of neuron  $j$
- the sign  $s$  depends on the state of the neuron, it preserves the sign of the linear response if it is armed and flips the sign of the linear response if it is refractory

## Notes

[illegible]

## McCulloch-Pitts Machines



Transition probability from  $x$  to  $y$ ,  $p_{yx} = \frac{\lambda_j}{\sum_{j'} \lambda_{j'}}$

## Notes

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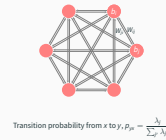
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## Simulating Recurrent Neural Networks on Graphic Processing Units

## └─ McCulloch-Pitts Machines

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**McCULLOCH-PITTS MACHINES**



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## Notes

[illegible]

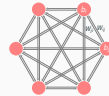
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## Simulating Recurrent Neural Networks on Graphic Processing Units

## └─ McCulloch-Pitts Machines

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**McCULLOCH-PITTS MACHINES**



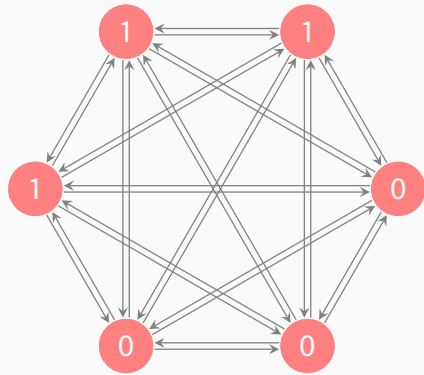
Transition probability from  $x$  to  $y$ ,  $p_{yx} = \frac{\lambda_y}{\sum_j \lambda_j}$

- McCulloch-Pitts machine is continuous time; we have a

## Notes

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## McCULLOCH-PITTS MACHINES



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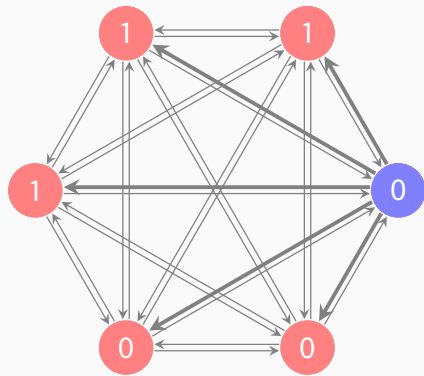
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## McCULLOCH-PITTS MACHINES



$(T_0, (1, 0, 0, 0, 1, 1))$

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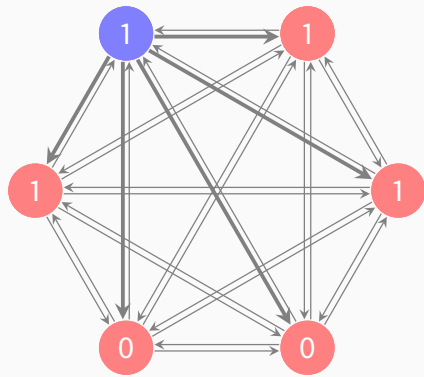
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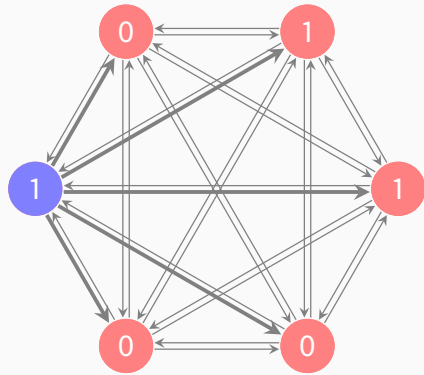
## McCulloch-Pitts Machines


$$(T_0, (1, 0, 0, 0, 1, 1))$$
$$(T_1, (1, 0, 0, 1, 1, 1))$$

## Notes

[illegible]

## McCulloch-Pitts Machines


$$(T_0, (1, 0, 0, 0, 1, 1))$$
$$(T_1, (1, 0, 0, 1, 1, 1))$$
$$(T_2, (1, 0, 0, 1, 1, 0))$$

## Notes

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# McCulloch-Pitts Machines

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# McCulloch-PITTS MACHINES

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## SIMULATING ON GPUS

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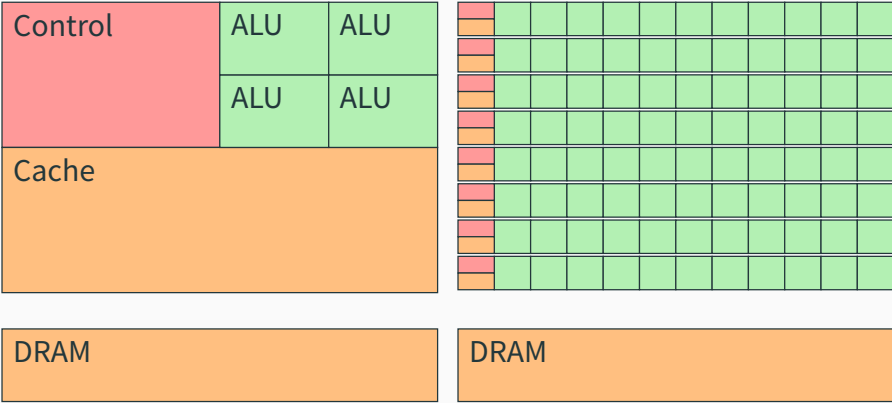
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## SIMULATING ON GPUS



**Figure 3:** Comparison between the amount of transistors devoted to different functions inside a CPU and a GPU.

## Notes

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# Simulating Recurrent Neural Networks on Graphic Processing Units

- └ Simulating on GPUs
- └ Simulating on GPUs

**Figure 3:** Comparison between the amount of transistors devoted to different functions inside a CPU and a GPU.

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# Simulating Recurrent Neural Networks on Graphic Processing Units

- └ Simulating on GPUs
- └ Simulating on GPUs

**Figure 3:** Comparison between the amount of transistors devoted to different functions inside a CPU and a GPU.

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# Simulating Recurrent Neural Networks on Graphic Processing Units

- └ Simulating on GPUs
- └ Simulating on GPUs

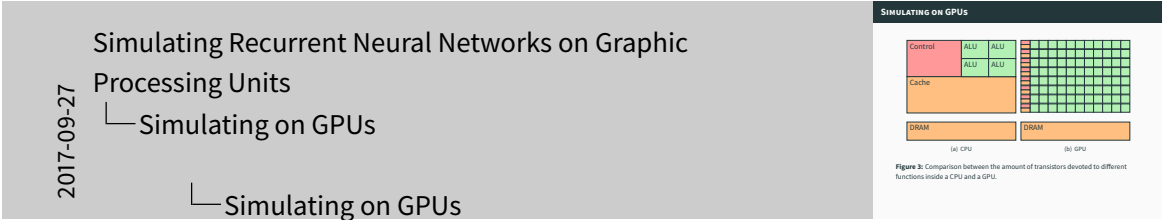
**Figure 3:** Comparison between the amount of transistors devoted to different functions inside a CPU and a GPU.

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# Simulating Recurrent Neural Networks on Graphic Processing Units

- └ Simulating on GPUs
- └ Simulating on GPUs

**Figure 3:** Comparison between the amount of transistors devoted to different functions inside a CPU and a GPU.



- To simplify quite a bit, think of a GPU as a factory and a CPU as Steven Hawking. Factory workers, each represented by a core, can complete lots of easy, similar tasks with incredible efficiency?tasks like geometry and shading. On the other hand Mr. Hawking, while incredibly smart and only occasionally baffled, is just one man. His skill set is better used on singular, complex problems like artificial intelligence.
- DRAM: dynamic random access memory, ALU: arithmetic logic unit,Cache, Control
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# SIMULATING ON GPUS

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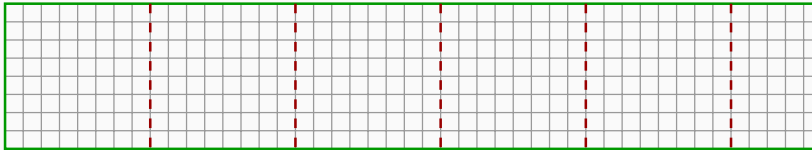
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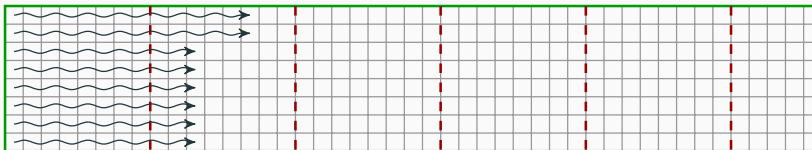
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## SIMULATING ON GPUS

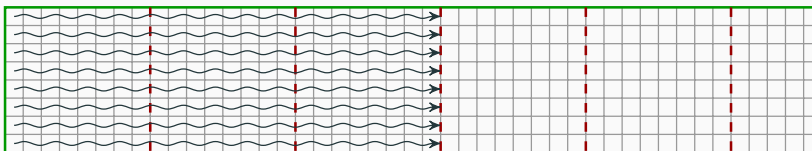
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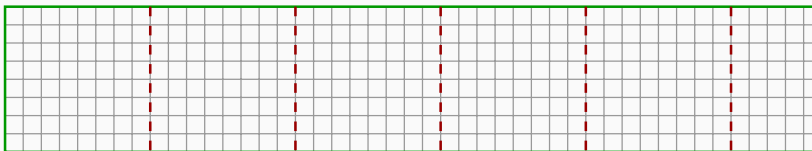
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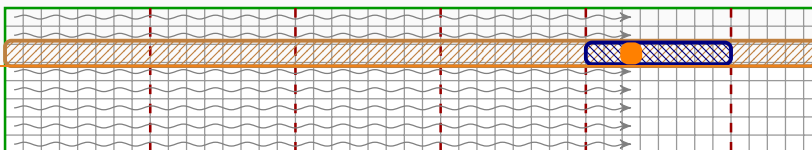
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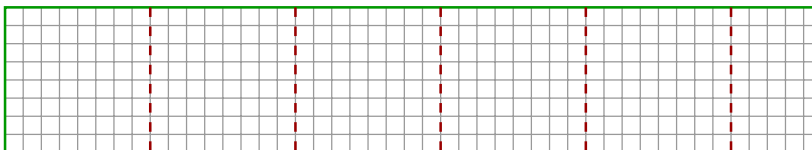


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# SIMULATING ON GPUS

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## REFERENCES



J. Macor.

## A Brief Introduction to Type Theory and the Univalence Axiom

<http://math.uchicago.edu/~may/REU2015/REUPapers/Macor.pdf>



## The Univalent Foundations Program

# Homotopy Type Theory: Univalent Foundations of Mathematics.

<https://homotopytypetheory.org/book>



## The n-Category Café

## From Set Theory to Type Theory

[https://golem.ph.utexas.edu/category/2013/01/from\\_set\\_theory\\_to\\_type\\_theory.html](https://golem.ph.utexas.edu/category/2013/01/from_set_theory_to_type_theory.html)



## The *n*Lab

### Function Type

<https://ncatlab.org/nlab/show/function+type>

## Notes

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