

Quiz 5 DLD (SE'23-A)

Name: _____

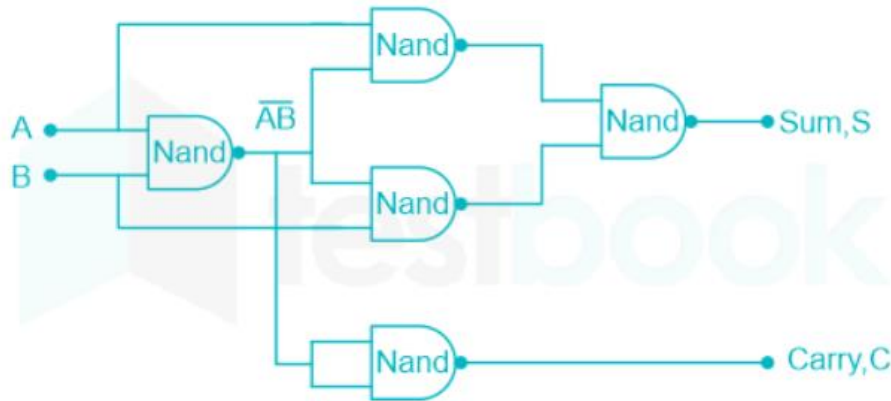
Roll no: _____

Time: 15 minutes

Total Marks: 10

1. Design a Half Adder using Nand gates

[5]



2. A full adder adds:

[2]

a) 4 bits b) 2 bits c) 3 bits

3. In Half adder total number of inputs and outputs are:

[2]

a) 3,2 b) 2,2 c) 2,1

4. How many input lines are there in full adder:

[1]

a) 3 b) 2 c) 4