

Information Technology University of the Punjab
SE301T Operating Systems – Spring 2025
Quiz 4 [CLO2] – April 15, 2025

Name: _____ Solution _____
Time allowed: 15 minutes

Roll No.: _____
Maximum Marks: 15

1. How does a buddy allocator merge free blocks? [2]

To merge, a buddy allocator checks if the addresses of the two free blocks differ only by one bit and if they are of the same size.

2. What would the following free list look like after an allocation request of size 12 is completed in case of (a) best fit and (b) worst fit? [3]



- (a) head → 10 → 30 → 8
(b) head → 10 → 18 → 20

3. Assume the following:
- Page size: 32 bytes
 - Address space: 32KB
 - Physical memory size: 4KB
 - Size of Page Directory Entry (PDE) and Page Table Entry (PTE): 1 byte
 - Both PDE and PTE only have a valid bit in addition to the PFN, so have this bit format: VALID | PFN bits (x ... 0)

Answer the following questions:

- i. How many bits would be there in the virtual address? [1]

32KB = $2^5 \times 2^{10} = 2^{15}$ bytes, so 15 bits.

- ii. How many bits would the offset have in the virtual address? And how many would be there for the virtual page number? [1]

As page size is 32 (2^5) bytes, the offset will be 5-bit wide. The bits for the VPN will then be: $15 - 5 = 10$

- iii. How many page frames are there in the physical memory? [1]

Physical memory size / Page frame size = $4\text{KB}/32\text{B} = 2^7$ page frames.

- iv. Considering two level paging, and the page directory to be 1 page long, how many bits of the VA will be used to index into the page directory? How many bits would be required to index into the page table? [1]

5 bits to index into the page directory

5 bits to index into the page table

- v. Given that the value of page directory base register (PDBR) is 30, which tells you that the page directory is located at page number 30, and a complete dump of each page of memory (remember, each page has 32 bytes), find the physical address translation (if valid, you would need to check the valid bit) and the value stored at this physical address for the following two virtual addresses: [6]

VA1: 0x4ea6 (0 100 1110 1010 0110) (PDEIndex: 19, PTEIndex: 21, offset: 6)

VA2: 0x21b0 (0 010 0001 1011 0000) (PDEIndex: 8, PTEIndex: 13, offset: 16)

Page dump:

[illegible][illegible]

VA1: 0x4ea6 ---- PDE: *dc (1101 1100, valid as highest bit is 1)*. PT frame is *101 1100 (92)*. PTE is *cc (1100 1100, valid)*. Page frame number of physical address: *1001100 (76)*. Physical address is *10 01100 00110 (value is: 08)*.

VA2: 0x21b0 ---- PDE: 94 (1001 0100, valid as highest bit is 1). PT frame is 0010100 (20). PTE is 7f (0111 1111, invalid as highest bit is 0). So, virtual address is not valid.