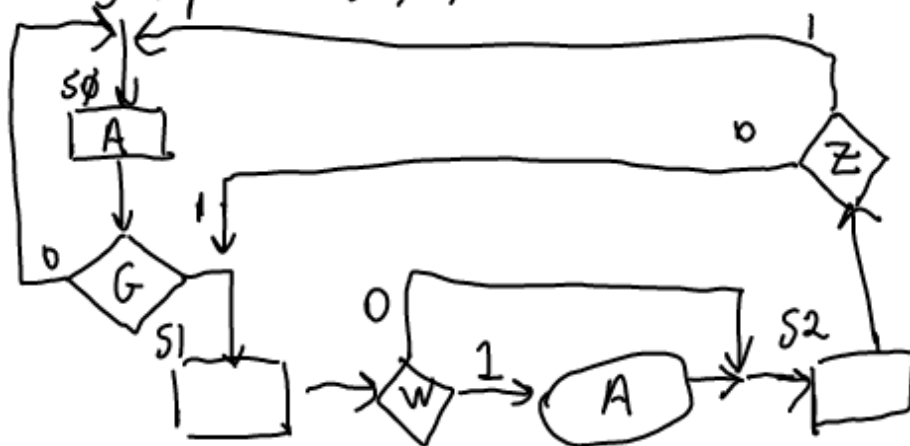
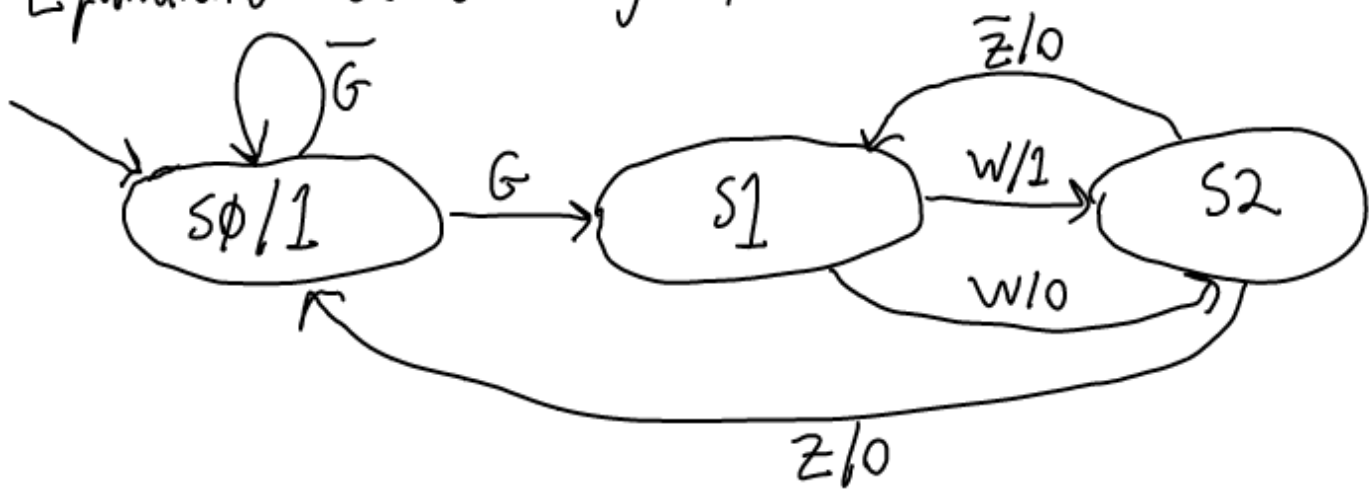


Example: ASM for circuit with three states (S_0, S_1, S_2), 3 inputs (G, W, Z) and one output (A)

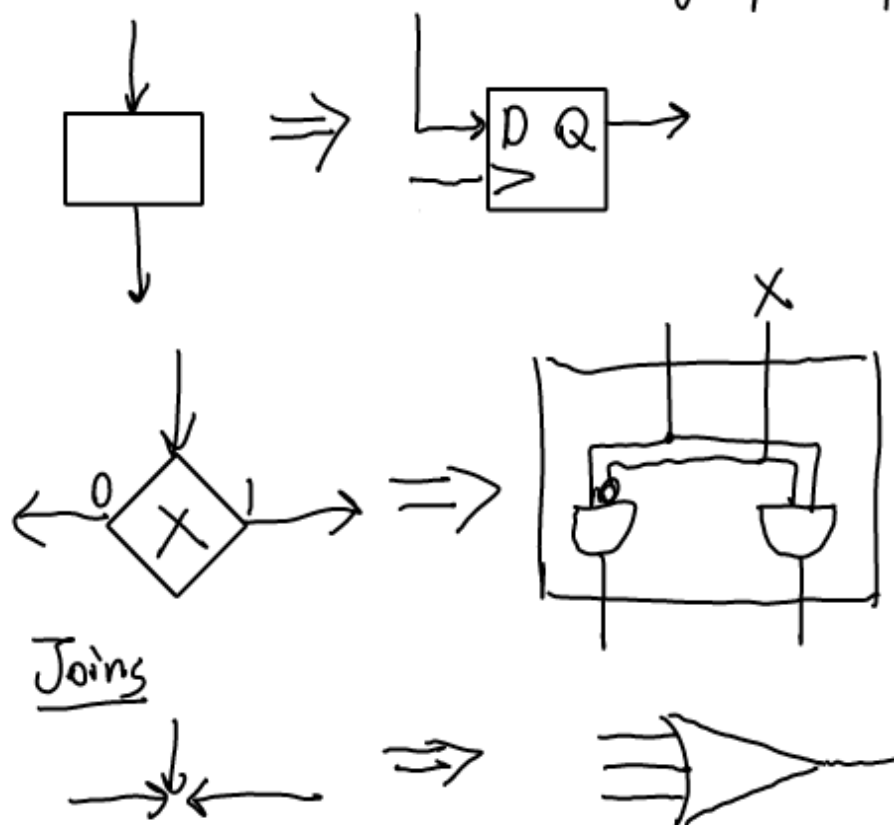


Equivalent State Diagram:

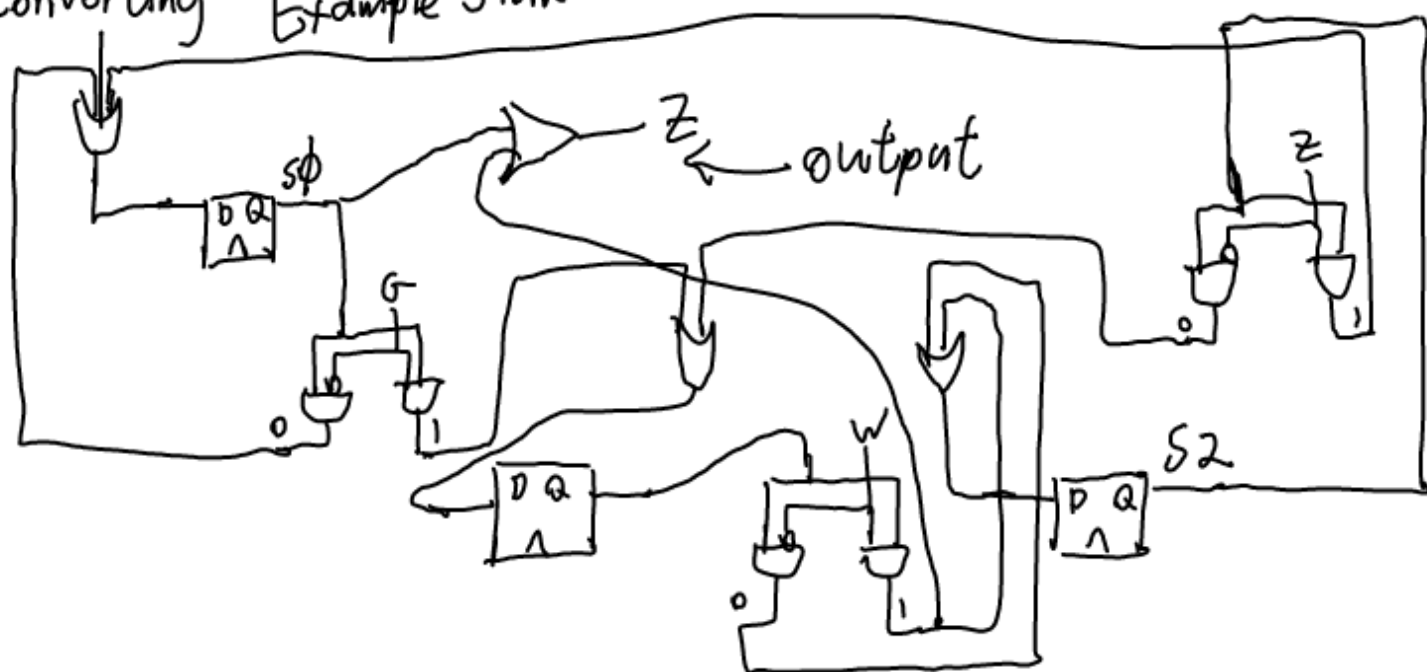


ASM + one hot encoding

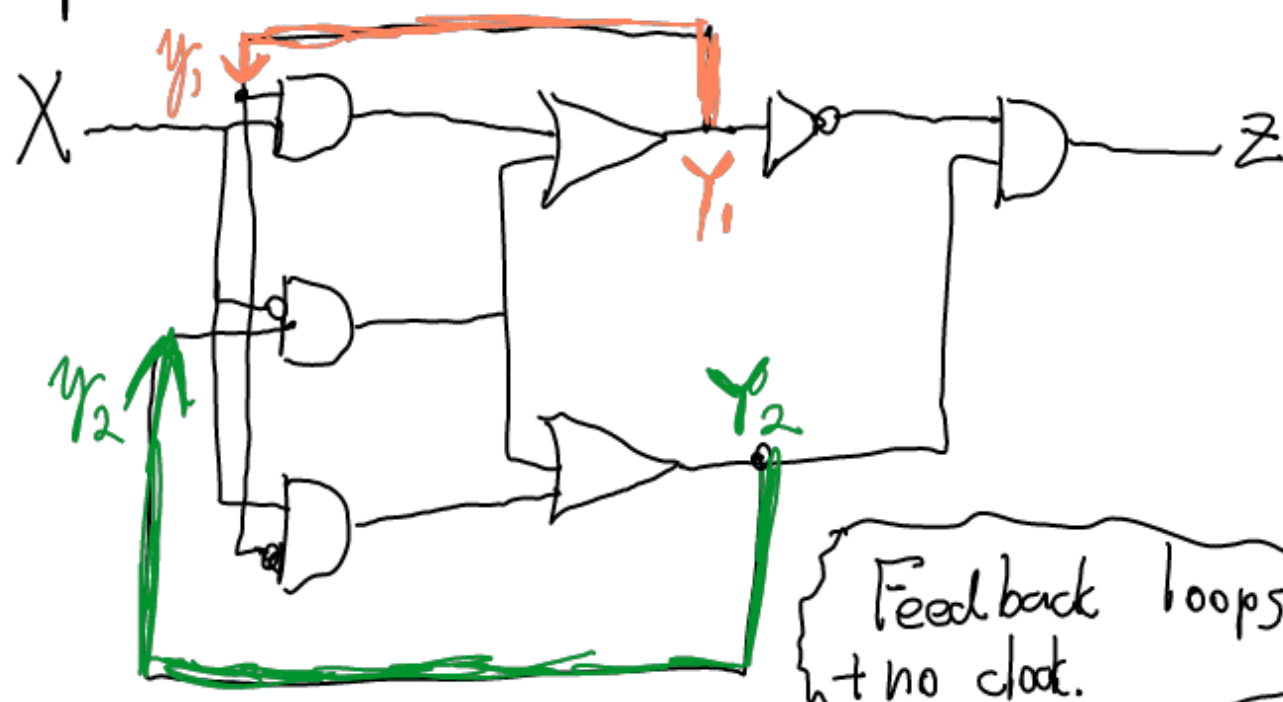
\Rightarrow ASM can be converted graphically.



Converting Example from last page:



Consider the following circuit. What's different about it. Compared to comp circuits or clocked sequential circuits?



Turns out that a circuit with feedback paths has the concept of memory state.

Question is how to analyze + design such circuits.

Y_1, Y_2 are excitation variables. "next state".

y_1, y_2 are secondary variables. "curr state".

