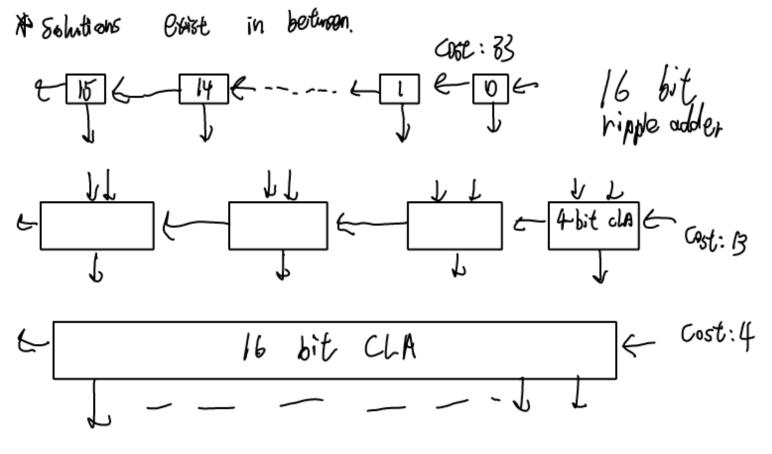
Last time: \* n-bit ripple addler. -> follows logic of how we add. -> Show for large n due to the worst path down the carry Chain. \*Q: What can we do to improve performance? A: Shorten Critical path; in this circuit it means producing carries faster. j-th stage of n-bit addler g; "gene rato" Ci+1 = dibi + Ci(a: 46i) = 9; +Cip; Cn Cn-1 C2 C2 C2 C1= gotcopo

C2=9,+C1P, = 9,+P,g0+P,P6Co C3=92+C2P2 = 92+P29++P2P,g0+P2P,P6Co

By substituting, all Cis oute New 2 levels from the Pi's and gis. The pi's & gi's ove 1 had from the inputs. -- all Cis one now 3-levels from the inputs. Cn 6 1 6 --- 82 1 6 --- 60 5n-2 5n-1 Si=a:⊕bi⊕ci l-level → S<sub>0</sub> } 4<sub>5</sub> J 953 Pis 2-level Cn a carry-lookahead adder (CLA) is could This arba alley pensormane trade performance



In base-2, the 2s complement is formed by flipping the bits and adding 1.

$$\frac{0101000}{11}$$

Subtraction of unsigned binary number.

Consider M-N.

Instead consider adding the r's complement of N.

2 0485.

2) M<N > underslaw

$$\frac{5}{3} \longrightarrow \frac{101}{2} \longrightarrow \frac{101}{101}$$

$$M-N \ge 2$$

use 3-bits

$$\frac{-\frac{3}{5}}{-\frac{7}{2}} \qquad \frac{-\frac{3}{10}}{-\frac{7}{2}} \rightarrow \frac{011}{\cancel{8}\cancel{1}\cancel{1}\cancel{0}}$$

25 comp of 5-3