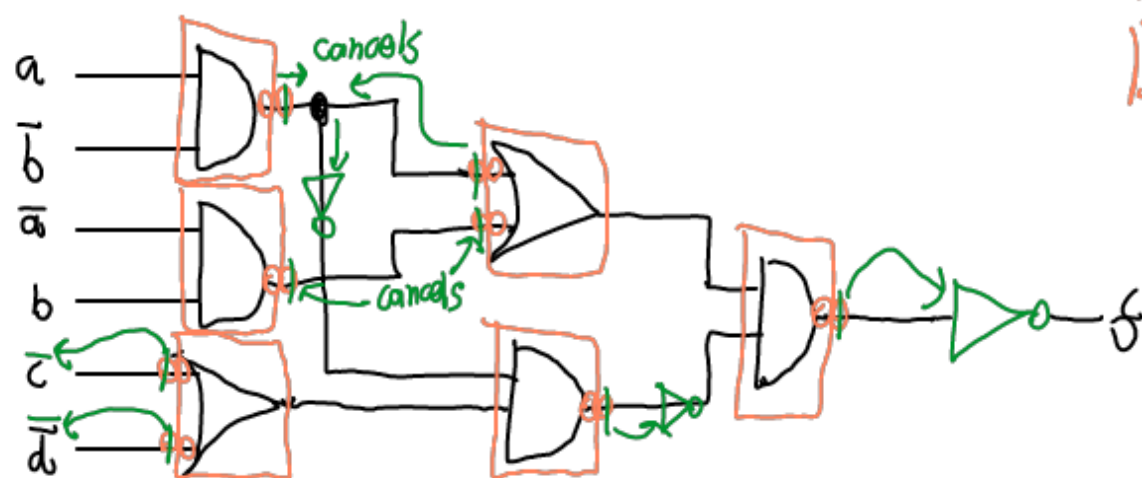


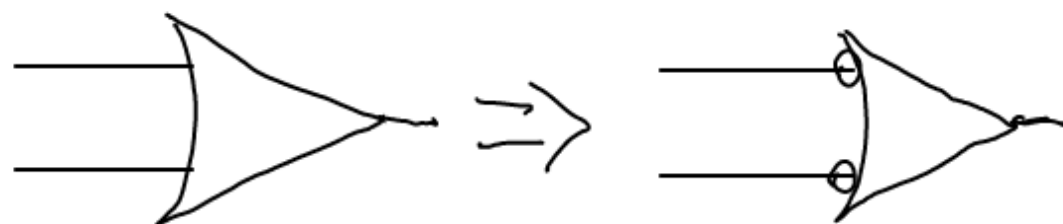
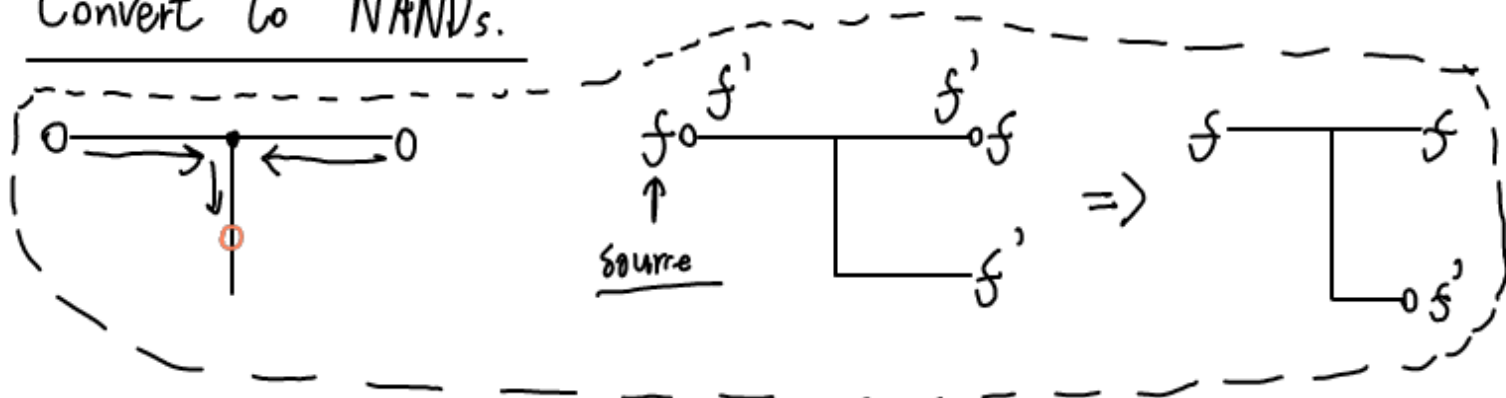
Multilevel NAND/NOR conversion.

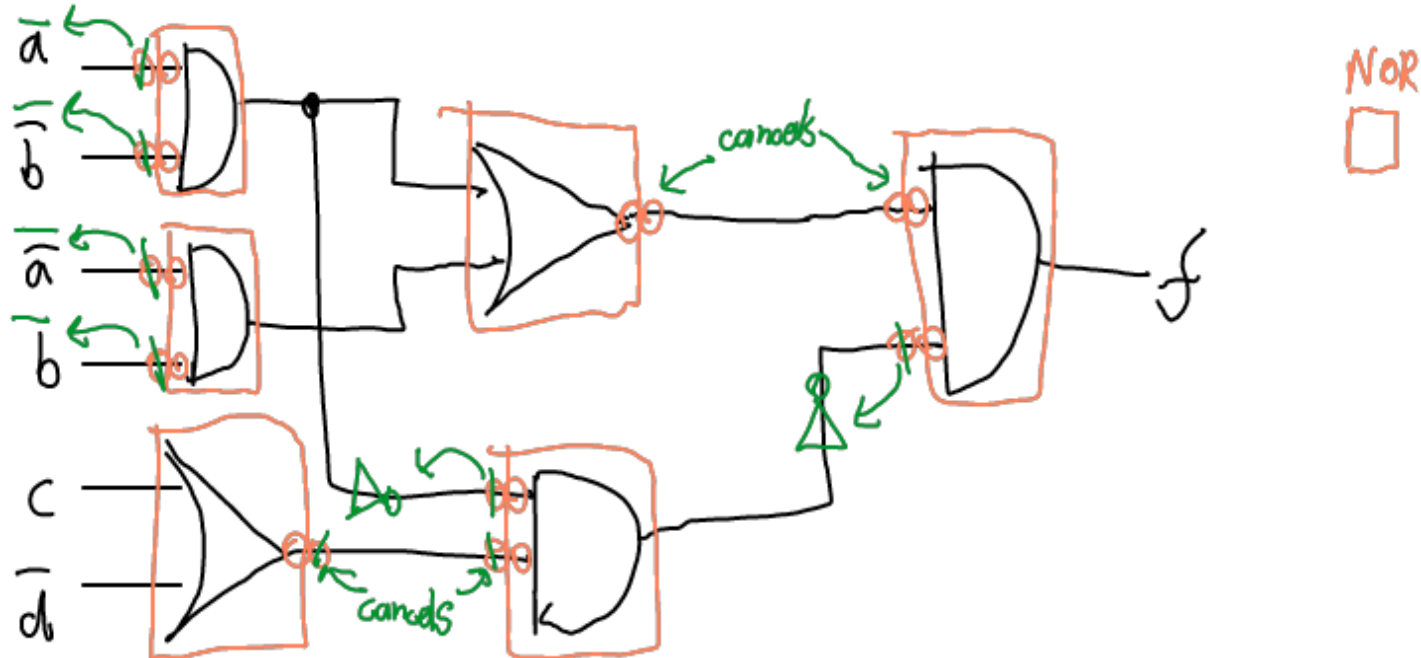
* Up to now, we have seen mostly 2-level circuits, but many circuits are multileveled. (≥ 3 levels of logic between inputs & outputs).



NAND


Convert to NANDs.





Convert to NORs:

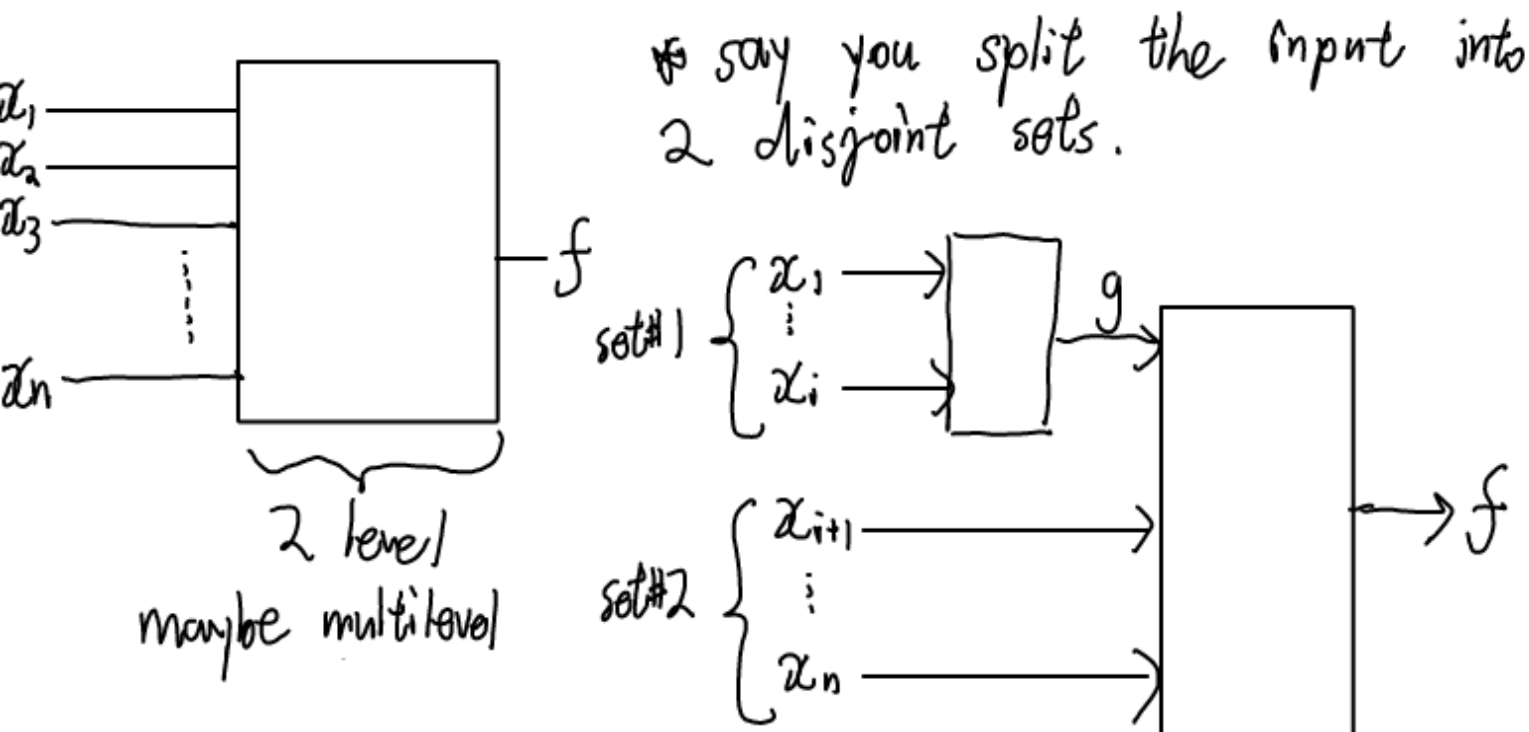


Factoring

* Maybe a 2-level circuit is large/costly. So, perhaps we can try to make a multilevel circuit instead.

* Lots of possible ways, so we will consider just one way.

* consider a function $f = f(x_1, x_2, x_3, \dots, x_n)$



$$f = f(x_1, \dots, x_n)$$

$$= f(g(x_1, \dots, x_i), x_{i+1}, \dots, x_n)$$

Try to find a minimum SOP.

ba		00	01	11	10
dc	00	1	0	0	0
	01	1	1	1	1
	11	1	0	0	0
	10	0	1	1	1

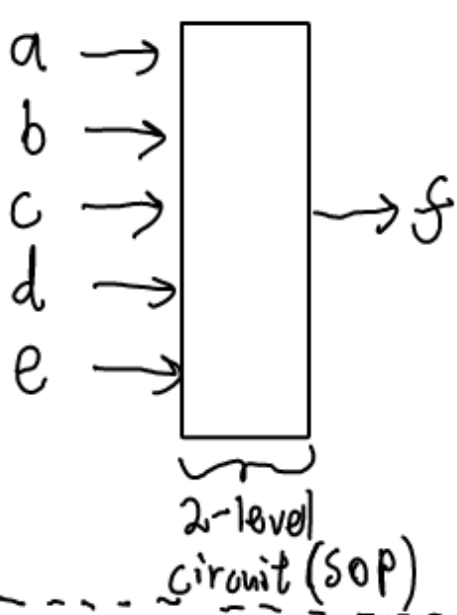
$\bar{a}\bar{b}\bar{d}\bar{e}$ (circled 1 at 00,00)
 $\bar{a}\bar{b}c\bar{e}$ (circled 1 at 01,00)
 $e=0$ (circled 1s in row 00)
 $a\bar{c}d$ (circled 1s at 01,11 and 10,01)
 $b\bar{c}d$ (circled 1s at 11,01 and 10,11)
 cd (circled 1s in row 01)

ba		00	01	11	10
dc	00	0	0	0	0
	01	1	1	1	1
	11	0	0	0	0
	10	1	1	1	1

$e=1$ (circled 1s in row 10)

$$f = cd\bar{e} + \bar{c}de + b\bar{c}d + a\bar{c}d + \underbrace{\bar{a}\bar{b}\bar{d}\bar{e} + \bar{a}\bar{b}c\bar{e}}_{\bar{a}\bar{b}\bar{e}(cd + c)}$$

6 AND
 1 OR
 19 inputs (AND)
 6 inputs (OR)
 32 Cost

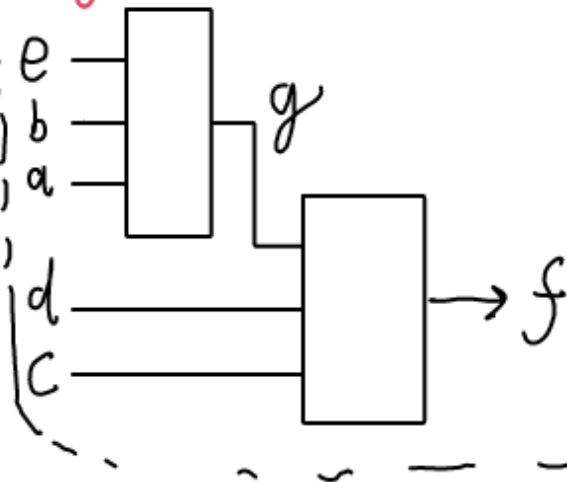


consider splitting inputs into sets $\{a, b, c\}, \{dc\}$.

dc	e	b	a	000	001	010	011	100	101	110	111
00				1	0	0	0	0	0	0	0
01				1	1	1	1	1	1	1	1
11				1	0	0	0	0	0	0	0
10				0	1	1	1	1	1	1	1
$g:$				0	1	1	1	1	1	1	1

e	b	a	g
0	0	0	0
0	0	1	1
0	1	0	1
0	1	1	1
1	0	0	1
1	0	1	1
1	1	0	1
1	1	1	1

dc	g	0	1
00		1	0
01		1	1
11		1	0
10		0	1



$$f = \bar{g}\bar{d} + \bar{g}c + d\bar{c} + gdc$$