```
MACHINE SR_M0
SEES SR0_Path
 VARIABLES
                     Route_Req
                     Route\_Cel
                     Route\_Occ
                     Route2Path
INVARIANTS
                     inv1: Route\_Req \subseteq ROUTE
                     inv2: Route\_Cel \subseteq ROUTE
                     inv3: Route\_Occ \subseteq ROUTE
                     inv4: Route2Path \in ROUTE \rightarrow PATH
                     \textbf{inv5}: \ \forall r1, r2 \cdot (r1 \neq r2 \land r1 \in dom(Route2Path) \land r2 \in dom(Route2Path)) \Rightarrow (PathConflict^{-1}[Route2Path[\{r1\}]] \cap (PathConflict^{-1}[Route2Path])) \Rightarrow (PathConflict^{-1}[Route2Path]) \cap (PathConflict(Route2PathConflict(Route2PathConflict(Route2PathConflict(Route2PathConflict(Route2PathConflict(Route2PathCon
                                  Route2Path[\{r2\}] = \emptyset)
                     inv6: \forall r \cdot r \in Route\_Occ \Rightarrow (Route2Path[\{r\}] \neq \emptyset)
EVENTS
Initialisation
                  begin
                                     act1: Route\_Req := \emptyset
                                     act2: Route\_Cel := \emptyset
                                     act3: Route\_Occ := \emptyset
                                     act4: Route2Path := \emptyset
                  end
Event ATS_Request (ordinary) \hat{=}
                  any
                  where
                                     grd1: r \notin Route\_Req
                  then
                                     act1: Route\_Req := Route\_Req \cup \{r\}
                  end
Event Route_Reserve (ordinary) \hat{=}
                  any
                  where
                                     grd1: r \in Route\_Req
                                     grd2: r \notin Route\_Cel
                                     grd3: r \notin dom(Route2Path)
                                     {\tt grd4:} \quad PathConflict[Route2InitPath[\{r\}]] \cap ran(Route2Path) = \varnothing
                  then
                                     act1: Route2Path := Route2Path \cup \{r \mapsto Route2InitPath(r)\}
 Event Train_Enter (ordinary) \hat{=}
                  any
                  where
                                     grd1: r \in dom(Route2Path)
                                     grd2: r \notin Route\_Occ
                  then
                                     act1: Route\_Occ := Route\_Occ \cup \{r\}
Event Route_Sequential_Release (ordinary) \hat{=}
                  any
                                     ср
                                     sp
```

16.03.2024 19:21 Page 1 of 2

```
where
             \mathbf{grd1} \colon \ r \in Route\_Occ
             \texttt{grd2:} \quad r \in dom(Route2Path)
             {\tt grd3:} \quad cp = Route2Path(r)
             grd4: cp \neq NullPath
             grd5: sp \in PathSub[\{cp\}]
      then
             act1: Route2Path(r) := sp
      end
Event Train_Leave (ordinary) \hat{=}
      any
      where
             grd1: r \in Route\_Occ
             grd2: r \in dom(Route2Path)
             grd3: Route2Path(r) = NullPath
      then
             act1: Route\_Occ := Route\_Occ \setminus \{r\}
      end
Event Route_Release ⟨ordinary⟩ =
      any
      where
             grd1: r \in dom(Route2Path)
             grd2: Route2Path(r) = NullPath
             grd3: r \notin Route\_Occ
      then
             \verb"act1": Route2Path := \{r\} \lhd Route2Path
             act2: Route\_Req := Route\_Req \setminus \{r\}
      end
Event ATS_Cancel (ordinary) \hat{=}
      any
      where
             grd1: r \in Route\_Req
      then
             act1: Route\_Cel := Route\_Cel \cup \{r\}
      end
Event Route_Cancel ⟨ordinary⟩ =
      any
      where
             \mathbf{grd1} \colon \ r \in Route Req
             grd2: r \in Route\_Cel
             grd3: r \notin Route\_Occ
             grd4: r \in dom(Route2Path)
      then
             act1: Route\_Req := Route\_Req \setminus \{r\}
             act2: Route\_Cel := Route\_Cel \setminus \{r\}
             \verb"act3": Route2Path := \{r\} \lhd Route2Path
      end
END
```

16.03.2024 19:21 Page 2 of 2