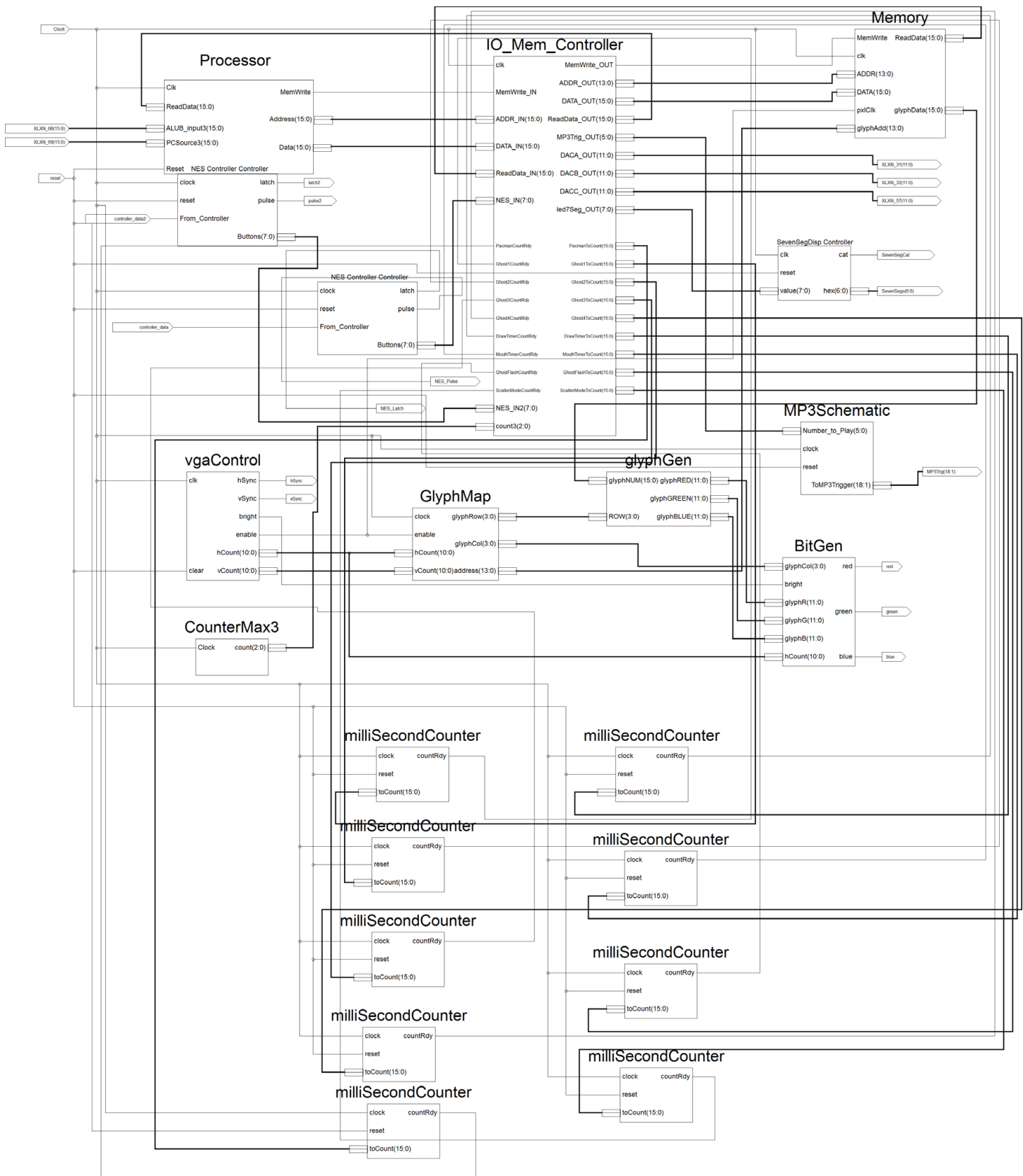


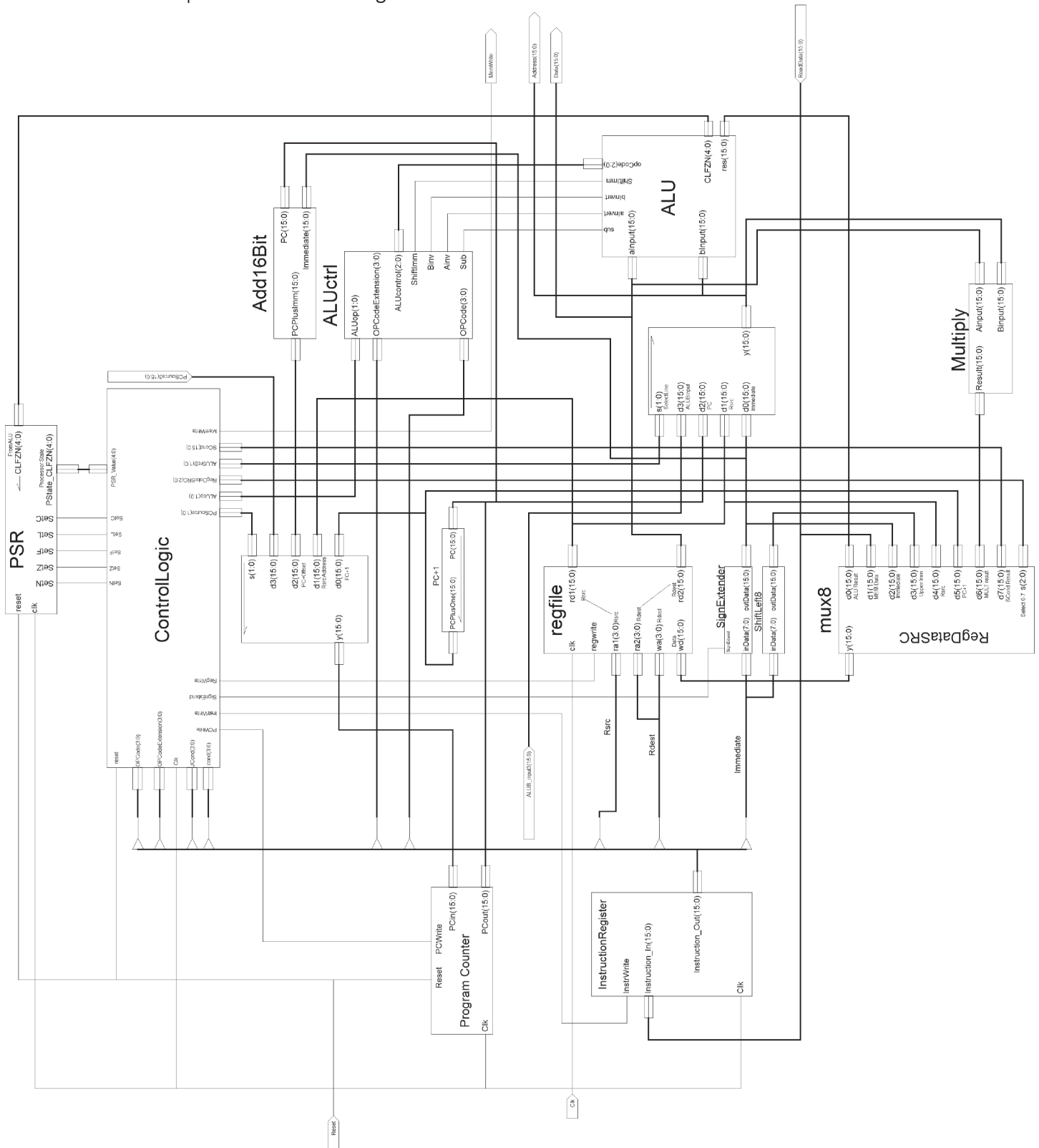
Top Level Schematic:

Processor, memory controller, and all I/O devices



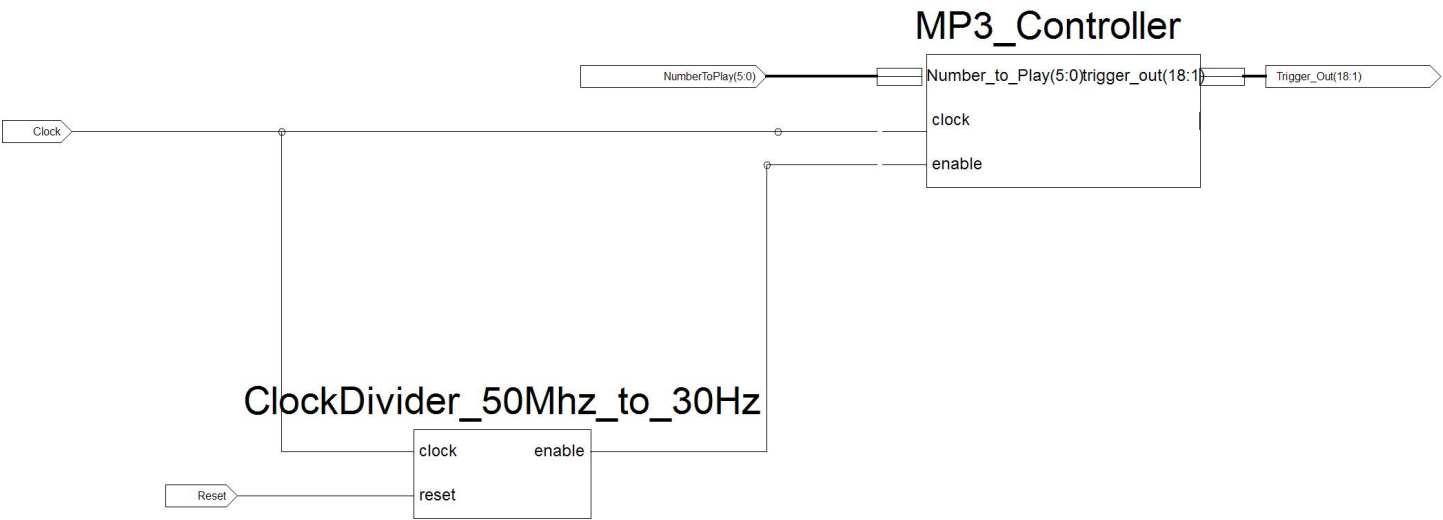
Processor Schematic:

Processor data path and control logic



MP3 Controller Schematic:

MP3 Controller with clock divider.



NES Controller Schematic:

Clock divider and NESController state machine.

