

Finite Computation

I. DEFINING COMPUTATION

Computers are stupid. We need to describe algorithms precisely.

Informal definition of an algorithm: An Algorithm is a set of instructions of how to compute an output from an input by following a sequence of “elementary steps”. An algorithm A computes a function F if for every input x , if we follow the instructions of A on the input x , we obtain the output $F(x)$.

A. Boolean circuit

Definition 1 (Boolean circuits) Let n, m, s be positive integers with $s \geq m$. A Boolean circuit with n inputs, m outputs, and s gates, is a labeled directed acyclic graph (DAG) $G = (V, E)$ with $s + n$ vertices satisfying the following properties:

1. Exactly n of the vertices have no in-neighbors. These vertices are known as inputs and are labeled with the n labels: $X[0], \dots, X[n-1]$.
2. The other s vertices are known as gates. Each gate is labeled with \wedge, \vee and \neg . Gates labeled with \wedge or \vee have two in-neighbors. Gates labeled with \neg have one in-neighbor. We will allow parallel edges (and so for example an AND gate can have both its in-neighbors be the same vertex).
3. Exactly m of the gates are also labeled with the m labels $Y[0], \dots, Y[m-1]$ (in addition to their label \wedge, \vee, \neg). These are known as outputs.

Let $f : \{0, 1\}^n \rightarrow \{0, 1\}^m$. We say that the circuit C computes f if for every $x \in \{0, 1\}^n$, $C(x) = f(x)$.

Briefly speaking, the computation of a Boolean circuit is layer by layer.

Definition 2 (AON-CIRC Programming language) An AON-CIRC program is a string of lines of the form $\text{foo} = \text{AND}(\text{bar}, \text{blah})$, $\text{foo} = \text{OR}(\text{bar}, \text{blah})$ and $\text{foo} = \text{NOT}(\text{bar})$ where foo , bar and blah are variable names. Variables of the form $X[i]$ are known as input variables, and variables of the form $Y[j]$ are known as output variables. In every line, the variables on the righthand side of the assignment operators must either be input variables or variables that have already been assigned a value before.

Theorem 1 (Equivalence of circuits and straight-line programs) Let $f : \{0, 1\}^n \rightarrow \{0, 1\}^m$ and $s \geq m$ be some number. Then f is computable by a Boolean circuit of s gates if and only if f is computable by an AON-CIRC program of s lines.

B. The NAND function

Theorem 2 NAND computes AND, OR, NOT. We can compute AND, OR, and NOT by composing only the NAND function.

$$\begin{aligned}\text{NOT}(a) &= \text{NAND}(a, a) \\ \text{AND}(a, b) &= \text{NOT}(\text{NAND}(a, b)) \\ \text{OR}(a, b) &= \text{NOT}(\text{NOT}(\text{NOT}(a), \text{NOT}(b)))\end{aligned}$$

Theorem 3 NAND is a universal operation. For every Boolean circuit C of s gates, there exists a NAND circuit C' of at most $3s$ gates that computes the same function as C .

Just like we did for Boolean circuits, we can define a programming-language analog of NAND circuits. It is even simpler than the AON-CIRC language since we only have a single operation.

Theorem 4 (Equivalence of NAND circuits and straight-line programs) Let $f : \{0, 1\}^n \rightarrow \{0, 1\}^m$ and $s \geq m$ be some number. Then f is computable by a NAND circuit of s gates if and only if f is computable by an NAND-CIRC program of s lines.