

2019 International Conference on Computer-Aided Design

The Westin Westminster | Westminster, CO | November 4-7, 2019

Third Place: 2019 CAD Contest

Timing Division Multiplexing Technique System-level FPGA Routing with

Xiaopeng Zhang, Dan Zheng, Chak-Wa Pui, Prof. Evangeline F. Y. Young





♦IEEE SEEDA Sig Sig Necession with



Contest Co-Chair

Contest Co-Chair

Mark Boly Long